# 5

**Distributed computing** 



# **Basic StarPU/MPI Example**

Straightforward port of existing MPI code



# **Basic StarPU/MPI Example**

#### Straightforward port of existing MPI code

Asynchronous requests

```
for (loop = 0; loop < NLOOPS; loop++) {
   if (!(loop == 0 && rank == 0))
        starpu_mpi_irecv_submit(data_handle, prev_rank, ...);

starpu_mpi_task_insert(&inc_cl, STARPU_RW, data_handle, 0);

if (!(loop == NLOOPS-1 && rank == size-1))
        starpu_mpi_isend_submit(data_handle, next_rank, ...);

starpu_task_wait_for_all();</pre>
```



# **Basic StarPU/MPI Example**

#### Straightforward port of existing MPI code

- Asynchronous requests
- Requests are DAG elements
  - Task may depend on Irecv completion
  - **Isend** may depend on task completion

```
for (loop = 0; loop < NLOOPS; loop++) {
   if (!(loop == 0 && rank == 0))
        starpu_mpi_irecv_submit(data_handle, prev_rank, ...);

starpu_mpi_task_insert(&inc_cl, STARPU_RW, data_handle, 0);

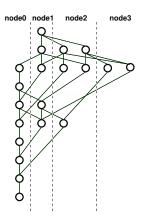
if (!(loop == NLOOPS-1 && rank == size-1))
        starpu_mpi_isend_submit(data_handle, next_rank, ...);

starpu_task_wait_for_all();</pre>
```



Sequential Task Flow Paradigm on Clusters

Each node unrolls the sequential task flow



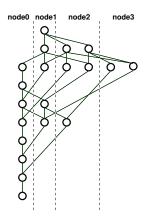


## Sequential Task Flow Paradigm on Clusters

#### Each node unrolls the sequential task flow

## **Data**↔**Node Mapping**

- Provided by the application
- Can be altered dynamically





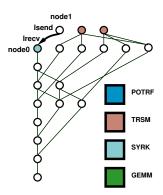
Sequential Task Flow Paradigm on Clusters

## Each node unrolls the sequential task flow

#### Inter-node dependence management

- Inferred from the task graph edges
- Automatic Isend and Irecv calls







## Sequential Task Flow Paradigm on Clusters

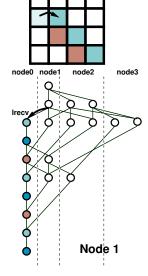
### Each node unrolls the sequential task flow

# **Task**↔**Node Mapping**

- Inferred from data location:
  - Tasks move to data they modify
- No global scheduling
- No synchronizations

#### Optimization

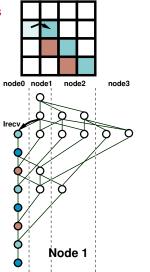
Local DAG pruning

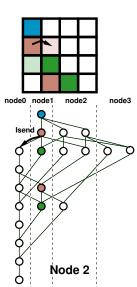




# Nodes infer required transfers

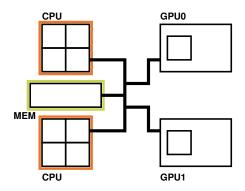
Task dependencies





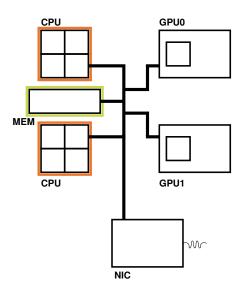


- Task dependencies
- Automatic MPI calls
  - Isend
  - Irecv
- Tasks wait for MPI requests



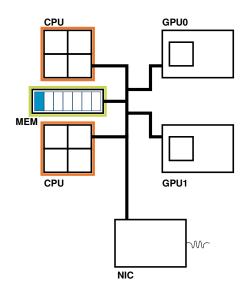


- Task dependencies
- Automatic MPI calls
  - Isend
  - Irecv
- Tasks wait for MPI requests



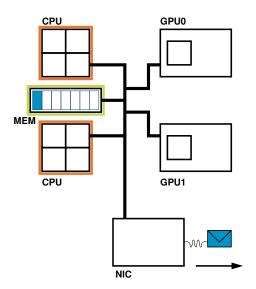


- Task dependencies
- Automatic MPI calls
  - Isend
  - Irecv
- Tasks wait for MPI requests



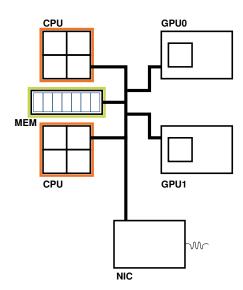


- Task dependencies
- Automatic MPI calls
  - Isend
  - Irecv
- Tasks wait for MPI requests



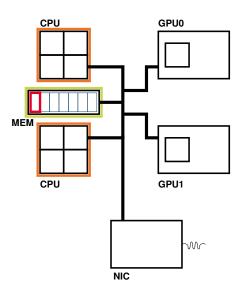


- Task dependencies
- Automatic MPI calls
  - Isend
  - Irecv
- Tasks wait for MPI requests



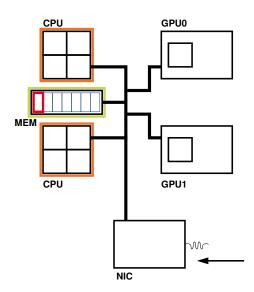


- Task dependencies
- Automatic MPI calls
  - Isend
  - Irecv
- Tasks wait for MPI requests



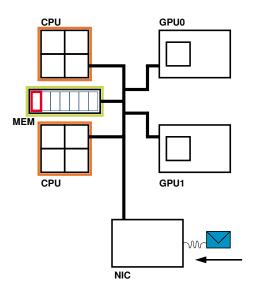


- Task dependencies
- Automatic MPI calls
  - Isend
  - Irecv
- Tasks wait for MPI requests



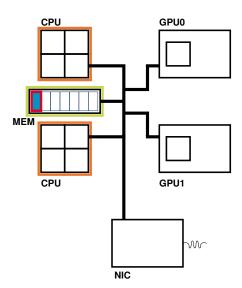


- Task dependencies
- Automatic MPI calls
  - Isend
  - Irecv
- Tasks wait for MPI requests





- Task dependencies
- Automatic MPI calls
  - Isend
  - Irecv
- Tasks wait for MPI requests





same code



#### Almost same code

MPI communicator



#### Almost same code

- MPI communicator
- Mapping function

```
int getnode(int i, int j) { return((i%p)*q + j%q); }
for (j = 0; j < N; j++) {
  POTRF (RW, A[j][j], MPI_COMM_WORLD, getnode(j,j));
  for (i = j+1; i < N; i++)
    TRSM (RW, A[i][j], R,A[j][j], MPI_COMM_WORLD, getnode(i,j));
  for (i = j+1; i < N; i++) {
    SYRK (RW, A[i][i], R,A[i][j], MPI_COMM_WORLD, getnode(i,i));
    for (k = j+1; k < i; k++)
      GEMM (RW, A[i][k],
        R,A[i][j], R,A[k][j], MPI_COMM_WORLD, getnode(i,k));
task wait for all():
```



#### Almost same code

- MPI communicator
- Mapping function

```
int getnode(int i, int j) { return((i%p)*q + j%q); }
set_rank(A, getnode);
for (j = 0; j < N; j++) {
  POTRF (RW, A[j][j], MPI_COMM_WORLD);
  for (i = j+1; i < N; i++)
    TRSM (RW, A[i][j], R,A[j][j], MPI_COMM_WORLD);
  for (i = j+1; i < N; i++) {
    SYRK (RW, A[i][i], R,A[i][j], MPI_COMM_WORLD);
    for (k = j+1; k < i; k++)
      GEMM (RW, A[i][k],
        R,A[i][j], R,A[k][j], MPI_COMM_WORLD);
task wait for all():
```



## Specific init/shutdown:

```
starpu_mpi_init_conf(&argv, 1, MPL_COMM_WORLD, NULL);
starpu_mpi_comm_rank(MPL_COMM_WORLD, &rank);
starpu_mpi_comm_size(MPL_COMM_WORLD, &size);
starpu_mpi_shutdown();
```



## (Optional) distribution function:



#### Data registration:

```
1 unsigned matrix[X][Y];
2 starpu_data_handle_t data handles[X][Y];
  for (x = 0; x < X; x++)
      for (v = 0; v < Y; v++) {
           int mpi_rank = my_distrib(x, y, size);
5
           /* StarPU registration step */
8
           if (mpi_rank == my_rank)
               /* Owning data */
               starpu_variable_data_register(&data_handles[x][y],
10
                    STARPU_MAIN_RAM, (uintptr_t)&(matrix[x][y]), sizeof(unsigned));
11
           else if (my rank = my distrib(x+1, y, size)
12
                  my rank = my distrib(x-1, y, size)
13
                  my_rank = my_distrib(x, y+1, size)
14
15
                  my rank = my distrib(x, y-1, size)
               /* Don't own this index, but will need it for computations */
16
               starpu_variable_data_register(&data_handles[x][y],
17
                                          -1, (uintptr_t)NULL, sizeof(unsigned));
18
           else
19
               /* Do not allocate anything for this */
20
               data handles [x][v] = NULL:
21
22
           /* StarPU-MPI registration step */
23
           if (data_handles[x][y]) {
24
               starpu mpi data register(data handles[x][y], x*X+y, mpi rank);
25
26
27
```

#### Task submission:



#### Task submission with pruning:

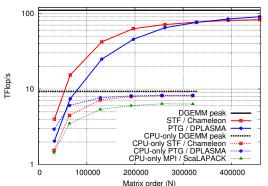
```
for(loop=0; loop<niter; loop++)
      for (x = 1; x < X-1; x++)
2
           for (y = 1; y < Y-1; y++)
3
               if (mv distrib(x.v.size) = mv rank
                 my_distrib(x-1,y,size) = my_rank
                 my distrib (x+1,y, size) = my rank
                 mv distrib(x.v-1.size) = mv rank
8
                 my_distrib(x,y+1,size) = my_rank)
                   starpu_mpi_task_insert(MPI_COMM_WORLD, &stencil5_cl,
                                       STARPU RW, data handles[x][y],
10
                                       STARPU R, data_handles[x-1][y],
11
                                       STARPU_R, data_handles[x+1][y],
12
                                       STARPU R, data handles [x][y-1],
13
14
                                       STARPU_R, data_handles[x][y+1],
15
                                       0);
16 starpu_task_wait_for_all();
```



# **Distributed Scalability Study Results**

#### Chameleon linear algebra library (Inria Team HiePACS)

Heterogeneous cluster: 1152 CPU cores+288 GPUs



**IEEE TPDS Paper:** 

DOI: 10.1109/TPDS.2017.2766064 — https://hal.inria.fr/hal-01618526

