

## **Experiment 06: Intermediate Code Generator**

**Learning Objective:** Student should be able to Apply Intermediate Code Generator using 3-Address code.

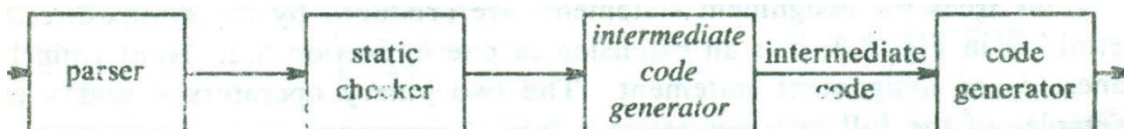
**Tools:** Jdk1.8, Turbo C/C++, Python, Notepad++

### **Theory:**

#### **Intermediate Code Generation:**

In the analysis-synthesis model of a compiler, the front end translates a source program into an intermediate representation from which the back end generates target code. Details of the target language are confined to the backend, as far as possible. Although a source program can be translated directly into the target language, some benefits of using a machine-independent intermediate form are:

1. Retargeting is facilitated; a compiler for a different machine can be created by attaching a back end for the new machine to an existing front end.
2. A machine-independent code optimizer can be applied to the intermediate representation.



Position of intermediate code generator.

#### **(Intermediate Languages) Intermediate Code Representation:**

- a) Syntax trees or DAG
- b) postfix notation
- c) Three address code

### Three-Address Code:

Three-address code is a sequence of statements of the general form

$$x := y \text{ op } z$$

where x, y, and z are names, constants, or compiler generated temporaries; op stands for any operator, such as fixed-or floating-point arithmetic operator, or a logical operator on Boolean-valued data.

A source language expression like  $x+y+z$  might be translated into a sequence

$t1 := y * z$

$t2 := x + t1$

where t1 and t2 are compiler-generated temporary names.

Example:  $a := b * -c + b * -c$

$a := b * -c + b * -c$



$t1 := -c$

$t2 := b * t1$

$t3 := -c$

$t4 := b * t3$

$t5 := t2 + t4$

$a := t5$

### Types of Three-Address Statements:

Three-address statements are akin to assembly code. Statements can have symbolic labels and there are statements for flow of control. A symbolic label represents the index of a three-address statement in the array holding intermediate code.

Actual indices can be substituted for the labels either by making a separate pass, or by using "back patching,"

Here are the common three-address statements:

I. Assignment statements of the form  $x = y \text{ op } Z$ , where op is a binary arithmetic or logical operation.

2. Assignment instructions of the form  $x := op\ y$ , where  $op$  is a unary operation. Essential unary operations include unary minus, logical negation, shift operators, and conversion operators that, for example, convert a fixed-point number to a floating-point number.
3. Copy statements of the form  $x := y$  where the value of  $y$  is assigned to  $x$ .
4. The unconditional jump `goto L`. The three-address statement with label  $L$  is the next to be executed.
5. Conditional jumps such as `if x relop y goto L`. This instruction applies a relational operator ( $<$ ,  $=$ ,  $>=$ , etc.) to  $x$  and  $y$ , and executes the statement with label  $L$  next if  $x$  stands in relation  $relop$  to  $y$ . If not, the three-address statement following `if x relop y goto L` is executed next, as in the usual sequence.
6. `param x` and `call p, n` for procedure calls and `return y`, where  $y$  representing a returned value is optional. Their typical use is as the sequence of three-address statements  
`param X1`  
`param X2`  
`param Xn`  
`call p,n`  
generated as part of a call of the procedure  $p$  ( $X1, X2, \dots, Xn$ ) The integer  $n$  indicating the number of actual parameters in "`call p, n`" is not redundant because calls can be nested.
7. Indexed assignments of the form  $X := y[i]$  and  $x[i] := y$ . The first of these sets  $x$  to the value in the location  $i$  memory units beyond location  $y$ . The statement  $x[i] := y$  sets the contents of the location  $i$  units beyond  $x$  to the value of  $y$ . In both these instructions,  $x$ ,  $y$ , and  $i$  refer to data objects. Address and pointer assignments of the form  $x := \&y$ ,  $x := *y$ , and  $*x := y$ .

### Implementations of Three-Address Statements

A three-address statement' is an abstract form of intermediate code. In a compiler, these statements can be implemented as records with fields for the operator and the operands. Three such representations are quadruples, triples, and indirect triples.

#### Quadruples:

- a) A quadruple is a record structure with four fields:  
`op`, `arg 1`, `arg 2`, and `result`.

- b) The op field contains an internal code for the operator.
- c) The three-address statement  $x := y \text{ op } z$  is represented by placing y in arg1, Z in arg 2, and x in result.
- d) Statements with unary operators like  $x := -y$  or  $x := y$  do not use arg 2. Operators like param use neither arg 2 nor result.
- e) Conditional and unconditional jumps put the target label in result.
- f) The quadruples are for the assignment  $a := b * -c + b * -c$ .  
They are obtained from the three-address code in Fig. (a).
- g) The contents of fieldsarg 1, arg 2, and result are normally pointers to the symbol-table entries for the names represented by these fields. If so, temporary names must be entered into the symbol table as they are created.

### Triples:

- a) To avoid entering temporary names into the symbol table, refer to a temporary value by the position of the statement that computes it.
- b) Three-address statements can be represented by records with only three fields: op, arg1 and arg2, as in Fig.(b).
- c) The fieldsarg1 and arg2, for the arguments of op, are pointer to the symbol table. Since three fields are used, this intermediate code format is known as triples.

	op	arg 1	arg 2	result
(0)	uminus	c		t <sub>1</sub>
(1)	*	b	t <sub>1</sub>	t <sub>2</sub>
(2)	uminus	c		t <sub>3</sub>
(3)	*	b	t <sub>3</sub>	t <sub>4</sub>
(4)	+	t <sub>2</sub>	t <sub>4</sub>	t <sub>5</sub>
(5)	:=	t <sub>5</sub>		a

(a) Quadruples

	op	arg 1	arg 2
(0)	uminus	c	
(1)	*	b	(0)
(2)	uminus	c	
(3)	*	b	(2)
(4)	+	(1)	(3)
(5)	assign	a	(4)

(b) Triples

**Quadruple and triple representations of three-address statements.**

A ternary operation like  $x[i] := y$  requires two entries in the triple structure, as shown in Fig.(a), while  $x := y[i]$  is naturally represented as two operations in Fig. (b).

	<i>op</i>	<i>arg 1</i>	<i>arg 2</i>
(0)	[ ]=	<b>x</b>	<b>i</b>
(1)	assign	(0)	<b>y</b>

(a)  $x[i] := y$

	<i>op</i>	<i>arg 1</i>	<i>arg 2</i>
(0)	=[ ]	<b>y</b>	<b>i</b>
(1)	assign	<b>x</b>	(0)

(b)  $x := y[i]$

### More triple representations.

#### Indirect Triples:

Another implementation of three-address code that has been considered is that of listing pointers to triples, rather than listing the triples themselves. This implementation is naturally called indirect triples.

For example, let us use an array statement to list pointers to triples in the desired order.

	<i>statement</i>		<i>op</i>	<i>arg 1</i>	<i>arg 2</i>
(0)	(14)	(14)	uminus	<b>c</b>	
(1)	(15)	(15)	*	<b>b</b>	(14)
(2)	(16)	(16)	uminus	<b>c</b>	
(3)	(17)	(17)	*	<b>b</b>	(16)
(4)	(18)	(18)	+	(15)	(17)
(5)	(19)	(19)	assign	<b>a</b>	(18)

### Indirect triples representation of three-address statements

#### Input:

$a := b * -c + b * -c.$

## Output:

	<i>op</i>	<i>arg 1</i>	<i>arg 2</i>	<i>result</i>
(0)	uminus	c		t <sub>1</sub>
(1)	*	b	t <sub>1</sub>	t <sub>2</sub>
(2)	uminus	c		t <sub>3</sub>
(3)	*	b	t <sub>3</sub>	t <sub>4</sub>
(4)	+	t <sub>2</sub>	t <sub>4</sub>	t <sub>5</sub>
(5)	:=	t <sub>5</sub>		a

(a) Quadruples

	<i>op</i>	<i>arg 1</i>	<i>arg 2</i>
(0)	uminus	c	
(1)	*	b	(0)
(2)	uminus	c	
(3)	*	b	(2)
(4)	+	(1)	(3)
(5)	assign	a	(4)

(b) Triples

**Application:** Intermediate code can be easily produced to the target code.

## Result and Discussion:

**Learning Outcomes:** The student should have the ability to

- LO1 **Define** the role of Intermediate Code Generator in Compiler design.
- LO2: **Describe** the various ways to implement Intermediate Code Generator.
- LO3: **Specify** the formats of 3 Address Code.
- LO4: **Illustrate** the working of Intermediate Code Generator using 3-Address code

**Course Outcomes:** Upon completion of the course students will be able to Evaluate the synthesis phase to produce object code optimized in terms of high execution speed and less memory usage.

**Conclusion:**

For Faculty Use:

Correction Parameters	Formative Assessment [40%]	Timely completion of Practical [ 40%]	Attendance / Learning Attitude [20%]	
Marks Obtained				



**Design:**

**Input:**

**Output:**