இந்திய தகவல் தொழில்நுட்பக் கழகம் திருச்சிராப்பள்ளி
भारतीय सूचना प्रौद्योगिकी संस्थान, तिरुचिरापल्ली
INDIAN INSTITUTE OF INFORMATION TECHNOLOGY TIRUCHIRAPPALLI
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NIT CAMPUS, TIRUCHIRAPPALLI -620015
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Laboratory Examinations (Even Semester) Schedule (Tentative)

- This timetable is tentative.

- The final timetable and further instructions will be displayed on the website soon.

- The students are to verify the examination schedule, if there is any concern,

  - First year students have to contact Dr. Kamakshi Koppole

  - Second and Third year students have to contact their concern HOD.

Director

Sd/

Department of Humanities and Sciences

Tentative Timetable For End-Semester Final Laboratory Examinations

(July 2020)

Course name	Date	Time	Quiz/Viva Schedule
Physics II Lab (PHIR13)	20-07-2020	11.00 am to 12.00 noon	Quiz (ECE & CSE all)
		1.00 to 5.00 pm	Viva (CSE: 01-16)
	21-07-2020	10.00 am to 1.00 pm	Viva (CSE: 17-24 & ECE: 01-04)
		2.00 pm to 5.00 pm	Viva (ECE 05 - 22)
Chemistry II Lab (CHIR13)	22-07-2020	11.00 am to 12.00 noon	Quiz (ECE & CSE all)
		1.00 to 5.00 pm	Viva (CSE: 01-16)
	23-07-2020	10.00 am to 1.00 pm	Viva (CSE: 17-24 & ECE: 01-04)
		2.00 pm to 5.00 pm	Viva (ECE 05 - 22)
Engineering Practice (PRIR11)	24-07-2020	11.00 am to 12.00 noon	Quiz (ECE & CSE all)
		1.00 to 5.00 pm	Viva (CSE: 01-16)
	25-07-2020	10.00 am to 1.00 pm	Viva (CSE: 17-24 & ECE: 01-04)
		2.00 pm to 5.00 pm	Viva (ECE 05 - 22)

Department of Computer Science and Engineering

Tentative Timetable For End-Semester Final Laboratory Examinations

(July 2020)

II YEAR IV Semester

Total number of students: 26 (CSE18U001 – 010,012-021, 023-026, 028-029)

Course Name/Code	Date	Time	Quiz/Viva Schedule
Algorithms Laboratory (CSLR23)	27-07-2020 (Monday)	09:00 am to 10:00 am	Quiz (for all 26 students)
		10:00 am to 01:00 pm	Viva (CSE18U001 – 010, 012-014)
		02:00 pm to 05:00 pm	Viva (CSE18U015 – 021, 023-026, 028-029)
Operating Systems Laboratory (CSLR24)	31-07-2020 (Friday)	09:00 am to 10:00 am	Quiz (for all 26 students)
		10:00 am to 01:00 pm	Viva (CSE18U001 – 010, 012-014)
		02:00 pm to 05:00 pm	Viva (CSE18U015 – 021, 023-026, 028-029)

III YEAR VI Semester

Total number of students: 26(CSE17U001 – 008, CSE17U010-0026, CSE15U038)

Course Name/Code	Date	Time	Quiz/Viva Schedule
Mobile Application Development (CSLR33)	27-07-2020 (Monday)	09:00 am to 10:00 am	Quiz (for all 26 students)
		10:00 am to 01:00 pm	Viva (CSE17U001 – 008, CSE17U010- 014)
		02:00 pm to 05:00 pm	Viva (CSE17U015– CSE17U026, CSE15U038)
Microprocessors and Microcontrollers Laboratory (CSLR34)	29-07-2020 (Wednesday)	09:00 am to 10:00 am	Quiz (for all 26 students)
		10:00 am to 01:00 pm	Viva (CSE17U001 – 008, CSE17U010- 014)
		02:00 pm to 05:00 pm	Viva (CSE17U015– CSE17U026, CSE15U038)

Department of Electronics and Communication Engineering

Tentative Timetable For End-Semester Final Laboratory Examinations

(July 2020)

II YEAR IV Semester

Total number of students: 28 (ECE18U001-028)

Course Name/Code	Date	Time	Quiz/Viva Schedule
Electronic Circuits Laboratory (ECLR41)	26/07/2020 Sunday	09:00 am to 10:00 am	Quiz (for all 28 students)
		10:00 am to 01:00 pm	Viva (ECE18U001 - 014)
		02:00 pm to 05:00 pm	Viva (ECE18U015 - 028)
Microprocessor and Microcontroller Laboratory (ECLR42)	02/08/2020 Sunday	09:00 am to 10:00 am	Quiz (for all 28 students)
		10:00 am to 01:00 pm	Viva (ECE18U001 - 014)
		02:00 pm to 05:00 pm	Viva (ECE18U015 - 028)

III YEAR VI Semester

Total number of students: 21 (ECE18U001-007, 009-017, 019-023)

Course Name/Code	Date	Time	Quiz/Viva Schedule
Communication Engineering Laboratory (ECLR61)	26/7/2020 Sunday	09:00 am to 10:00 am	Quiz (for all 21 students)
		10:00 am to 01:00 pm	Viva (ECE18U001 - 012)
		02:00 pm to 05:00 pm	Viva (ECE18U013 - 023)
VLSI and Embedded System Design Laboratory (ECLR62)	02/8/2020 Sunday	09:00 am to 10:00 am	Quiz (for all 21 students)
		10:00 am to 01:00 pm	Viva (ECE18U001 - 012)
		02:00 pm to 05:00 pm	Viva (ECE18U013 - 023)