Amogh Madanayakanahalli Kumar

in: linkedin.com/in/amoghmk : amoghmk.github.io

EDUCATION

• University of California, Santa Cruz (Current)

o Master of Science in Computer Science and Engineering

(Sep 2022 - Jun 2024 (Expected))

• PES University, Bengaluru, India

o Bachelor of Technology in Electronics and Communication Engineering - 9.37/10

(Aug 2015 - May 2019)

o Minors in Computer Science and Engineering - 9.6/10

(Aug 2016 - May 2018)

Relevant Coursework: Computer Networks, Operating Systems, Database Management Systems, Data Structures and Algorithms. Planned Coursework: Distributed Computing, Storage Systems.

TECHNICAL SKILLS

• Programming Languages: C, Python, Java, Perl, Golang, C++, Shell Script

• Database Management Systems: MongoDB, MySQL, SQLite, PostgreSQL

• Tools/Frameworks: Git, Wireshark, Docker, Flask, REST APIs

• Cloud Platforms: Amazon Web Services (AWS), Azure

Agile Methodologies, Object Oriented Programming

WORK EXPERIENCE (3+ years)

• Citrix R&D, Bengaluru, India

Software Development Engineer - Level 2

(Mar 2021 - Sep 2022)

- * Developed a feature to monitor health of internet links and consequently backhaul critical application traffic via stable MPLS links instead of direct breakout to internet upon detection of significant degradation (default of 1s latency or 10% loss).
- * Devised an infrastructure to automatically push SaaS application signatures for traffic classification onto Citrix SD-WAN (Software-Defined Wide Area Network) appliances from Citrix SD-WAN Orchestrator service on cloud. This enabled updating of the signatures on the appliances within 24 hours in opposition to requiring a new release.
- * Research on integration of Citrix SD-WAN with Citrix ITM (Intelligent Traffic Management) for intelligent wan-link path selection for optimal delivery of internet-bound SaaS application traffic.
- * Live environment analysis, debugging and resolution of 20+ critical customer issues.

o Software Development Engineer - Level 1

(Jul 2019 - Feb 2021)

- $* \ Classification \ and \ Optimization \ of \ Citrix \ Managed \ Desktops \ traffic \ and \ Web \ proxy \ traffic \ passing \ via \ Citrix \ Gateway \ Service.$
- * Added enhancement of IPv6 support for DNS parsing and proxying features as well as first packet classification in Citrix SD-WAN for routing of Office365 and other SaaS application traffic.
- * Lead a team of 4 to design and develop an infrastructure to create on-demand Citrix SD-WAN networks in private cloud (OpenStack) and configure the same via SD-WAN Orchestrator on the basis of a digitally visualized network topology.

o Software Engineering Intern

(Jan 2019 - Jun 2019)

* Designed and created Behavioral Driven Development (BDD) validation framework for Citrix SD-WAN Orchestrator. The framework provided a solid base for the product's Automation and helped to offload the majority of the manual testing.

ACADEMIC PROJECTS

- Implemented RSA public key cryptographic algorithm using Verilog and developed an ASIC (Application-Specific Integrated Circuit) design so that the hardware could be ideally added to the end network devices to ensure safe and secure data sharing capabilities while decreasing the processing power required by the devices.
- Researched and proposed secure storage of biometric data using Blockchain for distributed registration and access systems. As a proof of concept, using python, implemented the Kerberos authentication process for validating transactions from registration centers to enter data into the transaction pool and data retrieval from blockchain ledgers to access points.

PROFESSIONAL DEVELOPMENT CERTIFICATIONS

• AWS Partner: AWS Cloud Practitioner Essentials.

(Aug 2022)

• Citrix certified security track - Level 5 - Purple Belt in "Secure Cloud Development".

(Mar 2019 - Aug 2021)

• Citrix Learning Center course on "Docker with Kubernetes".

(Dec 2019) (Jun 2017 - Aug 2017)

• Coursera certified course on "Machine Learning" offered by Stanford University.

PATENTS and ACHIEVEMENTS

• US 17/841,135 - An optimal way to store multiple copies of files in local PoPs.

(Filed - Jun 15, 2022)

· Received CNR Rao Merit Scholarship from PES University for academic excellence in all 7 semesters it was offered.



(Established under Karnataka Act No.16 of 2013)

100 Feet Ring Road, BSK III Stage, Bangalore-560085

TRANSCRIPT

Name	AMOGH M K	Duration of the Program	4 Years
SRN	01FB15EEC027	Medium of Instruction	English
Transcript Number	UT/2015/ECE/0027	Date of Issue	August 27, 2021
Year of Admission	2015	Year of Completion	May 2019
Program of Study	B.Tech Electronics and Communication Engineering		



Program of Study B.Tech Electronics and Communication Engir					
SI. No	Course Code	Course Tile	Credits	Grade	Month & Year of Completion
1	UE15CS101	Introduction to Computing Using Python		s	Dec 2015
2	UE15CS102	Introduction to Computing Using Python Laboratory	1	s	Dec 2015
3	UE15EE101	Basic Electrical Engineering	4	S	Dec 2015
4	UE15HS102	Environmental Studies	-	AP	Dec 2015
5	UE15MA101	Engineering Mathematics - I	4	Α	Dec 2015
6	UE15ME104	Mechanical Engineering Sciences	4	А	Dec 2015
7	UE15ME106	Computer Aided Engineering Graphics	2	Α	Dec 2015
8	UE15PH101	Engineering Physics	4	S	Dec 2015
9	UE15PH102	Physics Laboratory	1	Α	Dec 2015
10	UE15CS151	Problem Solving with C	4	Α	May 2016
11	UE15CS152	Problem Solving with C Laboratory	1	S	May 2016
12	UE15CV101	Engineering Mechanics	4	S	May 2016
13	UE15CY101	Engineering Chemistry	4	Α	May 2016
14	UE15CY102	Chemistry Laboratory	1	А	May 2016
15	UE15EC101	Basic Electronics Engineering	4	S	May 2016
16	UE15HS101	Constitution of India and Professional Ethics	-	AP	May 2016
17	UE15JE101	Unified Engineering (JEDI)	2	A	May 2016
18	UE15MA151	Engineering Mathematics - II	4	А	May 2016
19	UE15EC201	Network Analysis & Synthesis	4	S	Dec 2016
20	UE15EC202	Electronic Devices and Circuits	4	A	Dec 2016
21	UE15EC203	Logic Design	4	S	Dec 2016
22	UE15EC204	Electromagnetic Field & Transmission Lines	4	A	Dec 2016
23	UE15EC205	Network Analysis and Synthesis	1	S	Dec 2016
24	UE15EC206	Laboratory Electronic Devices and Circuits Laboratory	1	S	Dec 2016
25	UE15EC207	Logic Design Laboratory	1	S	Dec 2016
26	UE15MA201	Engineering Mathematics -III	4	S	Dec 2016
27	UE15EC251	Linear Integrated Circuits	4	A	May 2017
28	UE15EC252	Signals and Systems	4	S	May 2017
29	UE15EC253	Microcontrollers	4	В	May 2017
30	UE15EC254	Probability and Random Processes	4	A	May 2017
31	UE15EC255	Linear Integrated Circuits Laboratory	1	A	May 2017
32	UE15EC256	Signals and Systems Laboratory	1	А	May 2017
33	UE15EC257	Microcontrollers Laboratory	1	A	May 2017
34	UE15MA251	Linear Algebra & Its Applications	4	S	May 2017
35	UE15EC301	Communication Engineering	4	В	Dec 2017
36	UE15EC302	Digital Signal Processing	4	S	Dec 2017

SI. No	Course Code	Course Tile	Credits	Grade	Month & Year of Completion
38	UE15EC304	Communication Engineering Laboratory	1	S	Dec 2017
39	UE15EC305	Digital Signal Processing Laboratory	1	S	Dec 2017
40	UE15EC306	VLSI Design Laboratory	1	S	Dec 2017
41	UE15EC316	Pattern Recognition and Classification	4	Α	Dec 2017
42	UE15EC326	Artificial Neural Networks	4	Α	Dec 2017
43	UE15EC336	Digital Image Processing	4	Α	May 2018
44	UE15EC341	Network Security	4	S	May 2018
45	UE15EC351	Computer Networks	4	S	May 2018
46	UE15EC352	Microwave Engineering	4	В	May 2018
47	UE15EC353	Digital System Design using HDL	4	S	May 2018
48	UE15EC354	Computer Networks Laboratory	1	S	May 2018
49	UE15EC355	Microwave Engineering Laboratory	1	S	May 2018
50	UE15EC356	Digital System Design using HDL Laboratory	1	S	May 2018
51	UE15EC401	Fundamentals of Antenna	4	Α	Dec 2018
52	UE15EC402	Embedded Systems Design	4	Α	Dec 2018
53	UE15EC403	Wireless Communication	4	Α	Dec 2018
54	UE15EC413	Network Security II	4	S	Dec 2018
55	UE15EC426	Speech Processing	4	S	Dec 2018
56	UE15EC491	Industry / International Internship	6	S	May 2019
57	UE15EC492	Project Work	8	А	May 2019
58	UE15IE442	Graphology	2	A	May 2019

Credits	176	CGPA	9.37
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Grade: S - 10 (Outstanding), A - 9 (Excellent), B - 8 (Very Good), C - 7 (Good), D - 6 (Fair), E - 5 (Satisfactory), AP - Audit Pass Class: >= 9.50 - First Class With Honors, >= 7.75 & 9.50 - First Class With Distinction, >= 6.75 & 7.75 - First Class, >= 5.75 & 6.75 - Second Class, >= 5.00 & 5.75 - Pass Class

UE15EC303

VLSI Design

VERIFIED BY

В

Dec 2017

CONTROLLER OF EXAMINATIONS
Controller of Examinations

PES University
100 Feet Ring Road, BSK III Stage

Bangalore-580 085



(Established under Karnataka Act No. 16 of 2013) 100 Feet Ring Road, BSK III Stage, Bengaluru - 560 085

CERTIFICATE FOR MINOR COURSE



It is certified that AMOGH M K bearing SRN: 01FB15EEC027 has fulfilled the requirement of **Minors in Computer Science and Engineering** by successfully passing the courses listed below:

SI. No	Course Code	Course Tile	Credits	Grade Awarded	Month & Year of Completion
1	UE14CS160	Data Structures	4	S	Dec 2016
2	UE14CS162	Data Structures Laboratory	1	S	Dec 2016
3	UE14CS180	DataBase Management Systems	4	Α	May 2017
4	UE14CS182	Data Base Management Systems Laboratory	1	S	May 2017
5	UE14CS240	Introduction to Operating Systems	4	А	Dec 2017
6	UE14CS242	Introduction to Operating Systems Laboratory	1	S	Dec 2017
7	UE14CS280	Design and Analysis of Algorithms	4	S	May 2018
8	UE14CS282	Design and Analysis of Algorithms Laboratory	1	S	May 2018

This certificate is issued under the seal and signature of the undersigned.

Date: August 31,2019

Place: Bengaluru



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Controller of Examinations

Control of Elementations

PES University

100 Feet Ring Road, BSK III Stage

Bandolpro, F50 095





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DEGREE CERTIFICATE

The Board of Governors of PES University on the recommendation of the Academic Council hereby confers the Degree of

Bachelor of Technology

(in Electronics and Communication Engineering)

on

AMOGH M K (SRN:01FB15EEC027)



at the Convocation held on this THIRTY FIRST DAY OF AUGUST, TWO THOUSAND NINETEEN at Bengaluru, for successfully completing the requirements for the degree in **First Class With Distinction** by securing a CGPA of **9.37** on 10-point scale.

ಮೇಲ್ಕಂಡ ವಿವರಗಳುಳ್ಳವರನ್ನು ಅಧ್ಯಕ್ಷ ಮಂಡಳಿಯು, ಶಿಕ್ಷಣ ಪರಿಷತ್ತಿನ ಶಿಫಾರಿಸಿನ ಮೇರೆಗೆ, ಮೇಲೆ ನಮೂದಿಸಿರುವ ಪಿ. ಇ. ಎಸ್. ವಿಶ್ವವಿದ್ಯಾಲಯದ ಪದವಿಗೆ ಅಗತ್ಯವಾದ ಅರ್ಹತೆಗಳನ್ನುಳ್ಳವರೆಂದು ಅಂಗೀಕರಿಸಿದೆ.

ಕುಲಪತಿ Vice-Chancellor

Vice - Chancellor PES University 100 Feet Ring Road, BSK III Stage Bengaluru-560085,



ಕುಲಾಧಿಪತಿ Chancellor Chancellor PES University 100 Feet Ring Road, BSK III Stage Bengaluru - 560 085.

GRADE PO	OINT SCALE
MEDIUM OF INSTR	UCTION IS ENGLISH
Letter Grade	Grade Point
'S'	10
'A'	09
'B'	08
'С'	07
'D'	06
٠E'	05
'F'	00
'A P'	Audit Pass
'A F'	Audit Fail
'W'	Withdraw

SGPA = Σ (Course Credits X Grade Points) / Σ Course Credit for all courses with letter grades including "F" in current semester

CGPA = Σ (Course Credits X Grade Points) / Σ Course Credit for all courses with letter grades including "F" upto the end of current semester

PERCENTAGE EQUIVALENCE OF GRADE POINTS

4		
CGPA	Equivalent Percentage	Class / Division
≥ 5.0 - < 5.75	≥ 42.5 & < 50	Pass Class
≥ 5.75 - < 6.75	≥ 50 & < 60	Second Class
≥6.75 - < 7.75	≥ 60 & < 70	First Class
≥7.75 - < 9.50	≥ 70 & < 87.5	First Class with Distinction
≥ 9.50	≥ 87.5	First Class with Honors

