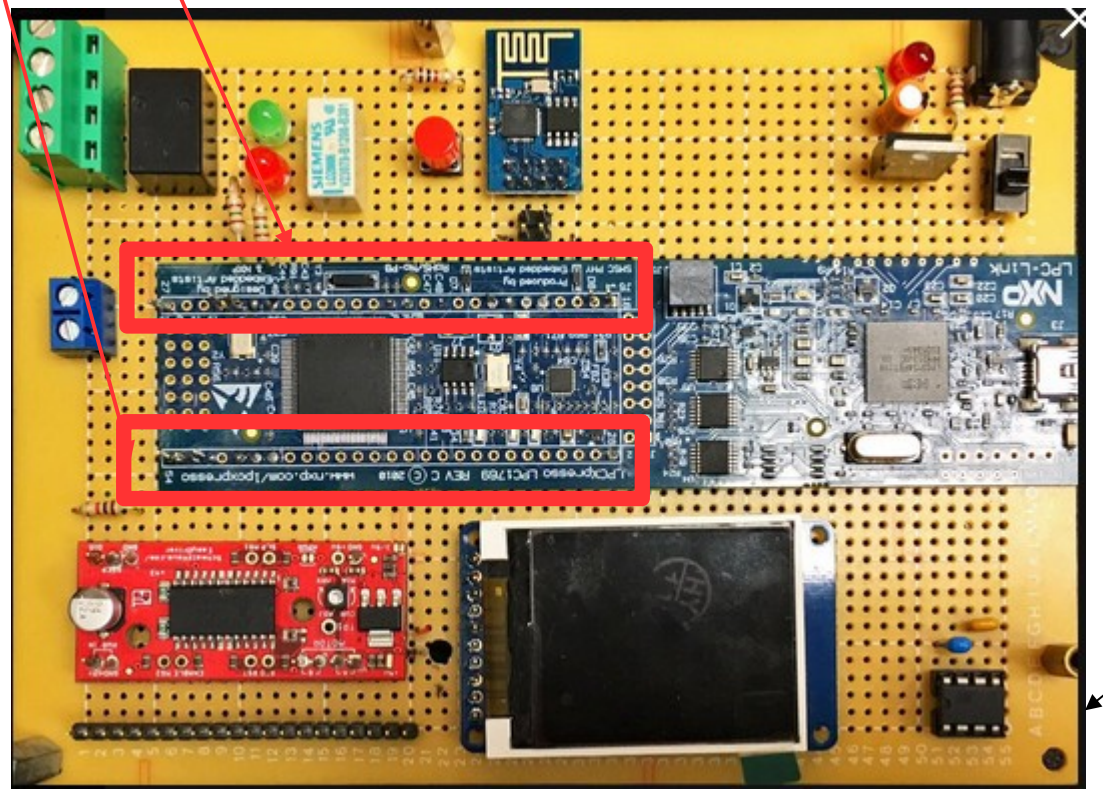
J2/(or 6 for rev. B) Connector for CPU SPI Pins

J2 (Rev. D) or J6 (Rev.B)
connector with CPU GPIO Pin

P0.9	MOSI1	P0.9	J2-5
P0.8	MISO1	P0.8	J2-6
P0.7	SCK1	P0.7	J2-7
P0.6	SSEL1	P0.6	J2-8
P0.0	TXD3/SDA1	P0.0	J2-9
P0.1	RXD3/SCL1	P0.1	J2-10
P0.18	MOSI0	P0.18	J2-11
P0.17	MISO0	P0.17	J2-12
P0.15	TXD1/SCK0	P0.15	J2-13
P0.16	RXD1/SSEL0	P0.16	J2-14

From schematics



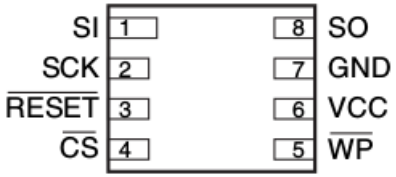
S

Note: Use one SPI port for FLASH and one SPI for Colour LCD

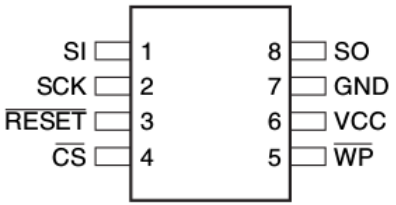
Dimension: 16 x 11 mm or 6.25 x 4.50 inch

Harry Li, Ph.D.

FLASH with SPI Interface

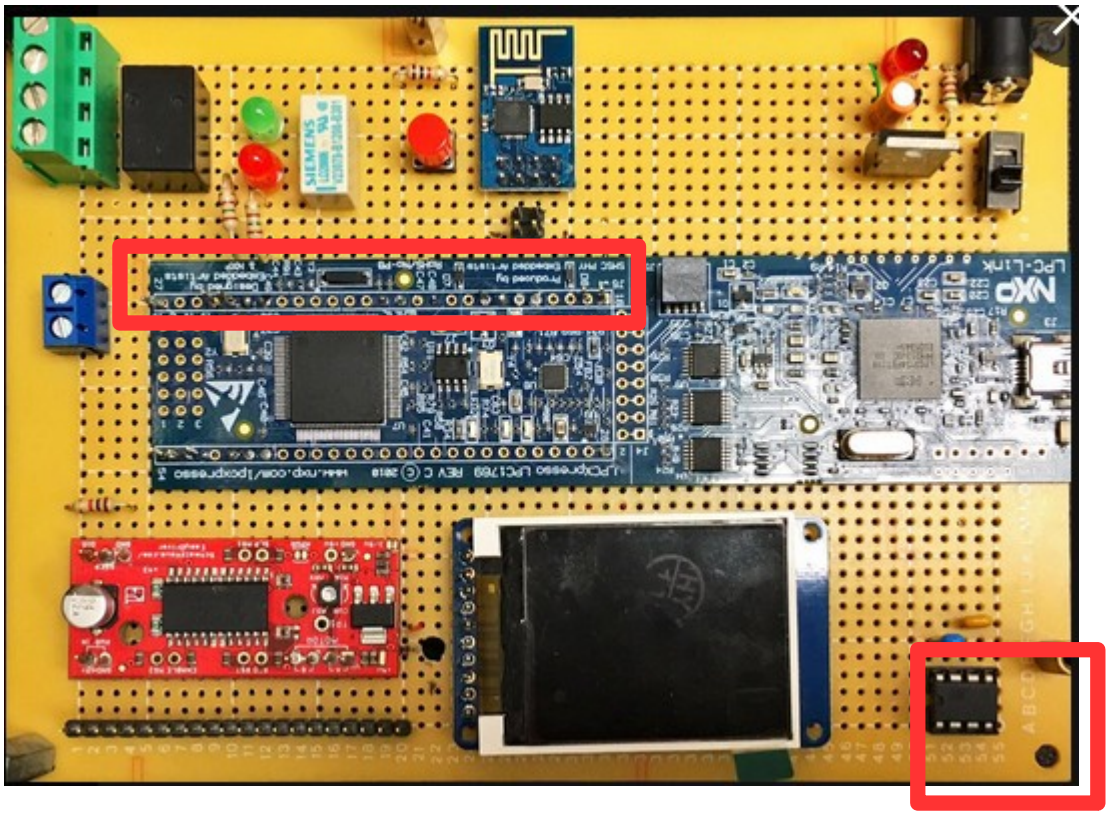


MLF package



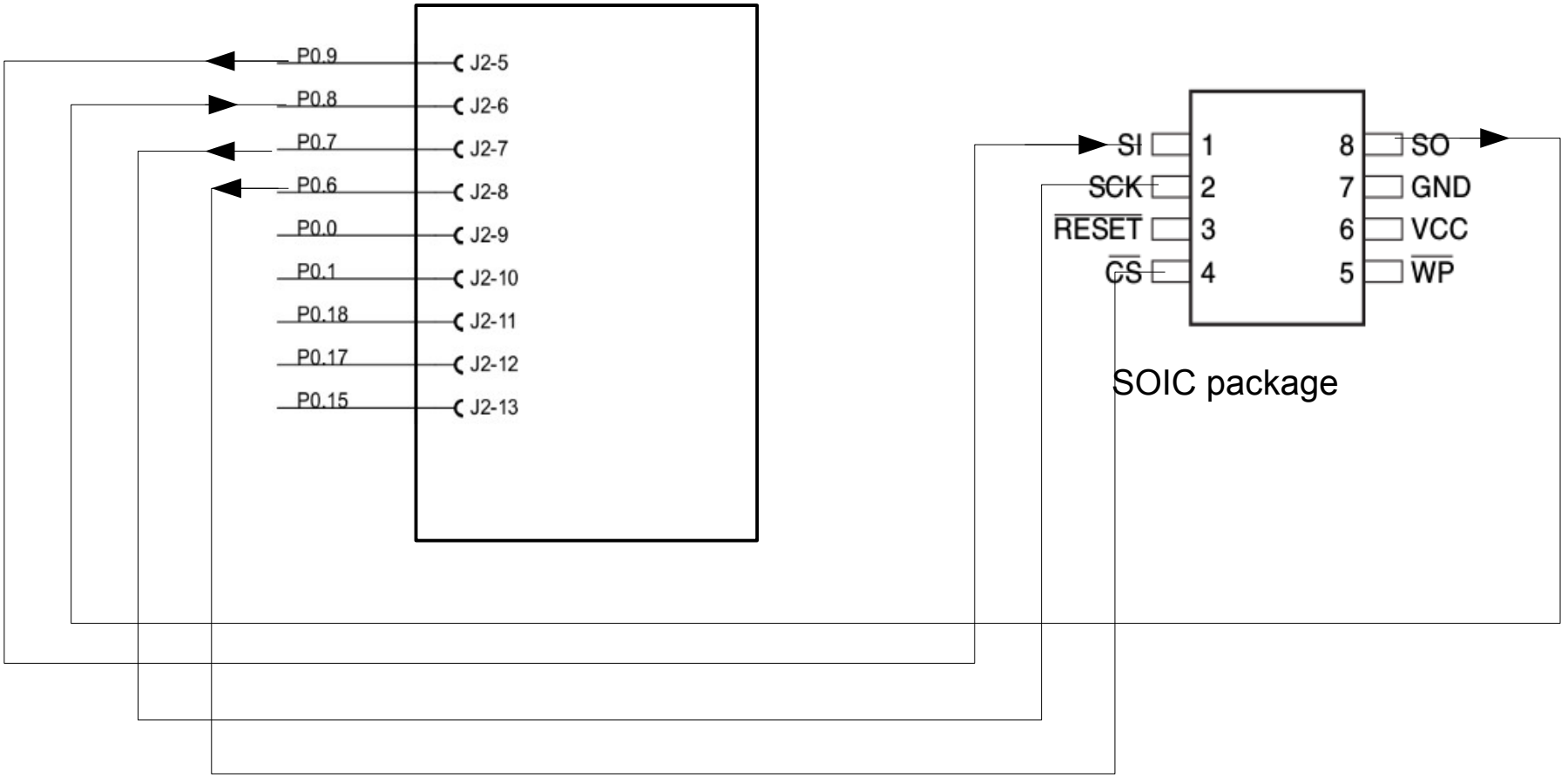
SOIC package

Note: 1. read data sheet to find the right figure match up with your IC package, so you can identify the pin assignment; (I have used PDIP package, in that case, you will have to find pin assignment from relevant data sheet. 2. The above 2 package are SMD type, need an adapter or fine tip soldering tool, you can buy soldering fine tip from Frey's Electronics for example. It costs about $8 ~ $10.



SPI I/F Flash

Hardware Design for SPI FLASH Interface



CPU		FLASH	
P0.9	MOSI1	SI	pin 1
P0.8	MISO1	SO	pin 8
P0.7	SCK1	SCK	pin 2
P0.6	SSEL1	nCS	pin 4

Build connectivity table to clearly indicate which pin connects to each

CPU SPI Controller and SPRs