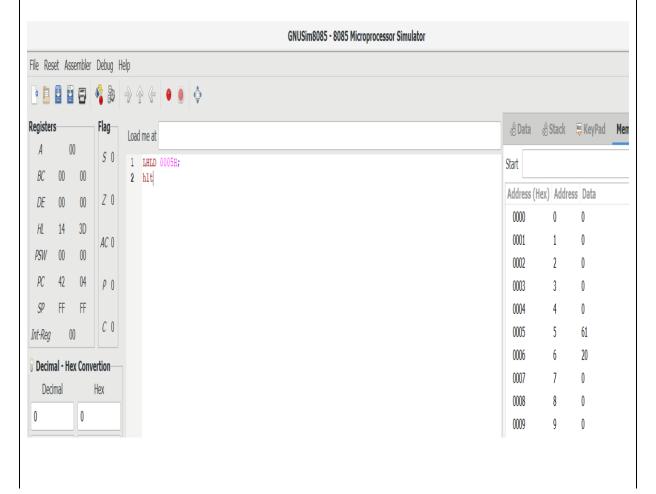
ASSIGNMENT 7 DCO -LAB

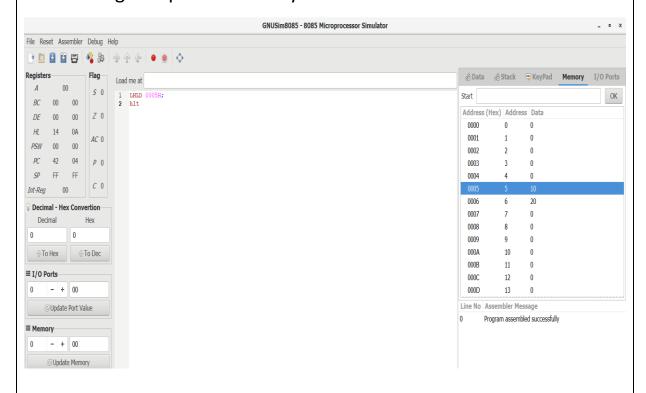
Amulya Kashyap 2020CA007

Q1. Load the content of memory location 0005 and 0006 into the register pair HL directly.

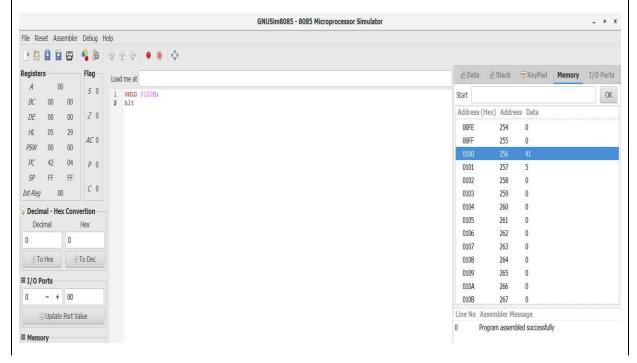
To store content of a memory location to HL pair directly we can use **LHLD** command as:

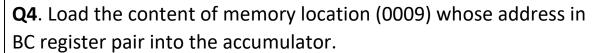


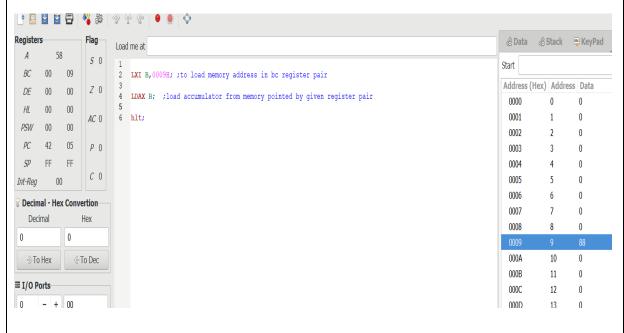
Q2. Store 10 at memory location 0005 and 20 at next memory location. Then Load the content of memory location 0005 and 0006 into the register pair HL directly.



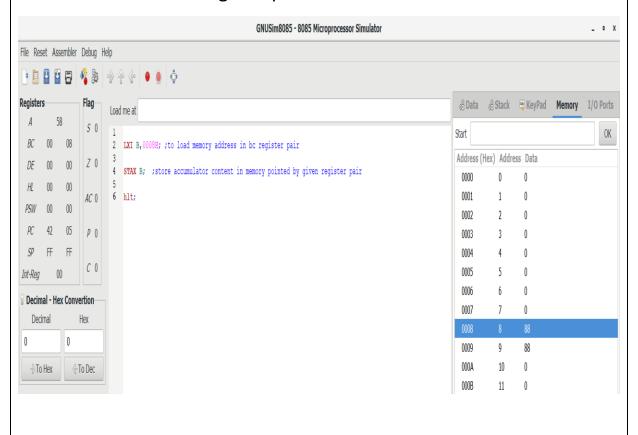
Q3. Store the content of HL register pair directly to the two consecutive memory locations starting from 0100.







Q5. Store the content of accumulator to the memory location (0008) whose address in BC register pair.



Q6. Place 06 into DE register pair and 02 into HL register pair then swap the content of DE register pair with HL register pair.

To swap we can use **eXCHanGe (XCHG)** command as:

```
Registers
                      Flag
                               Load me at
            00
                       S 0
                                1
  ВС
         00
                00
                                    LXI D,0006H; ;to load memory address in DE register pair
                                    LXI H,0002H;
                       Z 0
         00
                02
  DE
                                   XCHG; ; EXCHANGE
         00
  HL
               06
                      AC 0
  PSW
         00
               00
                                7 hlt;
  PC
         42
                08
                       P 0
  SP
         FF
                FF
                       C 0
            00
Int-Reg

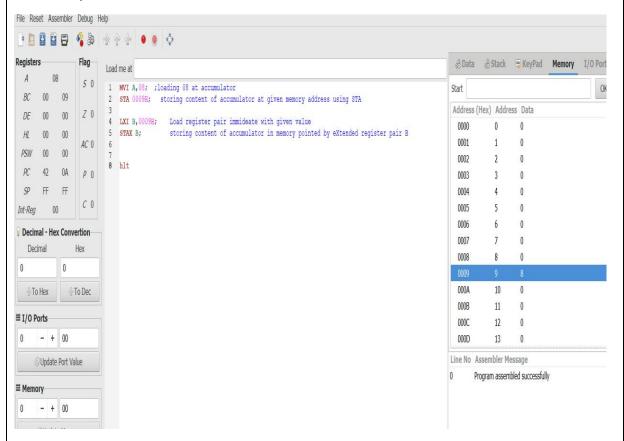
    □ Decimal - Hex Convertion
    □

  Decimal
```

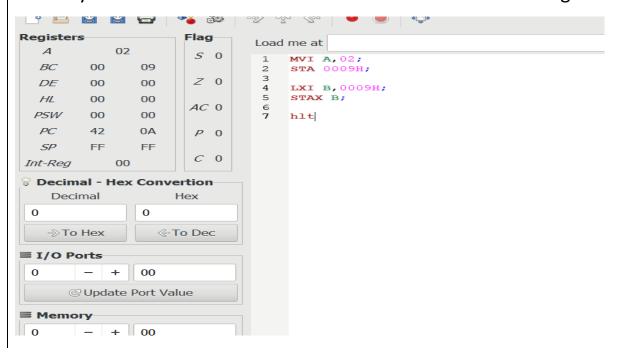
Q7. Place 10 in the register B, and then add the contents of register B with accumulator.

```
Registers
                    Flag-
                             Load me at
            0F
  A
                      S 0
                                  ; before execution accumulator is holding value 05
  ВС
        0A
               00
                              3 MVI B, 10; ;Storing 10 at register B
                      Z 0
  DE
        00
               00
                                 ADD B;
                              5
        00
  HL
               00
                     AC 0
                                 hlt;
 PSW
        00
               00
                                 ;after execution value at accumulator become 10+5=15 (0FH)
  PC
        42
               04
                      P 1
        FF
               FF
  SP
                      C 0
Int-Reg
            00
Decimal - Hex Convertion
```

Q8. Place 08 at memory location 0009, and the contents of this memory location to accumulator.



Q9. Place 02 at memory location 0009, and the contents of this memory location to accumulator. Also indicate the status of flag bits.



Q10. Place 09 in the register B, and then add the contents of register B with accumulator. Also indicate the status of flag bits.

