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RV COLLEGE OF ENGINEERING®
(An Autonomous Institution Affiliated to VTU)
V Semester B. E. Examinations April/May -2024
Information Science and Engineering
COMPLIER DESIGN

Time: 03 Hours**Maximum Marks: 100****Instructions to candidates:**

1. Answer all questions from Part A. Part A questions should be answered in first three pages of the answer book only.
2. Answer FIVE full questions from Part B. In Part B question number 2, 7 and 8 are compulsory. Answer any one full question from 3 and 4 & one full question from 5 and 6.

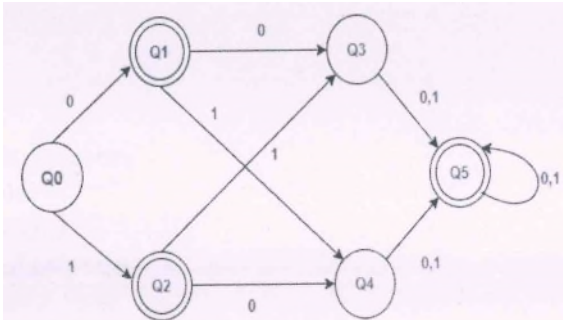
PART-A

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1	1.1	The number of tokens in the following C statement is: <i>printf("i = %d, &i = %x", i, &i);</i>	01	1	1																									
	1.2	Eliminate left recursion from the following grammar. <i>E</i> → <i>E</i> + <i>T</i> <i>T</i> <i>T</i> → <i>T</i> * <i>F</i> <i>F</i> <i>F</i> → (<i>E</i>) <i>id</i>	02	2	1																									
	1.3	List any four error recovery strategic that a parser can employ.	02	1	1																									
	1.4	File produced by <i>YACC</i> and used by <i>lex</i> is _____.	01	2	2																									
	1.5	Define Kernel and Non-kernel Items.	02	1	2																									
	1.6	Give an example for <i>LALR</i> parser generator.	01	1	1																									
	1.7	Write the Tripe representation for the given three address code: <i>T1</i> = <i>e</i> ↑ <i>f</i> <i>T2</i> = <i>b</i> × <i>c</i> <i>T3</i> = <i>T2</i> / <i>T1</i> <i>T4</i> = <i>b</i> × <i>a</i> <i>T5</i> = <i>a</i> + <i>T3</i> <i>T6</i> = <i>T5</i> + <i>T4</i>	02	2	2																									
	1.8	Given the following <i>ACTION/GOTO</i> table, perform a parse of “ <i>a</i> ”, assuming 0 as the start state. Show the contents of the stack, remaining input, and action performed at each step of the shift-reduce parse. <table border="1"><tr><td><i>State</i></td><td><i>A</i></td><td><i>\$</i></td><td><i>A</i></td><td><i>B</i></td></tr><tr><td>0</td><td><i>Shift 1</i></td><td><i>Reduce B</i> → <i>ε</i></td><td>2</td><td>3</td></tr><tr><td>1</td><td><i>Shift 3</i></td><td><i>Reduce A</i> → <i>a</i></td><td>0</td><td></td></tr><tr><td>2</td><td></td><td><i>Accept</i></td><td>3</td><td>0</td></tr><tr><td>3</td><td><i>Shift 1</i></td><td><i>Accept</i></td><td></td><td>1</td></tr></table>	<i>State</i>	<i>A</i>	<i>\$</i>	<i>A</i>	<i>B</i>	0	<i>Shift 1</i>	<i>Reduce B</i> → <i>ε</i>	2	3	1	<i>Shift 3</i>	<i>Reduce A</i> → <i>a</i>	0		2		<i>Accept</i>	3	0	3	<i>Shift 1</i>	<i>Accept</i>		1	02	2	1
	<i>State</i>	<i>A</i>	<i>\$</i>	<i>A</i>	<i>B</i>																									
	0	<i>Shift 1</i>	<i>Reduce B</i> → <i>ε</i>	2	3																									
	1	<i>Shift 3</i>	<i>Reduce A</i> → <i>a</i>	0																										
2		<i>Accept</i>	3	0																										
3	<i>Shift 1</i>	<i>Accept</i>		1																										
1.9	List the primary tasks of code generator.	02	1	1																										
1.10	List the performance metrics to be considered while designing garbage collection.	02	1	1																										
1.11	The quality of generated code is determined by _____ and _____.	01	2	2																										

1.12	<p>Consider the intermediate code given below.</p> <ul style="list-style-type: none"> i) $i = 1$ ii) $j = 1$ iii) $t1 = 5 * i$ iv) $t2 = t1 + j$ v) $t3 = 4 * t2$ vi) $t4 = t3$ vii) $a[t4] = -1$ viii) $j = j + 1$ ix) <i>if</i> $j \leq 5$ <i>goto</i> (iii) x) $i = i + 1$ xi) <i>if</i> $i < 5$ <i>goto</i> (ii) <p>Identify the leaders.</p>	02	2	2
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PART-B

2	<p>a) In addition to a compiler, several other programs may be required to create an executable target program. Describe all such programs required to convert a source program to an executable target program.</p> <p>b) Define distinguishable and indistinguishable states. Identify the same and thereby minimize the <i>DFA</i> (fig 2b) using table filling algorithm.</p>	06	2	1
	 <p style="text-align: center;">Fig 2b</p>	10	2	1
3	<p>a) Construct <i>LR</i> Parsing table for the below context free grammar.</p> <p>$S \rightarrow AA$ $A \rightarrow aA \mid b$</p> <p>b) Write an algorithm for <i>LR</i>-Parsing. With the help of schematic diagram discuss the features of <i>LR</i> parser.</p>	08	3	2
	OR	08	1	2
4	<p>Consider the grammar</p> <p>$S \rightarrow AS \mid \epsilon$ $A \rightarrow 0A1 \mid B$ $B \rightarrow B1 \mid 01$</p> <ul style="list-style-type: none"> i) Construct the Canonical <i>LR</i> sets of items for the grammar. ii) Show the construction of <i>LALR</i> parsing table. iii) Show the sequence of moves made by the parser for the string 001101. 	16	3	2
5	<p>a) Construct the <i>DAG</i> for the basic block</p> <p>$y = u * v$ $x = v + w$ $v = v + w$ $u = y/x$</p> <p style="text-align: center;">$x = x - u$</p>			

6	b	<p>Simplify the above three address code assuming</p> <ol style="list-style-type: none"> Only z is live on exit from the block. u and v are live on exit. <p>Define Synthesized Attribute and Inherited Attribute.</p> <p>The following grammar generates dotted binary numbers. Design an S-attributed <i>SDD</i> to compute $S.val$, the decimal number value of an input string. For example the translation of string 101.10 should be the decimal number 5.5. Construct an annotated parse tree to translate a string 1010.11 to its equivalent decimal number.</p> <p>$S \rightarrow L1.L2$ $L \rightarrow LB \mid B$ $B \rightarrow 0 \mid 1$</p>	06	4	3
		OR			
	a	<p>Define the following:</p> <ol style="list-style-type: none"> Three address codes Quadruples Indirect triples Annotated parse tree Dependency graph <p>Translate the arithmetic expression $a + b * c / e + f - b * d$ into</p> <ol style="list-style-type: none"> A Syntax tree Quadruples Indirect triples 	10		
	b	<p>Give the <i>SDD</i> to process a sample variable declaration D in 'C' language consisting of a basic type T followed by a list L of identifiers. T can be int or float. Construct the dependency graph for the input.</p> <p><i>Float a, b, c</i></p>	08	3	3
			08	3	3
7	a	<p>Derive activation record. Draw the stepwise (for each call of function fact()) stack activation record for the following code when $n = 3$.</p> <pre>int fact (int n) { if (n == 0) return 1; x = fact(n - 1); return x; }</pre>			
	b	Briefly explain the issues in the design of a code generation process.	10	2	4
			06	2	4
8	a	<p>Translate the given program into three-address statements. Assume that the array entries are numbers the requires 4 bytes.</p> <ol style="list-style-type: none"> List the rules for partitioning the three-address codes into basic blocks. Identify the basic blocks Construct the flow graph for the three- address code Identify the loops in the flow graph. 			

	<i>begin</i> <i>prod</i> := 0; <i>i</i> := 1; <i>do begin</i> <i>prod</i> := <i>prod</i> + <i>a</i> [<i>i</i>] * <i>b</i> [<i>i</i>]; <i>i</i> := <i>i</i> + 1; <i>end</i> <i>while i</i> ≤ 20 <i>end</i>			
b	Explain different loop optimization techniques.	10 06	3 2	4 3