|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **Phase** | **Port Pin** | **Sequence** | | | |
| **1** | **2** | **3** | **4** |
| **A** | **PB\_10** | H | L | L | H |
| **B** | **PB\_4** | H | H | L | L |
| **A’** | **PB\_5** | L | H | H | L |
| **B’** | **PB\_3** | L | L | H | H |

|  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Phase** | **Port Pin** | **Sequence** | | | | | | | |
| **1** | **2** | **3** | **4** | **5** | **6** | **7** | **8** |
| **A** | **PB\_10** | H | H | L | L | L | L | L | H |
| **B** | **PB\_4** | L | H | H | H | L | L | L | L |
| **A’** | **PB\_5** | L | L | L | H | H | H | L | L |
| **B’** | **PB\_3** | L | L | L | L | L | H | H | H |

|  |  |  |  |
| --- | --- | --- | --- |
| **State** | **Next State** | | **Output** |
| **DIR = 0** | **DIR = 1** | **(A B A’ B’)** |
| **S0** | S1 | S3 | (1 1 0 0) |
| **S1** | S2 | S0 | (0 1 1 0) |
| **S2** | S3 | S1 | (0 0 1 1) |
| **S3** | S0 | S2 | (1 0 0 1) |

|  |  |  |  |
| --- | --- | --- | --- |
| **State** | **Next State** | | **Output** |
| **DIR = 0** | **DIR = 1** | **(A B A’ B’)** |
| **S0** | S1 | S7 | (1 0 0 0) |
| **S1** | S2 | S0 | (1 1 0 0) |
| **S2** | S3 | S1 | (0 1 0 0) |
| **S3** | S4 | S2 | (0 1 1 0) |
| **S4** | S5 | S3 | (0 0 1 0) |
| **S5** | S6 | S4 | (0 0 1 1) |
| **S6** | S7 | S5 | (0 0 0 1) |
| **S7** | S0 | S6 | (1 0 0 1) |