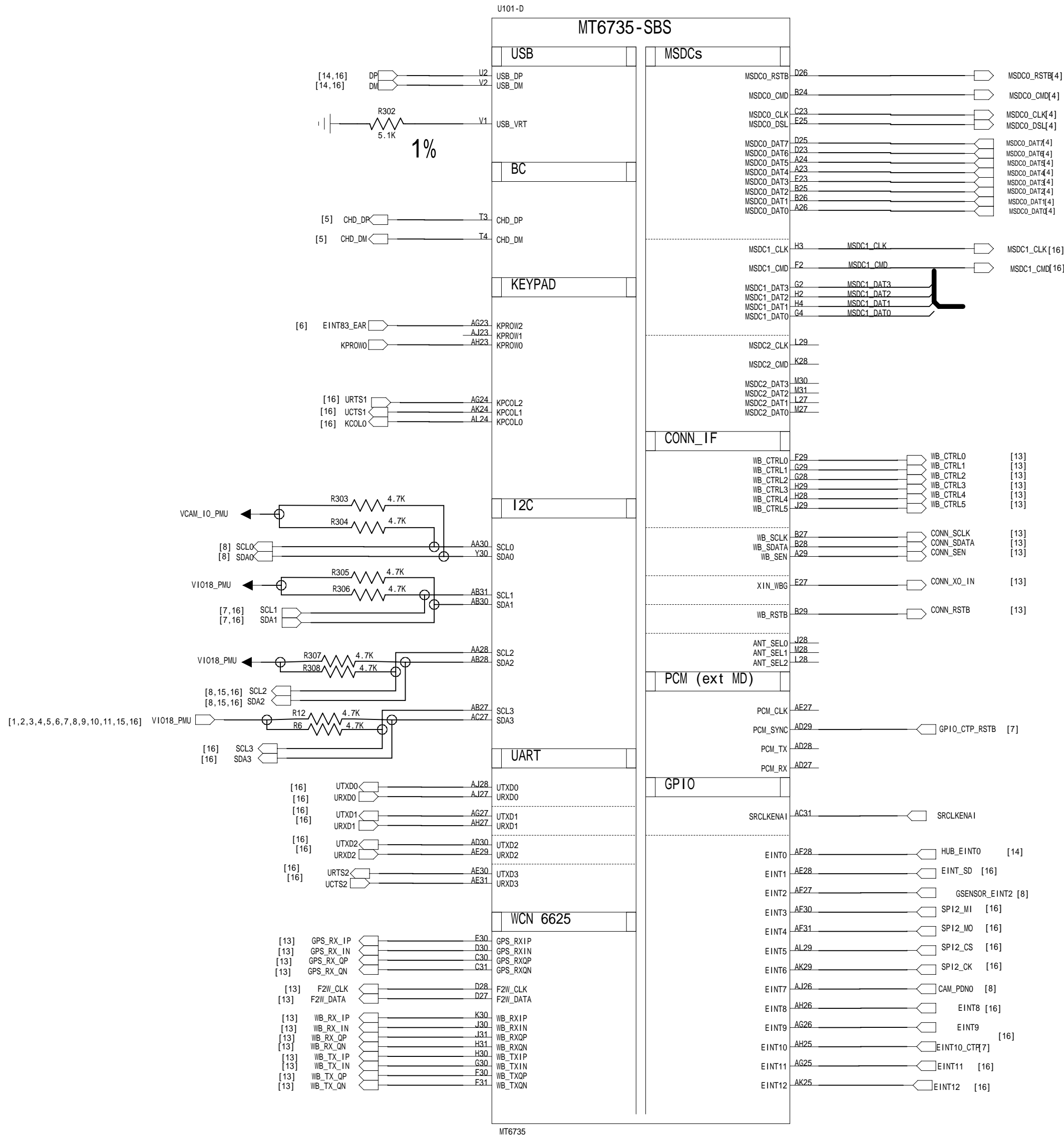
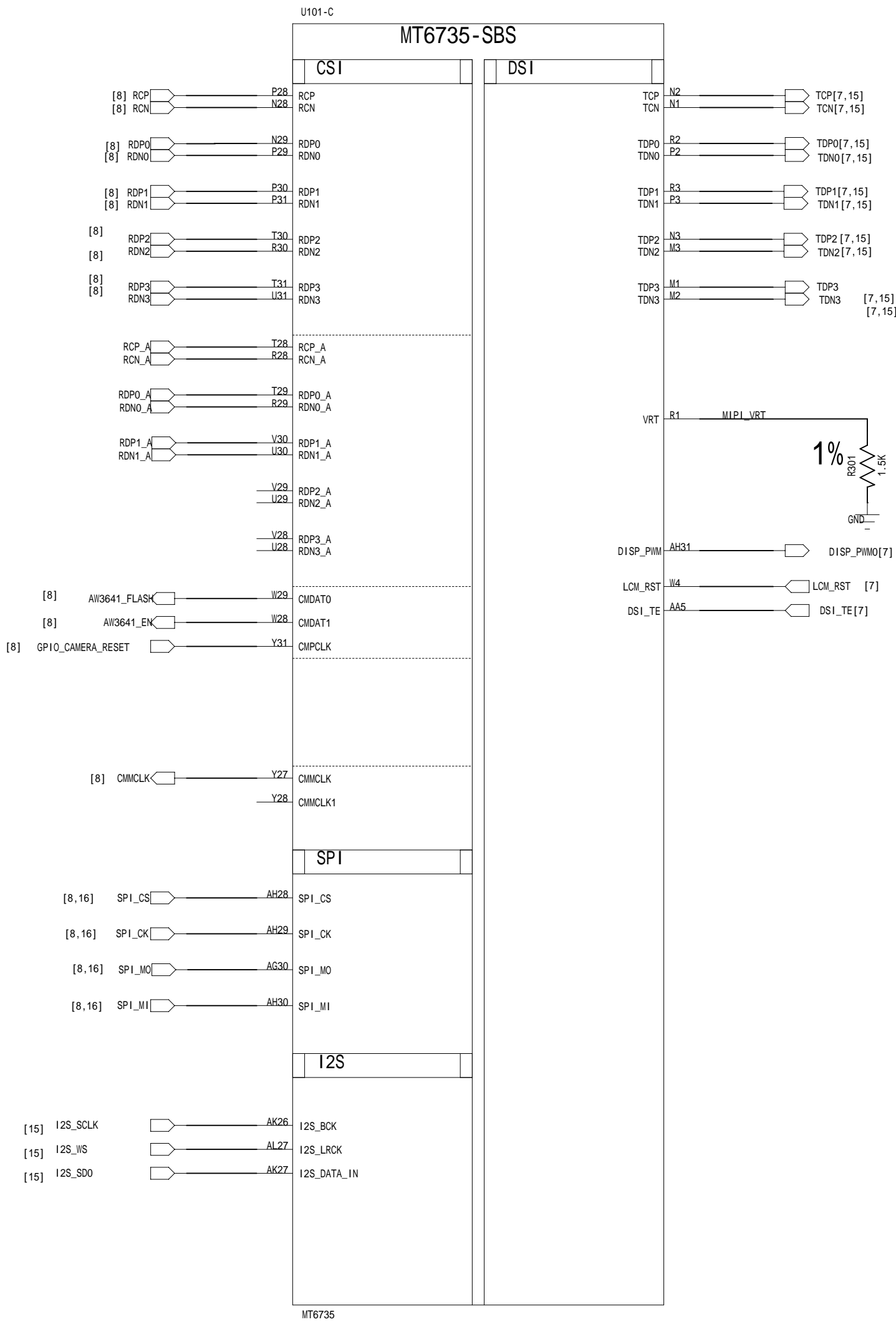


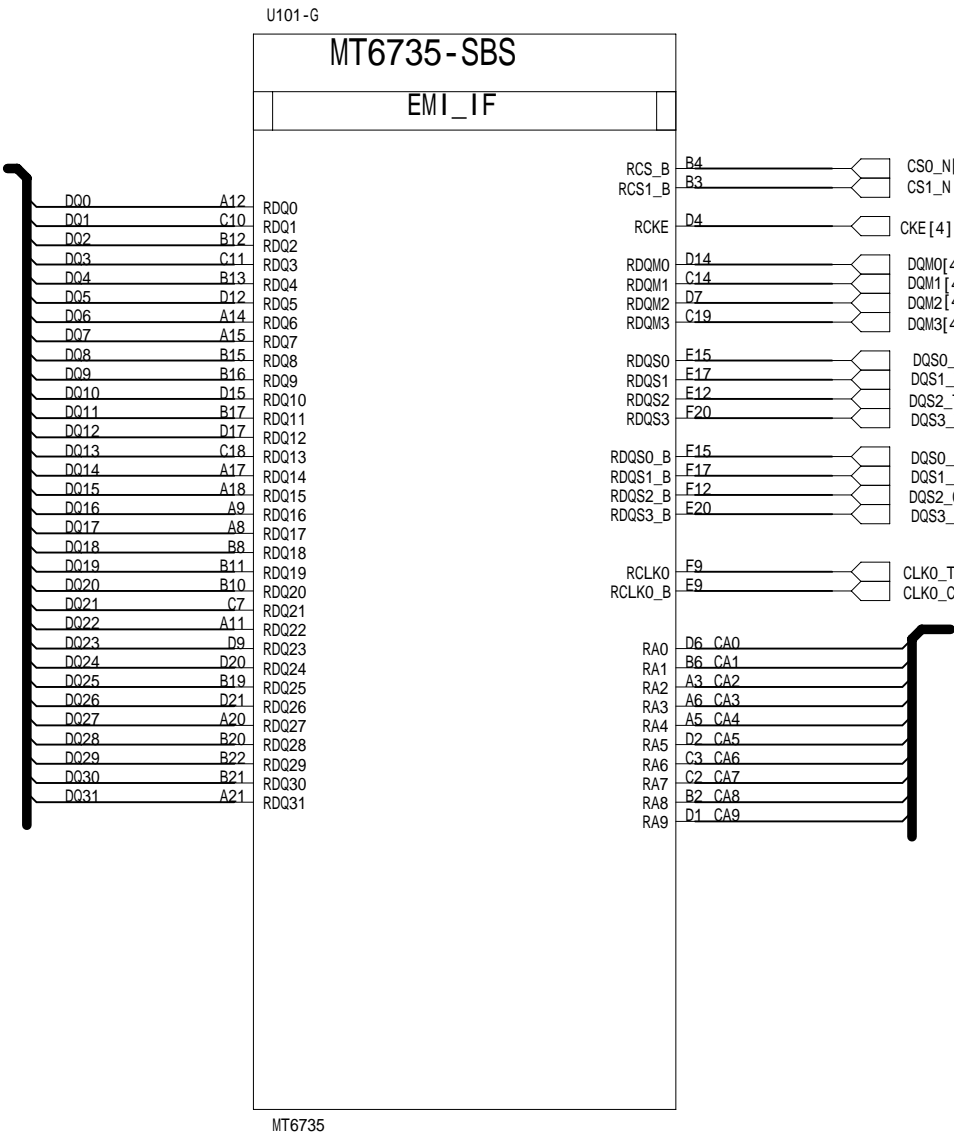
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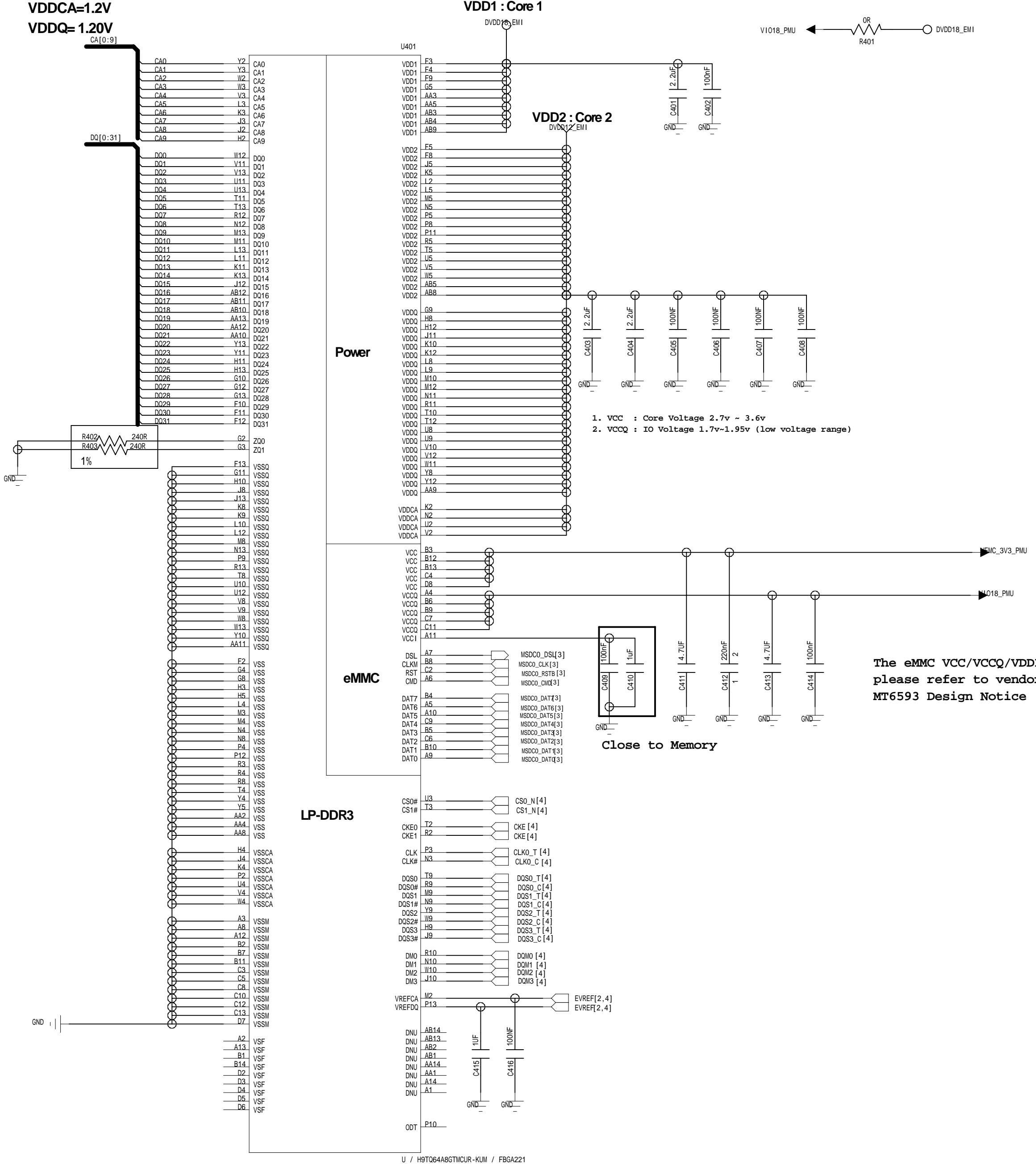
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eMMC+LPDDR3

8GB eMMC + 8Gb LPDDR3
Hynix/H9TQ64A8GTMCUR-KUM

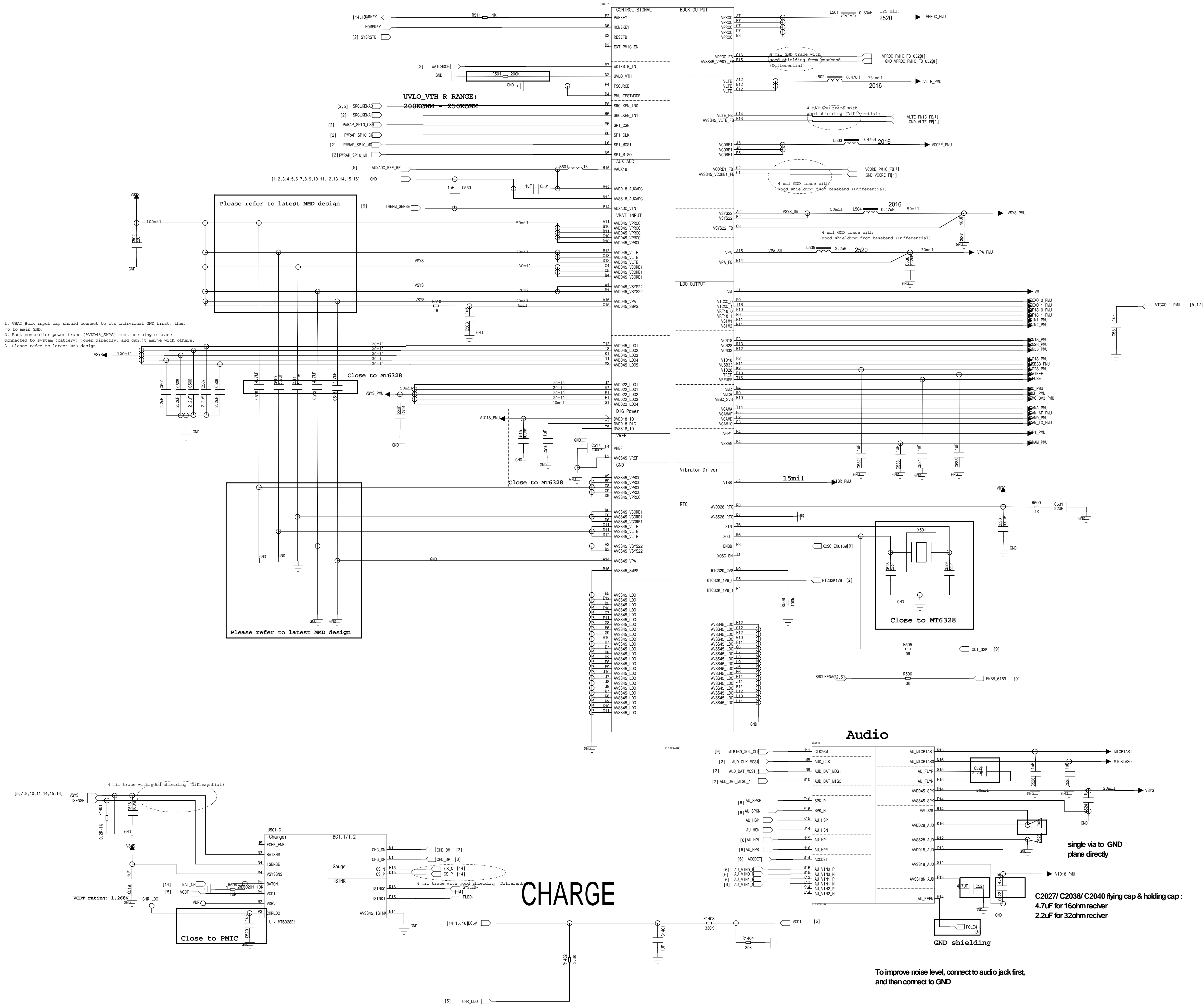


221 Ball, 0.5mm pitch
VDD1=1.8V
VDD2=1.20V
VDDCA=1.2V
VDDQ= 1.20V



The eMMC VCC/VCCQ/VDDI bypass cap recommend value, please refer to vendor datasheet or MT6593 Design Notice (eMMC power capacitor value reference)

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1. VBAT Buck input cap should connect to its individual GND first, then go to main GND.
2. Buck controller power trace (AVDD45_SHP5) must use single trace connected to system (battery) power directly, and can't merge with others.
3. Please refer to latest MMD design

Please refer to latest MMD design

Please refer to latest MMD design

CHARGE

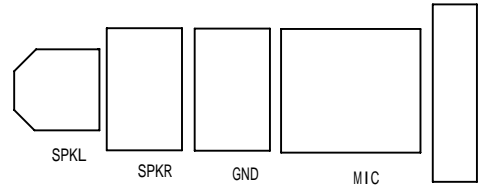
Audio

single via to GND plane directly

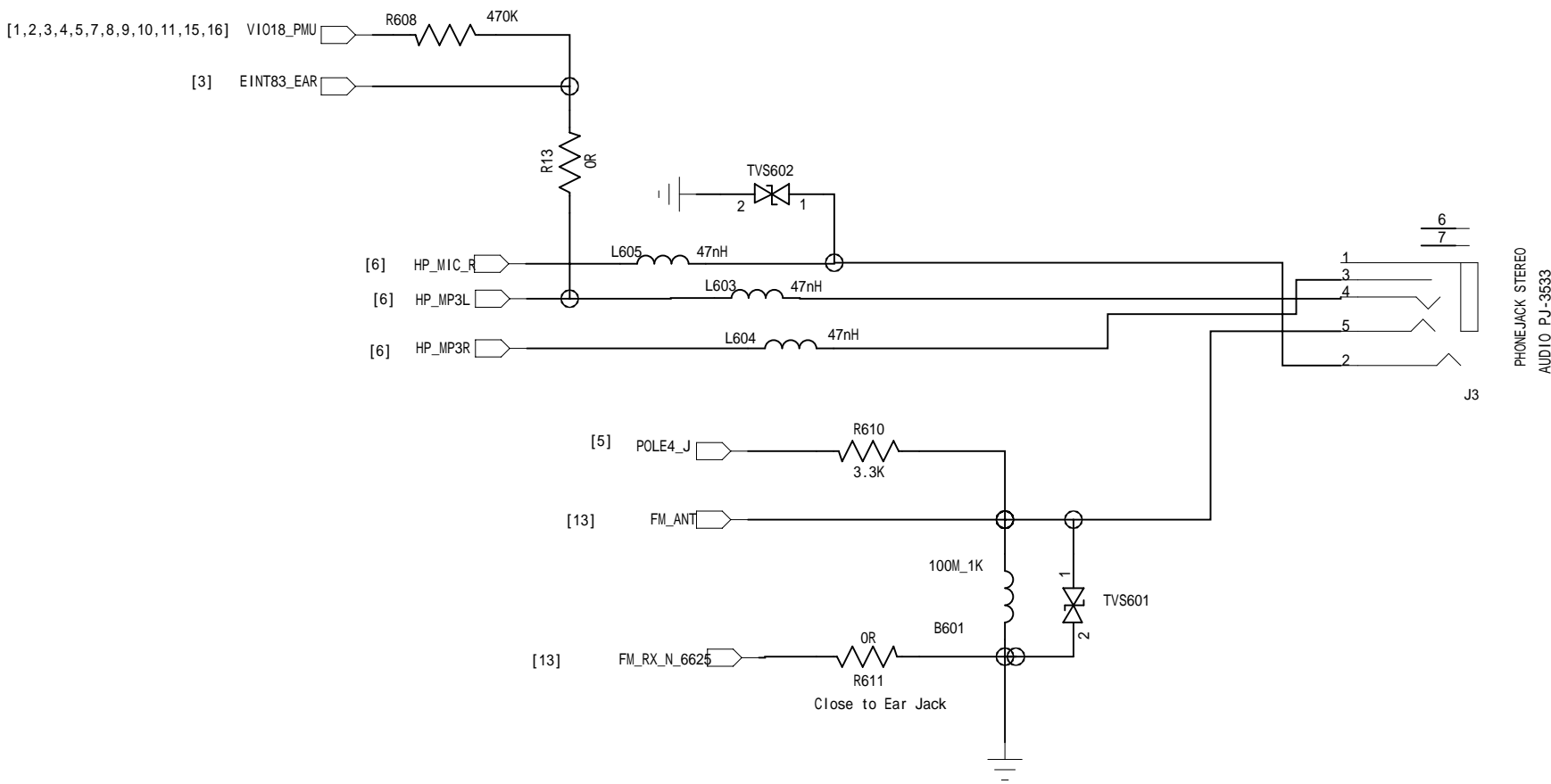
C2027/ C2038/ C2040 flying cap & holding cap : 4.7uF for 16ohm rectifier 2.2uF for 32ohm rectifier

To improve noise level, connect to audio jack first, and then connect to GND

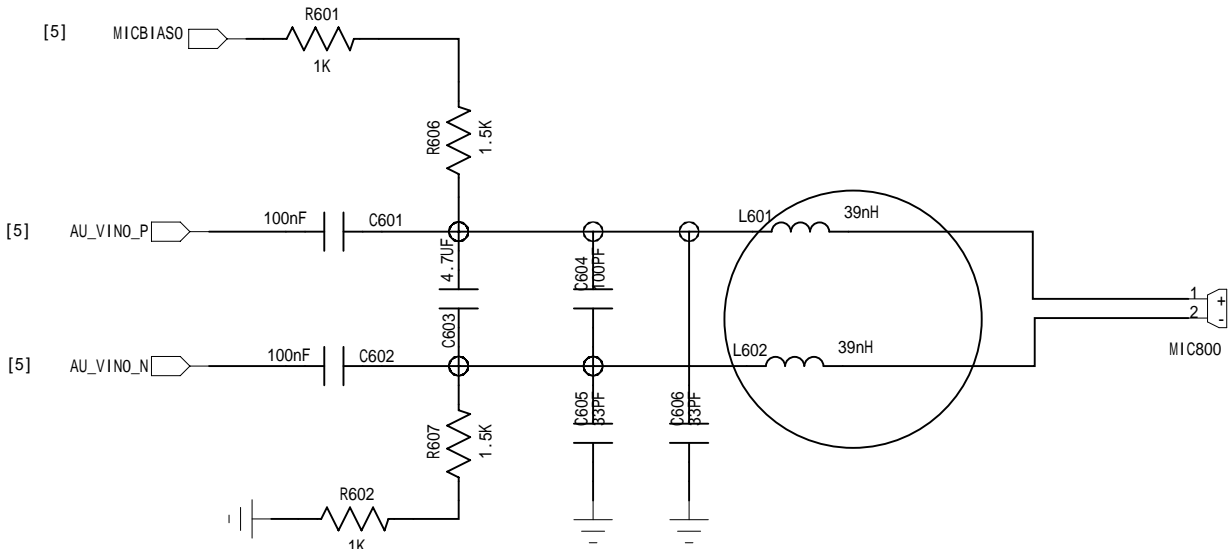
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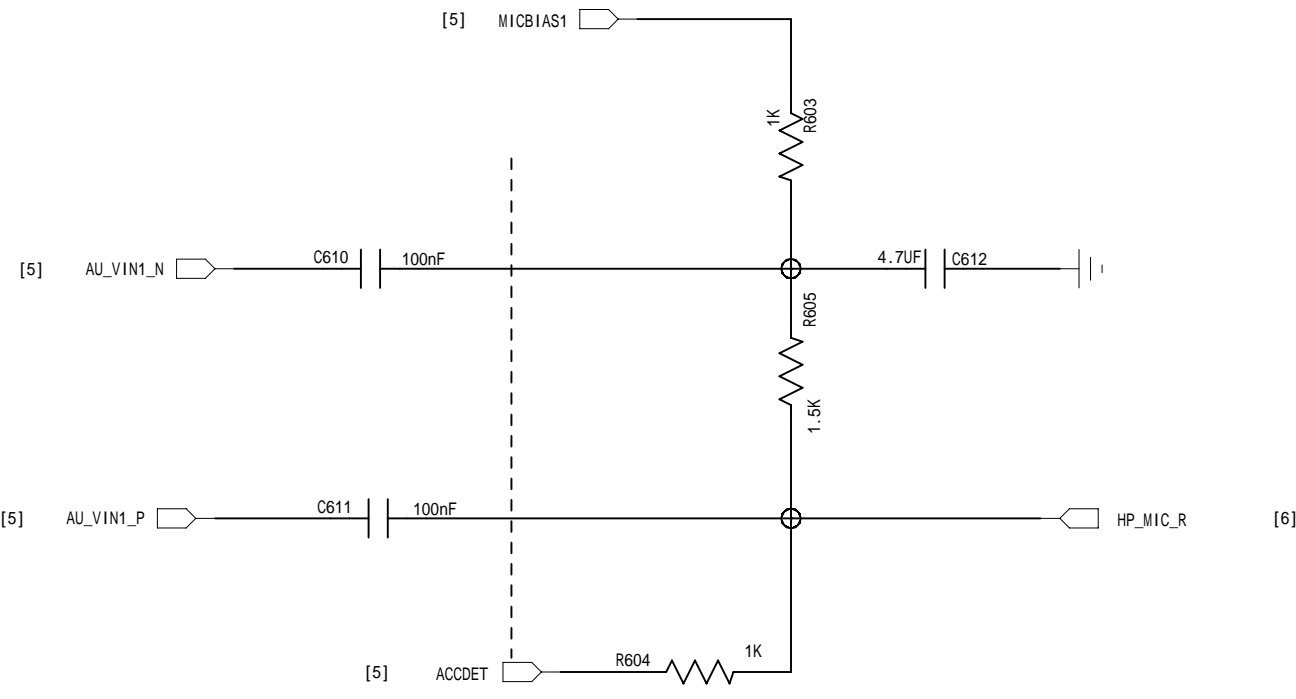
苹果定义



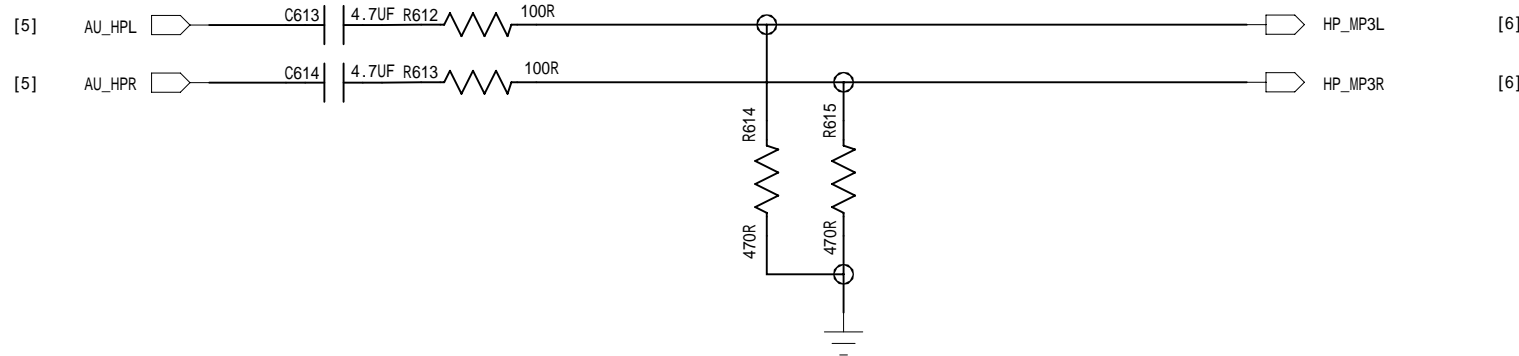
MIC



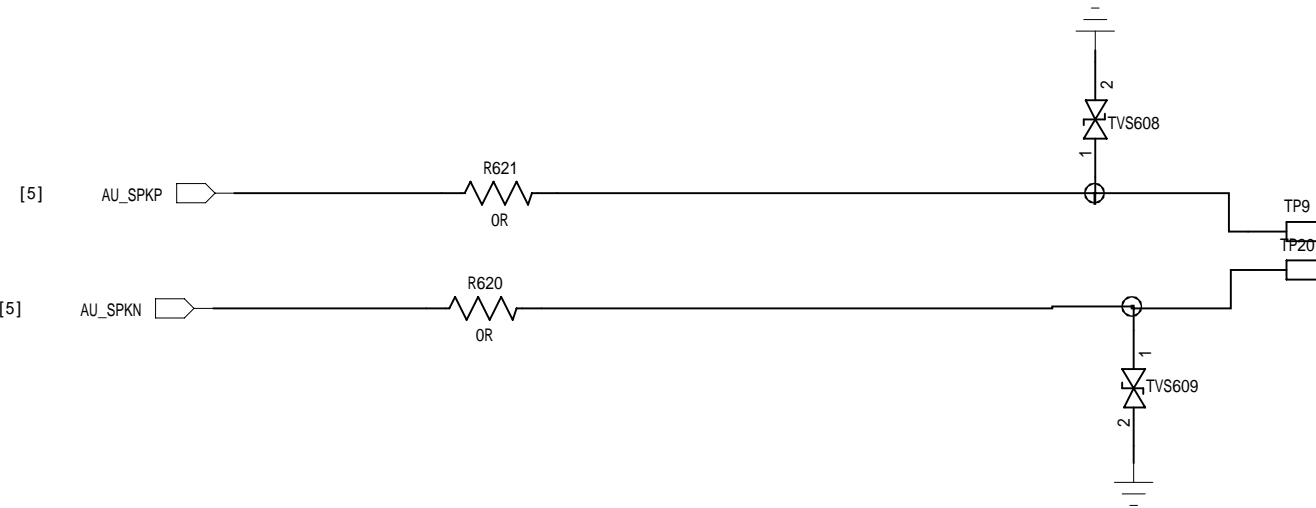
Earphone MIC



MP3

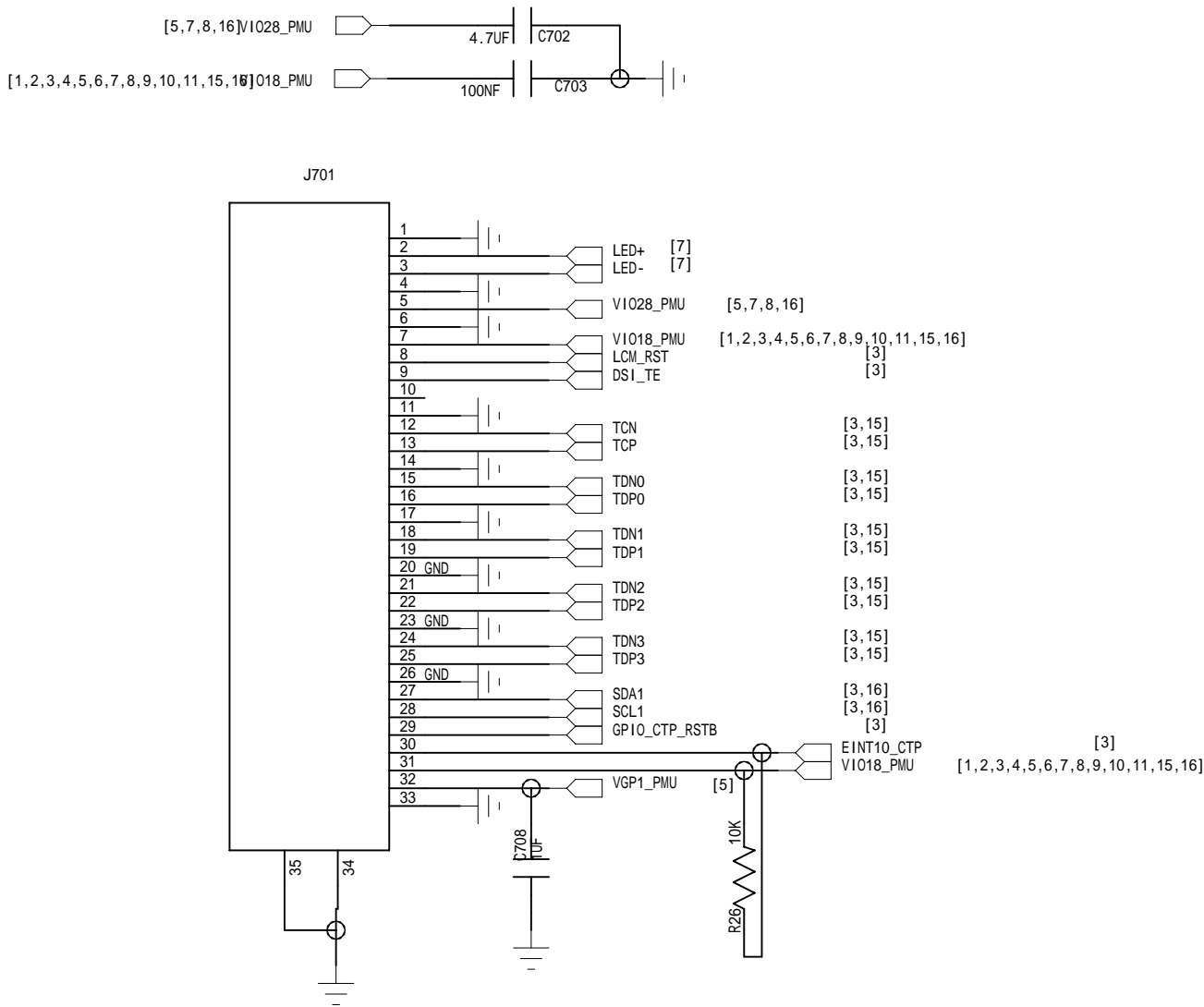


SPK

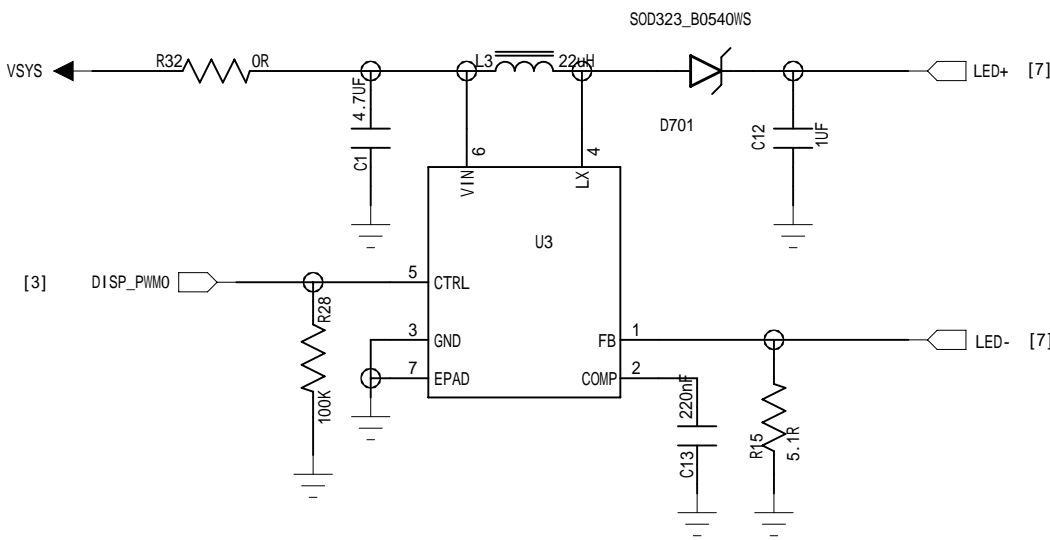


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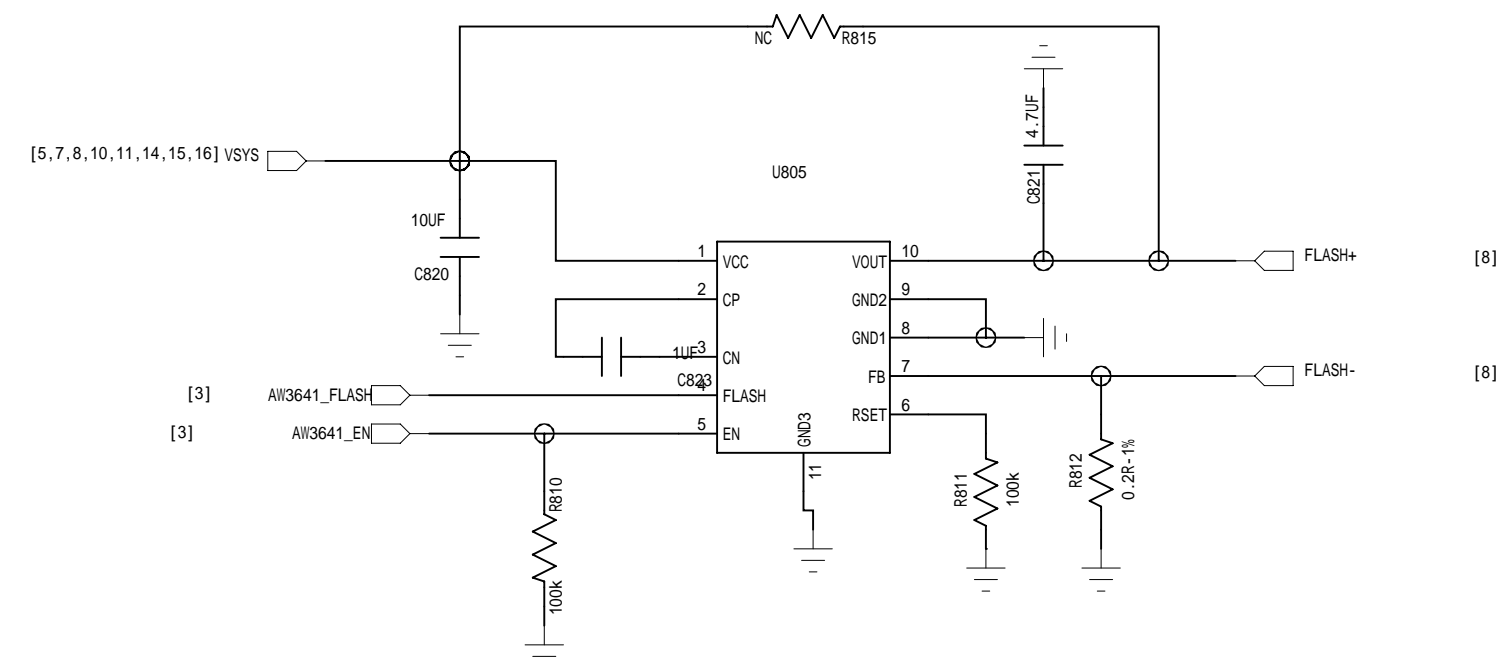
LCD



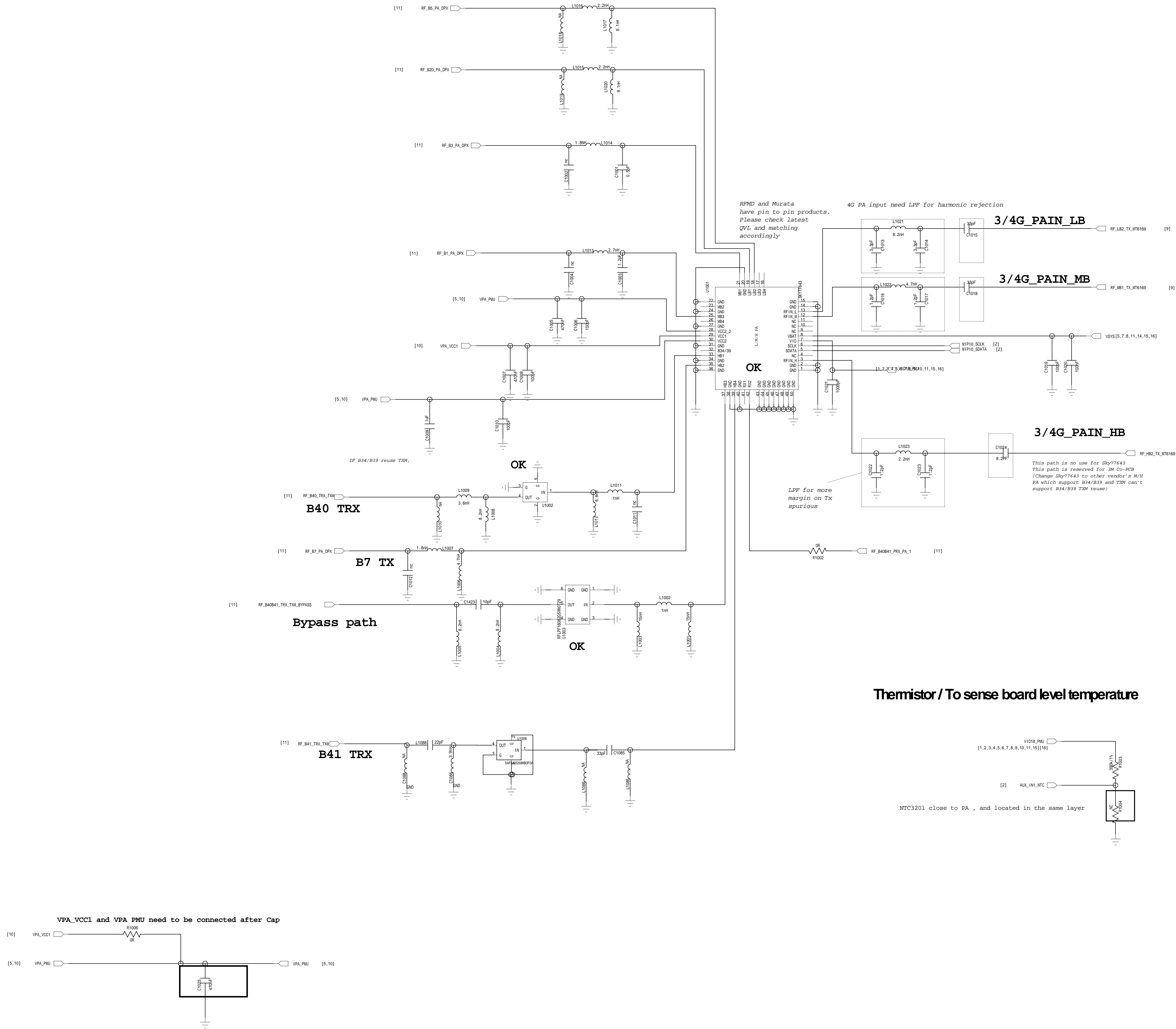
Back Light



Xunlong Software			
Design Name 4G-IOT_V1.1			
Size	A1	Page Name	REV
		CAMERA/G/P_SENSOR	1.1
Date: Wednesday, March 21, 2018		Sheet 8 of 16	



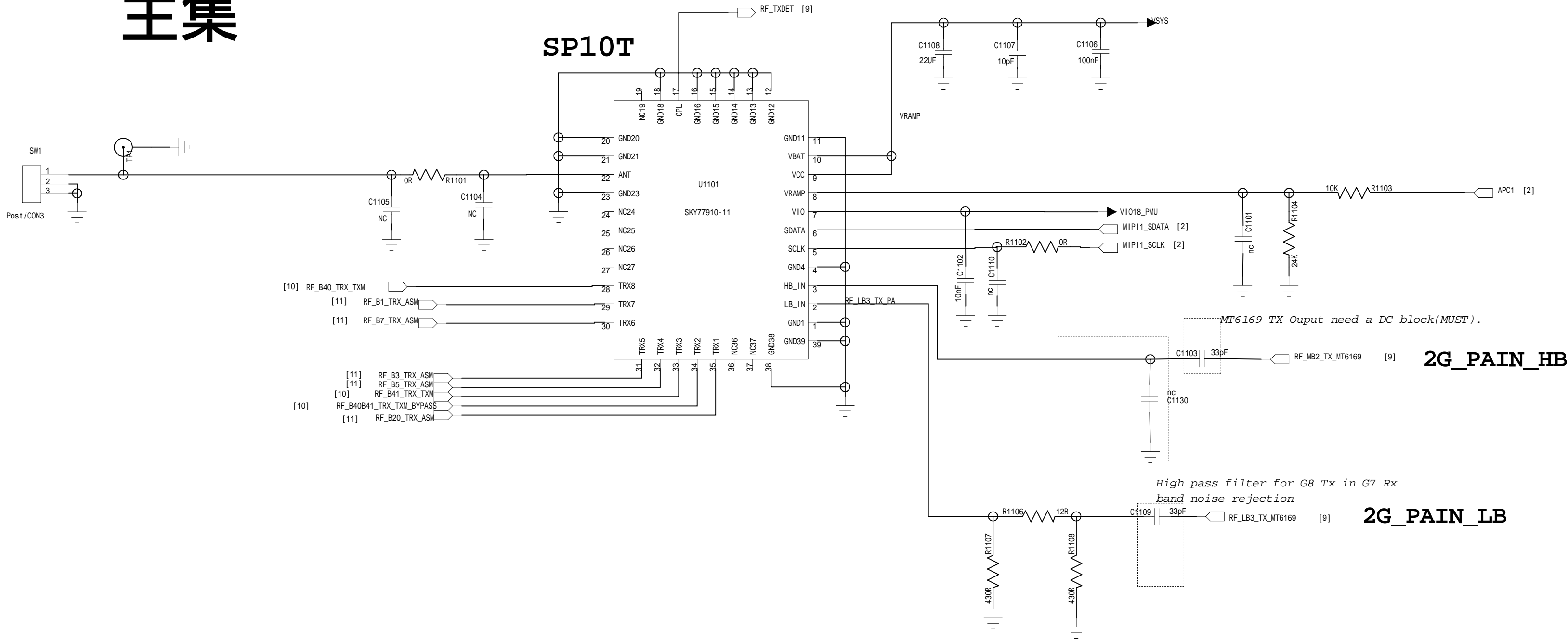
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ASM_Main_703~2690MHZ

主集



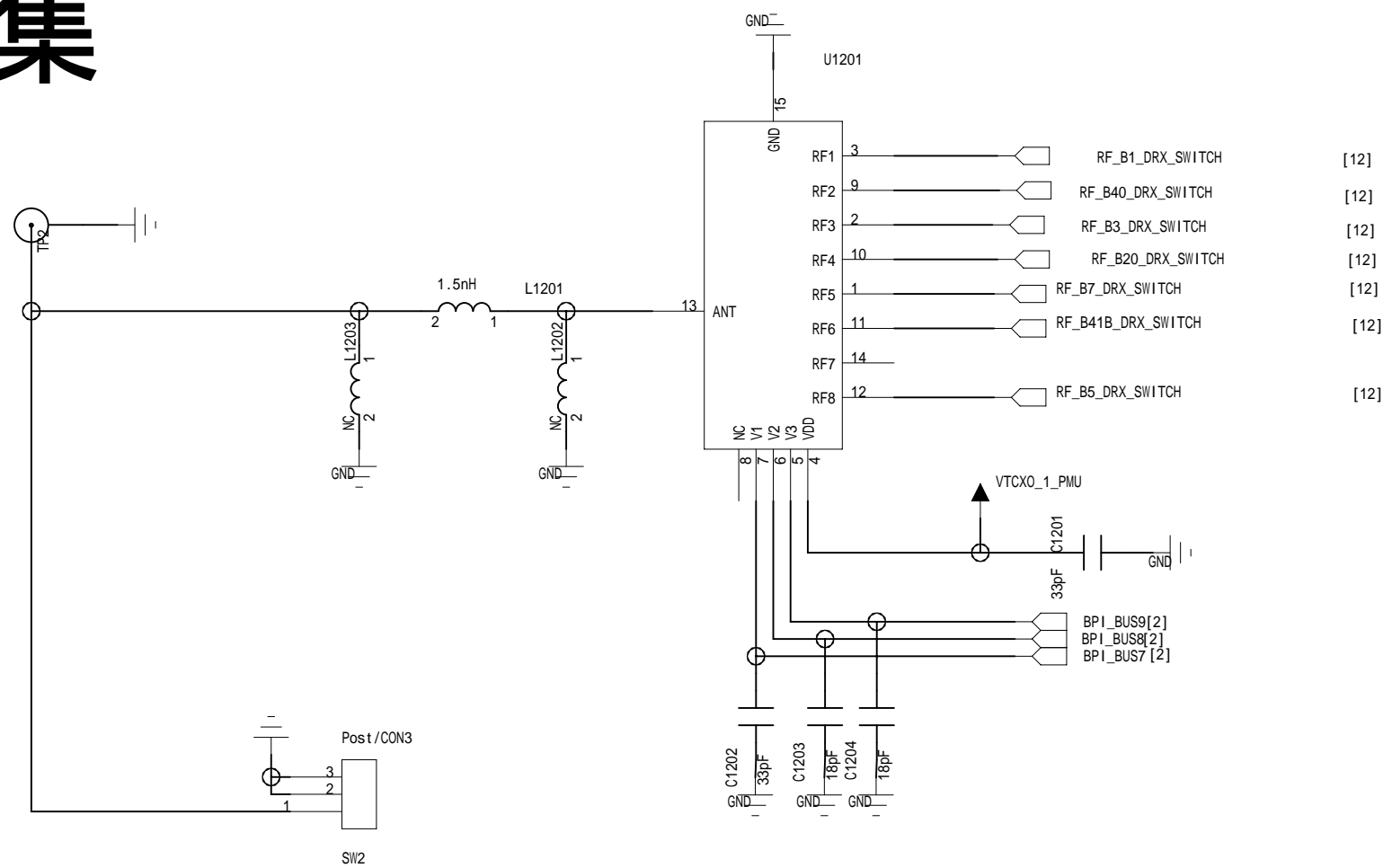
D

C

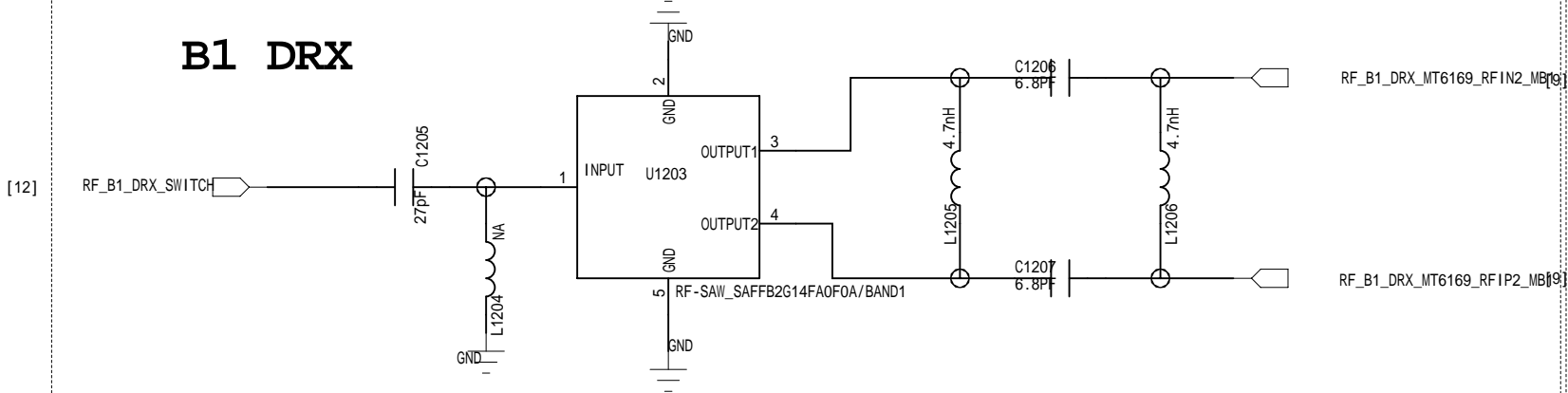
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A

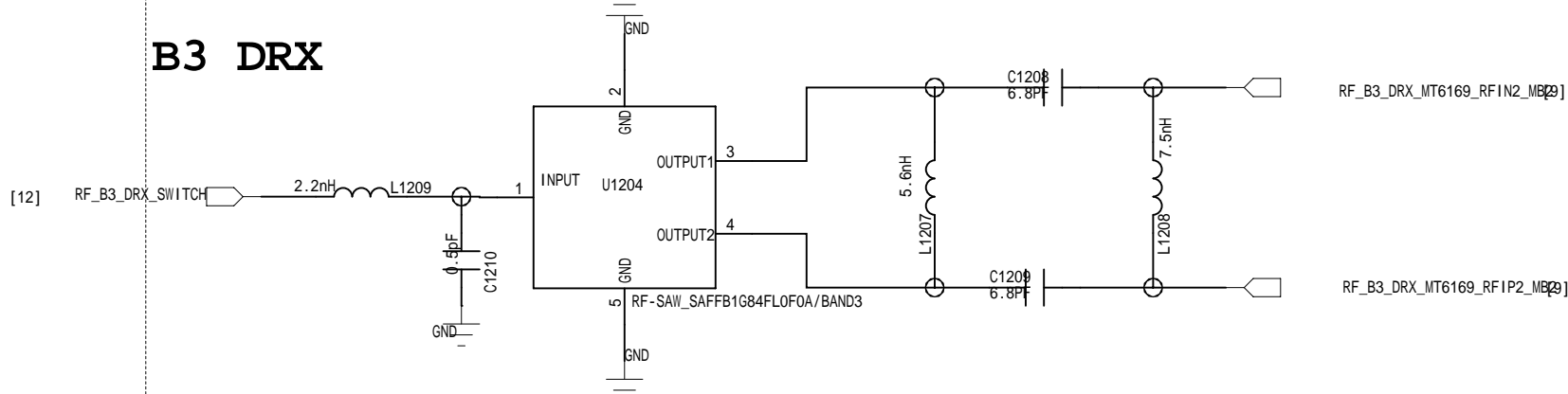
分集



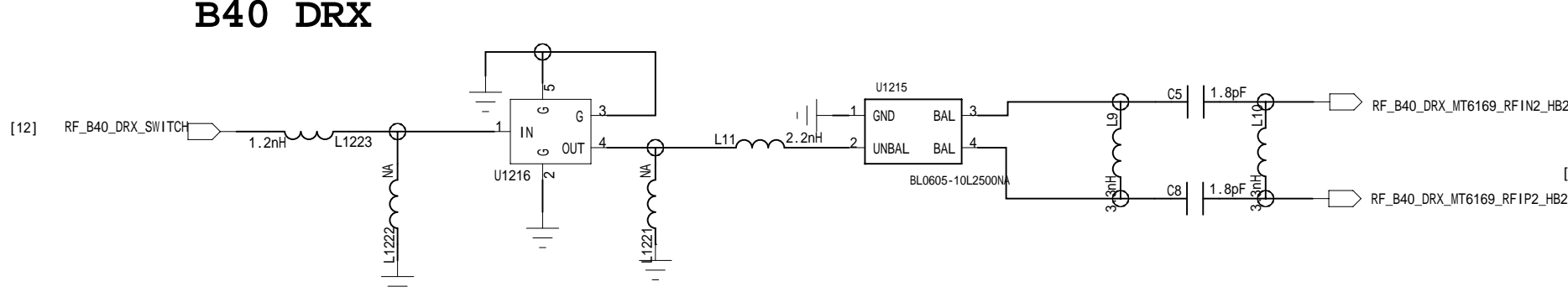
B1 DRX



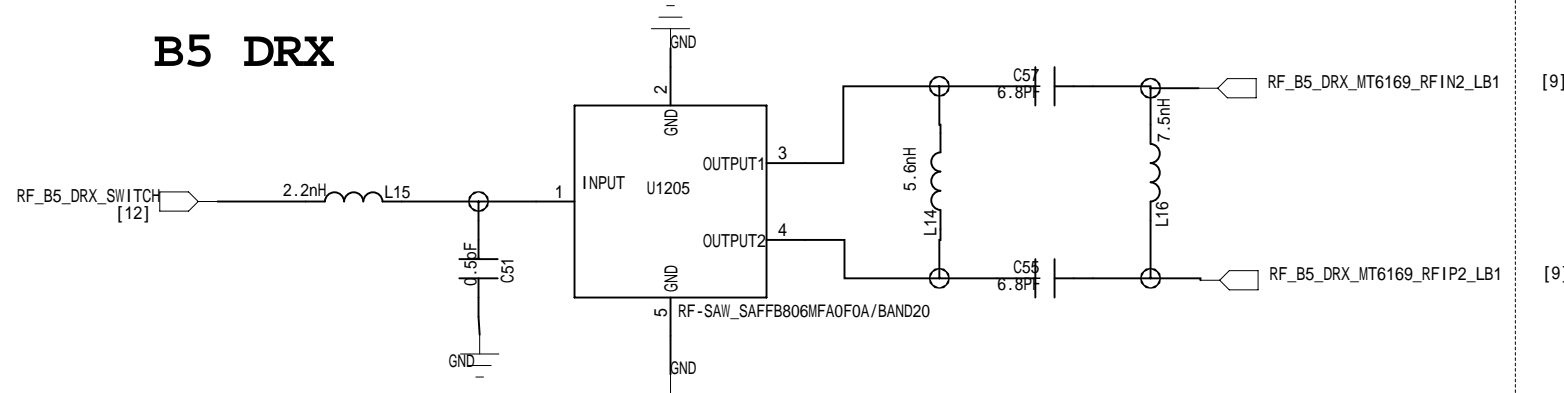
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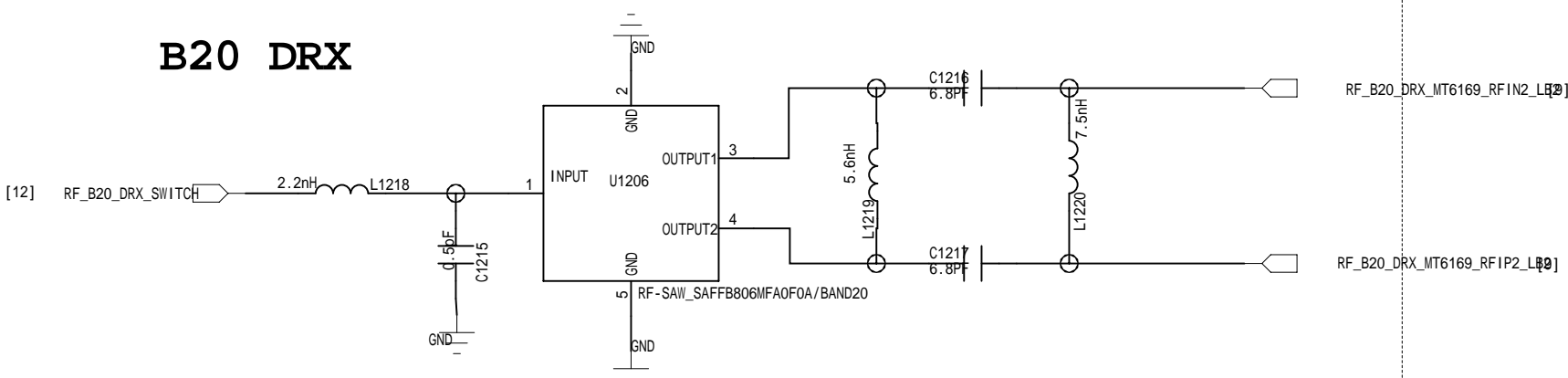
B40 DRX



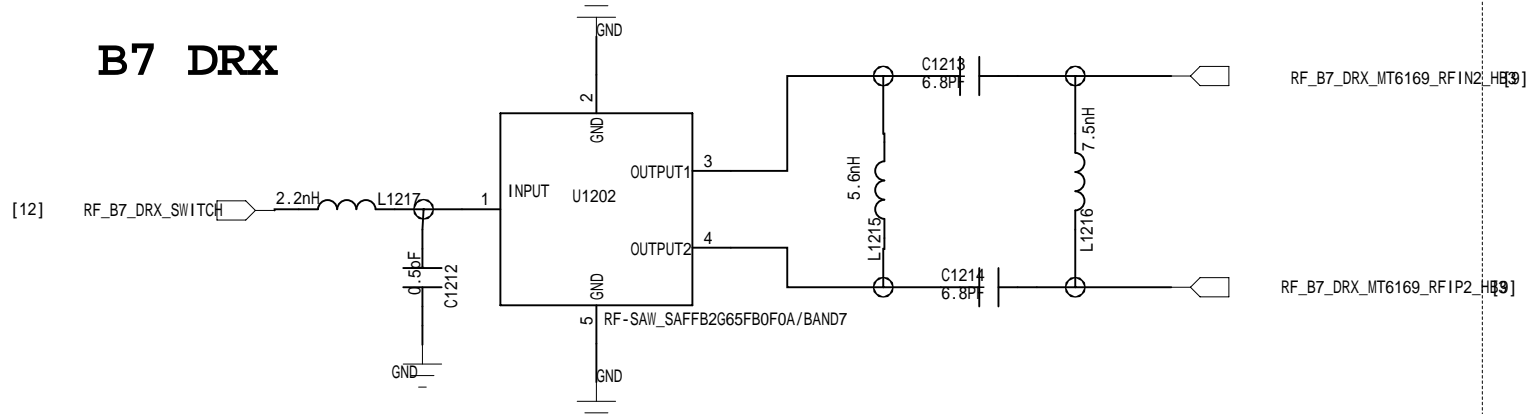
B5 DRX



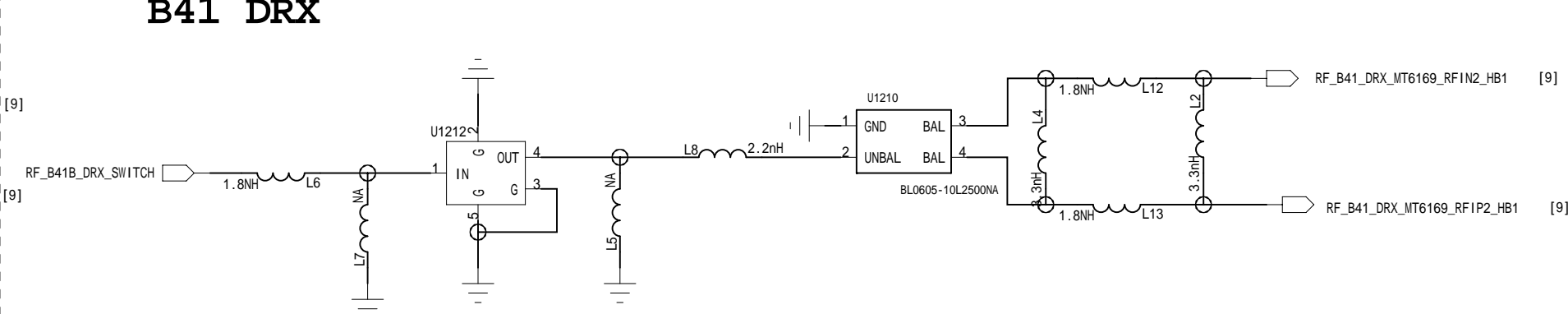
B20 DRX



B7 DRX

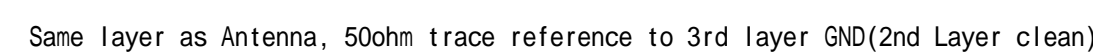


B41 DRX



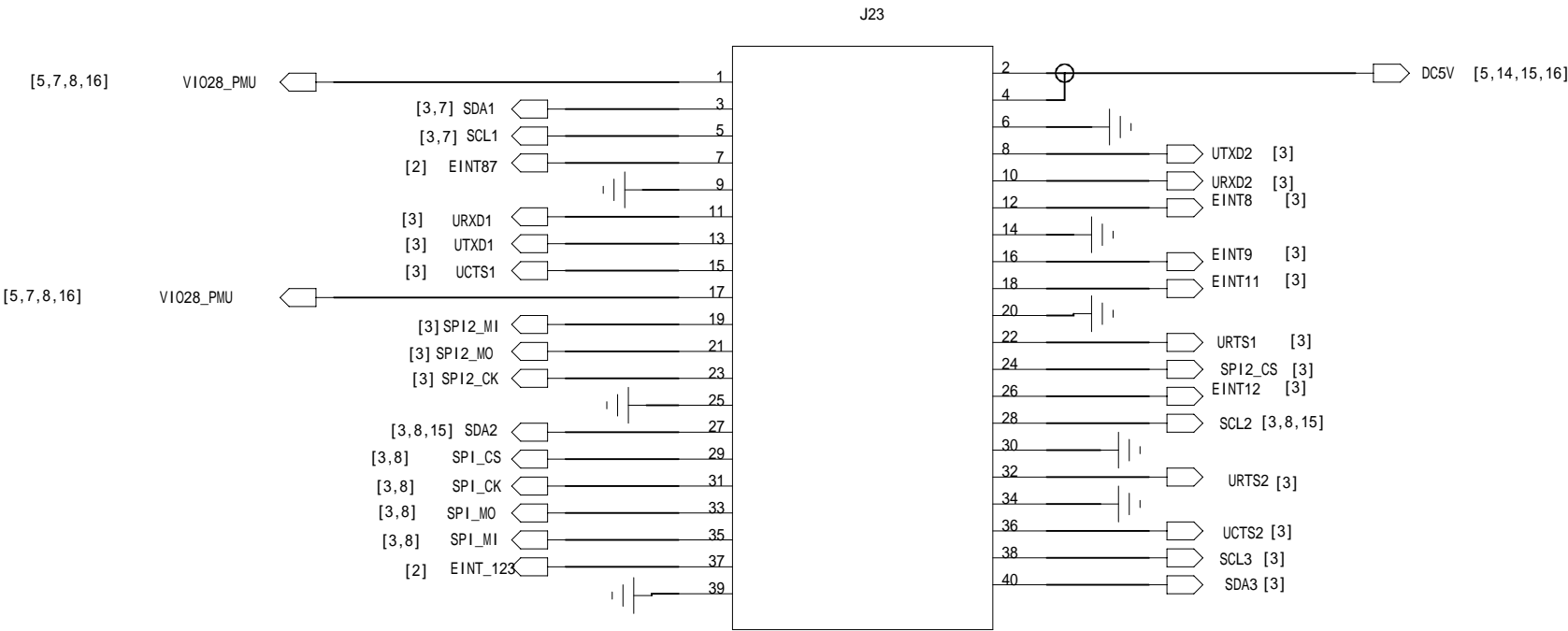
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Xunlong Software			
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EXPORT



2组SPI ; 2组UART ; 3组I2C ; 数个GPIO

SIM Card

T Card

TEST POINT

