

REVISION RECORD

LTR ECO NO: APPROVED: DATE:

MT6735-SBS [14,16] DP USB\_DP USB\_DM MSDCO\_RSTB D26 \_\_\_\_ MSDC0\_RSTB[4] MT6735-SBS MSDCO\_CMD B24 MSDCO\_CMD[4] MSDCO\_DAT7 MSDCO\_DAT6 MSDCO\_DAT5 MSDCO\_DAT3 MSDCO\_DAT3 MSDCO\_DAT3 MSDCO\_DAT3 MSDCO\_DAT3 MSDCO\_DAT1 MSDCO\_DAT1 MSDCO\_DAT0 MSDCO\_DAT0 A26 | MSDC0\_DATf[4] | MSDC0\_DAT[4] [8] RCP P28 RCP RCN TCP N2 TCP[7,15] TCN[7,15] [5] CHD\_DP T3 CHD\_DP [5] CHD\_DM T4 CHD\_DM MSDC1\_CLK H3 MSDC1\_CLK MSDC1\_CLK[16] MSDC1\_DAT3
MSDC1\_DAT2
H2 MSDC1\_DAT2
MSDC1\_DAT1
MSDC1\_DAT1
MSDC1\_DAT0
G4 MSDC1\_DAT0 KEYPAD MSDC2\_CLK L29 MSDC2\_CMD K28 MSDC2\_DAT3 M30 MSDC2\_DAT2 M31 MSDC2\_DAT1 L27 MSDC2\_DAT0 M27 [16] URTS1 AG24 KPC0L2 AK24 KPC0L1 [16] KC0L0 AL24 KPC0L0 1% 55 55 CONN\_IF WB\_CTRL0
WB\_CTRL1
WB\_CTRL2
WB\_CTRL3
WB\_CTRL4
WB\_CTRL5 \_\_\_\_V28\_ \_\_\_U28\_\_ RDP3\_A \_\_\_DN3\_A 12C DISP\_PWM AH31 DISP\_PWM0[7] CONN\_SCLK
CONN\_SDATA
CONN\_SEN LCM\_RST V4 LCM\_RST [7] [8] AW3641\_FLASH W29 CMDATO DSI\_TE AA5 DSI\_TE[7] [8] AW3641\_EN W28 CMDAT1 [8] GPIO\_CAMERA\_RESET \_\_\_\_\_\_\_\_\_\_Y31 CMPCLK WB\_RSTB B29 CONN\_RSTB [13] PCM (ext MD) [8] CMMCLK Y27 CMMCLK PCM\_CLK AE27 [1,2,3,4,5,6,7,8,9,10,11,15,16] VIO18\_PMU R12 R4.7K 4.7K PCM\_SYNC AD29 GPIO\_CTP\_RSTB [7] PCM\_TX AD28 PCM\_RX AD27 GPIO [8,16] SPI\_CK \_\_\_\_\_\_ AH29 SPI\_CK SRCLKENA I SRCLKENA I [8,16] SPI\_MO \_\_\_\_\_\_AG30 SPI\_MO EINTO AF28 HUB\_EINTO [14] EINT\_SD [16] [8,16] SPI\_MI \_\_\_\_\_\_\_ AH30 SPI\_MI EINT2 AF30 GSENSOR\_EINT2 [8]
SPI2\_MI [16] WCN 6625 128 EINT4 AF31 SPI2\_MO [16] [13] GPS\_RX\_IP [13] GPS\_RX\_IN [13] GPS\_RX\_QP [13] GPS\_RX\_QN E30 GPS\_RXIP
D30 GPS\_RXIN
C30 GPS\_RXQP
G75\_RXQP
G75\_RXQN EINT5 AL29 SPI2\_CS [16] EINT6 AK29 SPI2\_CK [16] [13] F2W\_CLK D27 F2W\_DATA EINT7 AJ26 \_\_\_\_\_\_ CAM\_PDNO [8] [15] 12\$\_\$D0 \_\_\_\_\_\_AK27 | 12\$\_DATA\_IN EINT8 AH26 EINT8 [16] EINT9 AG26 EINT9 [16]
EINT10 AH25 EINT10\_CTF[7] EINT11 AG25 EINT11 [16] EINT12 AK25 \_\_\_\_\_\_\_EINT12 [16] MT6735

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Design Name

4G-IOT\_V1.1

Size A1 Page Name MT6737\_BB2\_PERIPHERAL REV 1.1

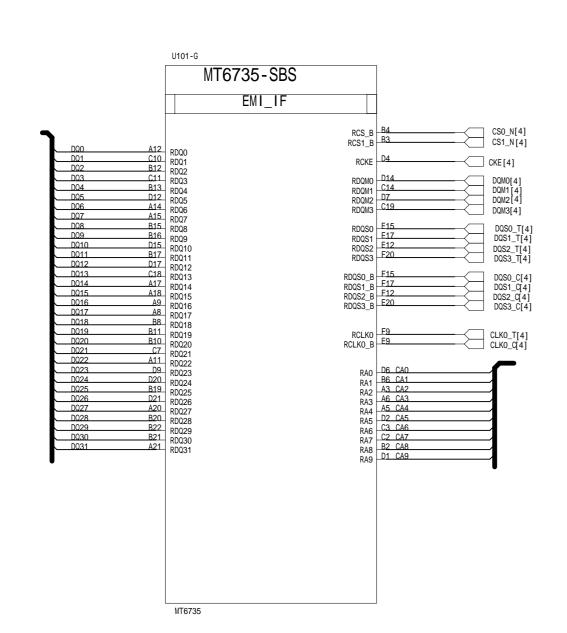
Date: Wednesday, March 21, 2018 Sheet 3 of 16

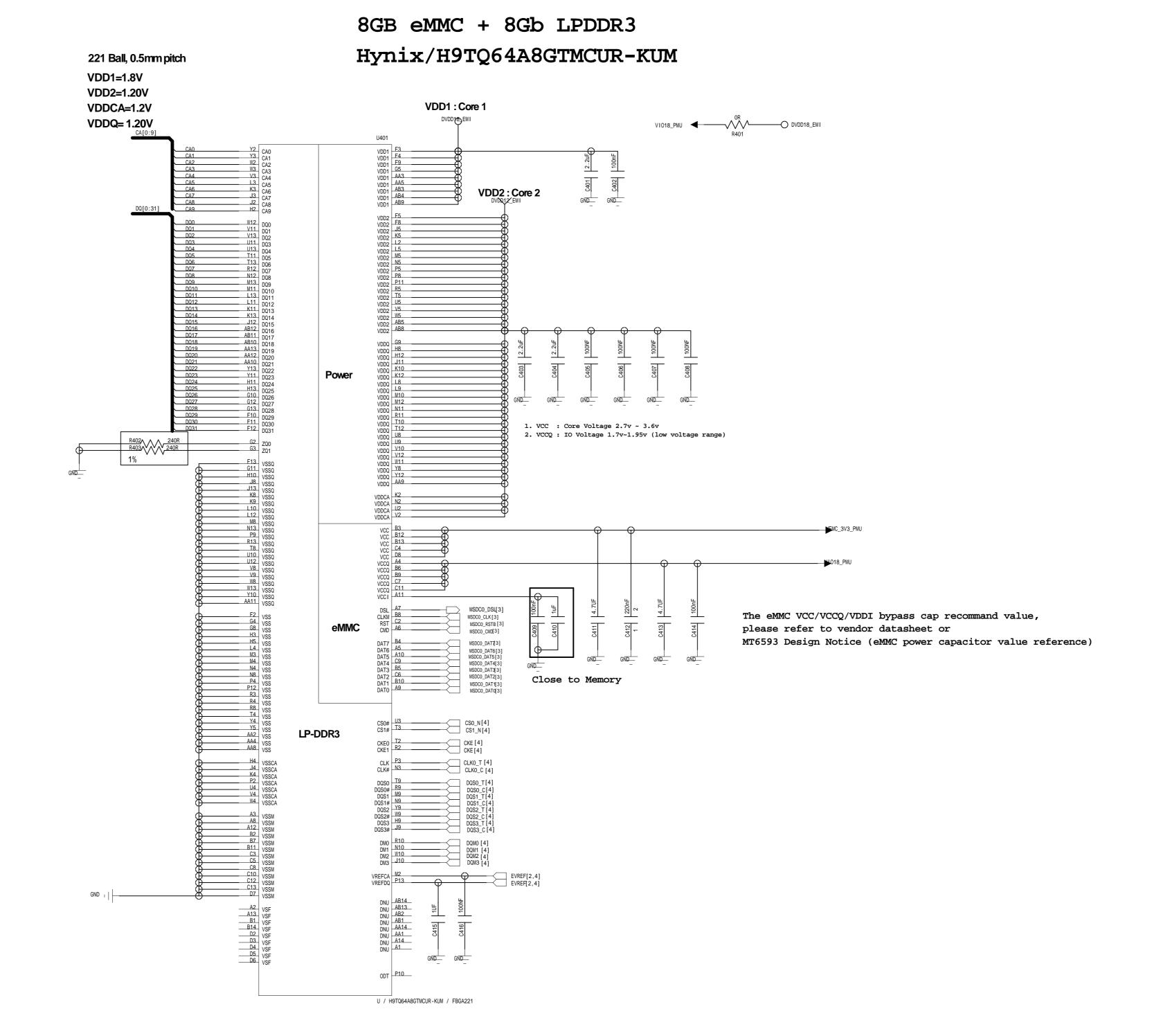
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## eMMC+LPDDR3





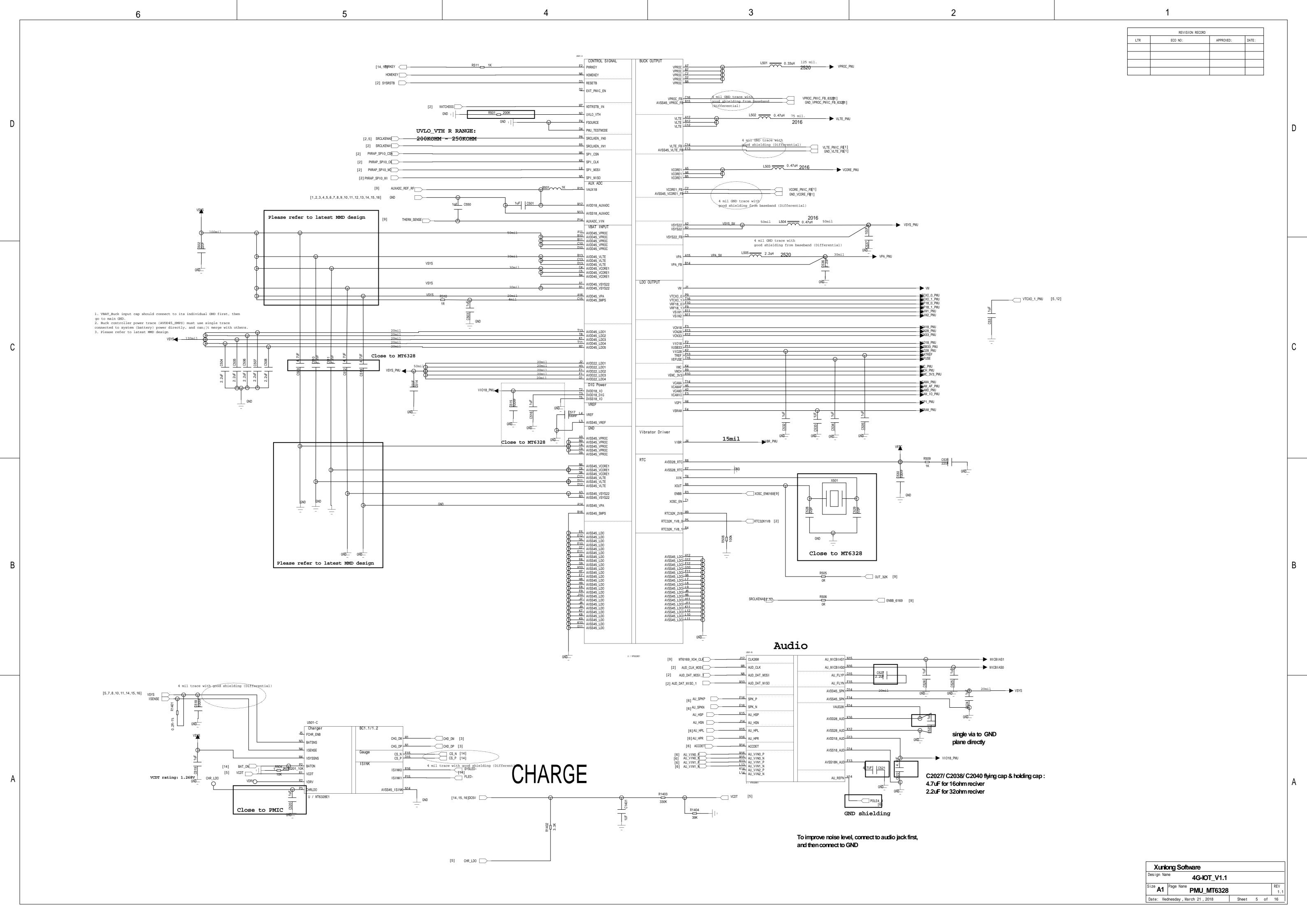
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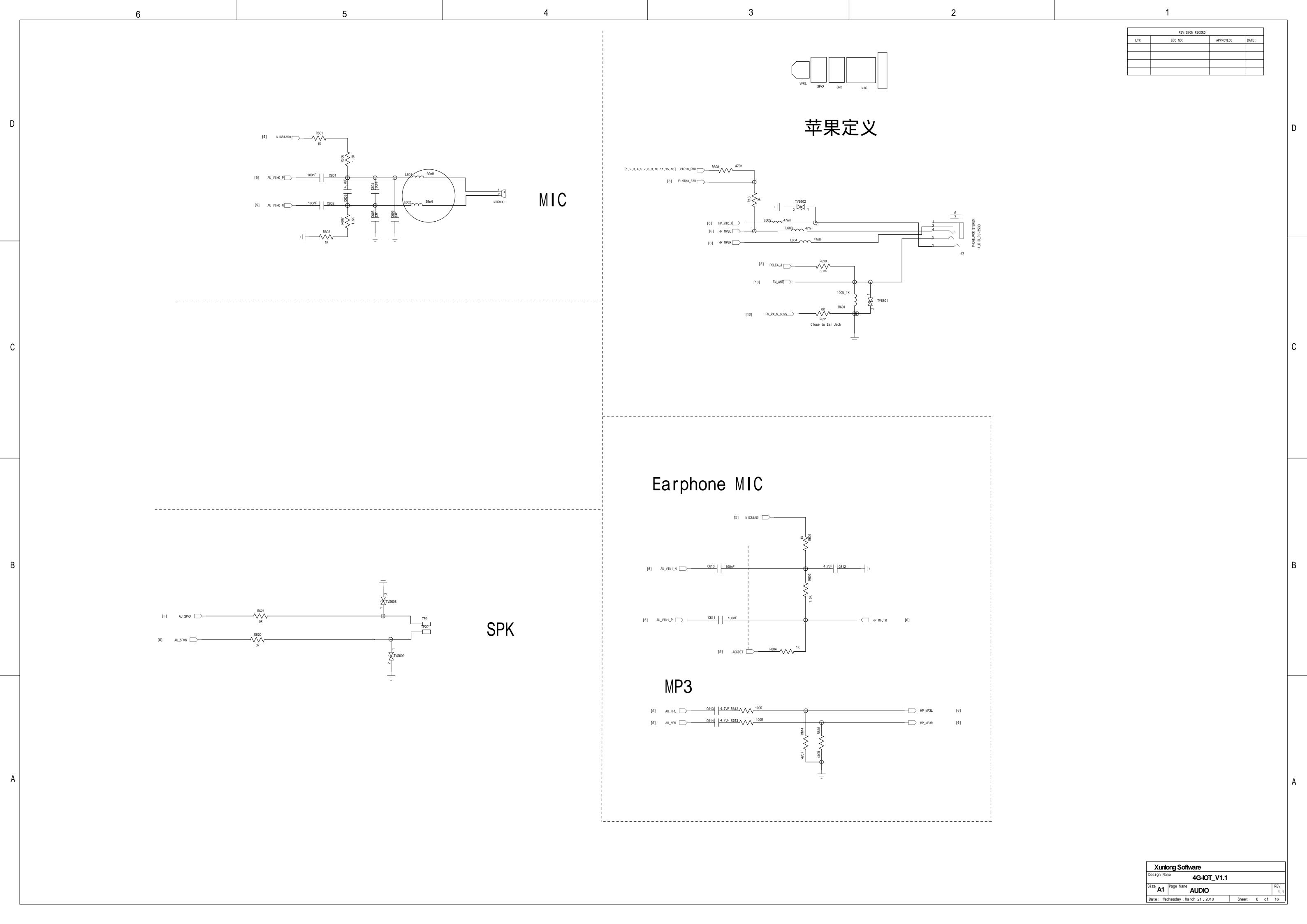
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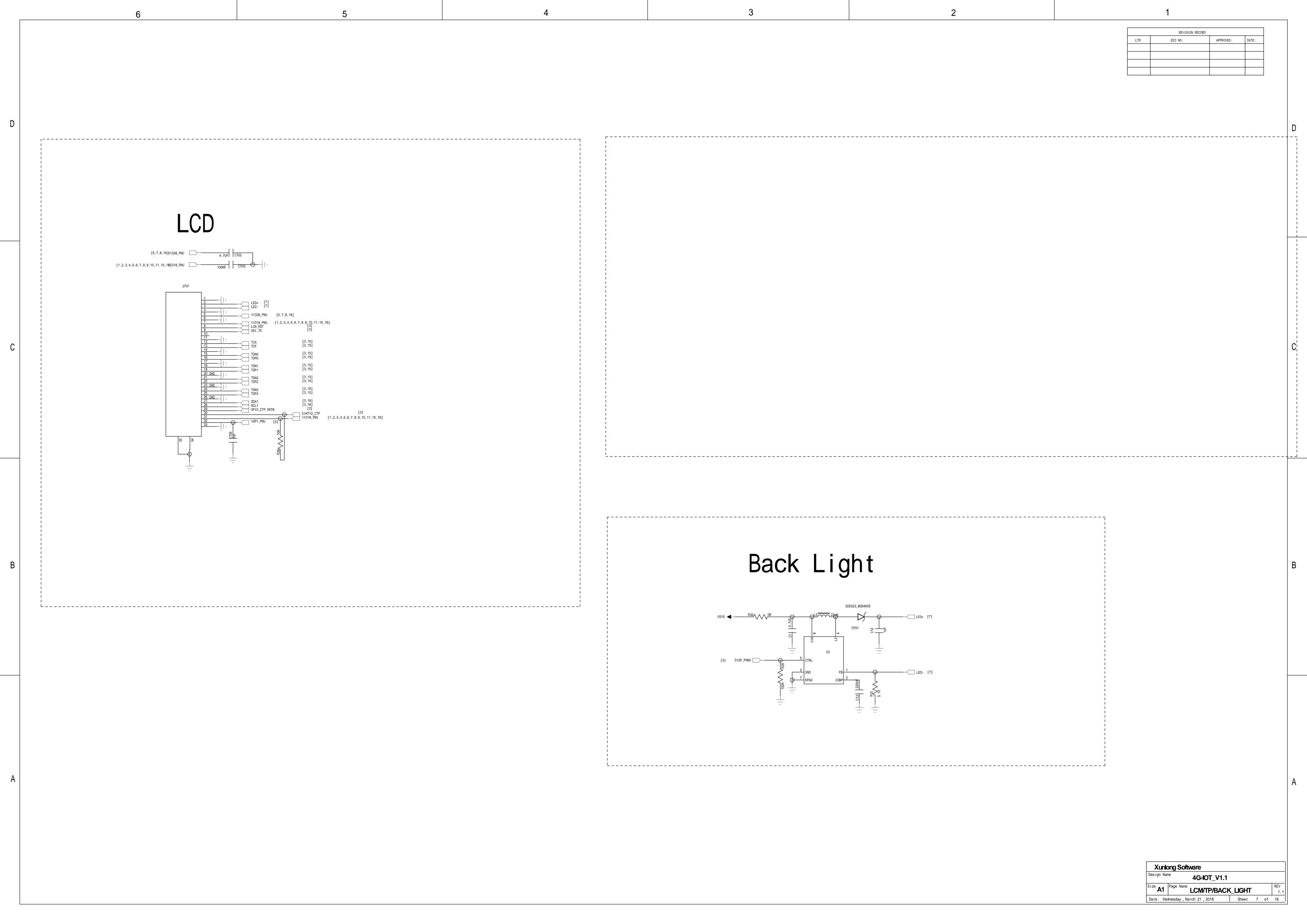
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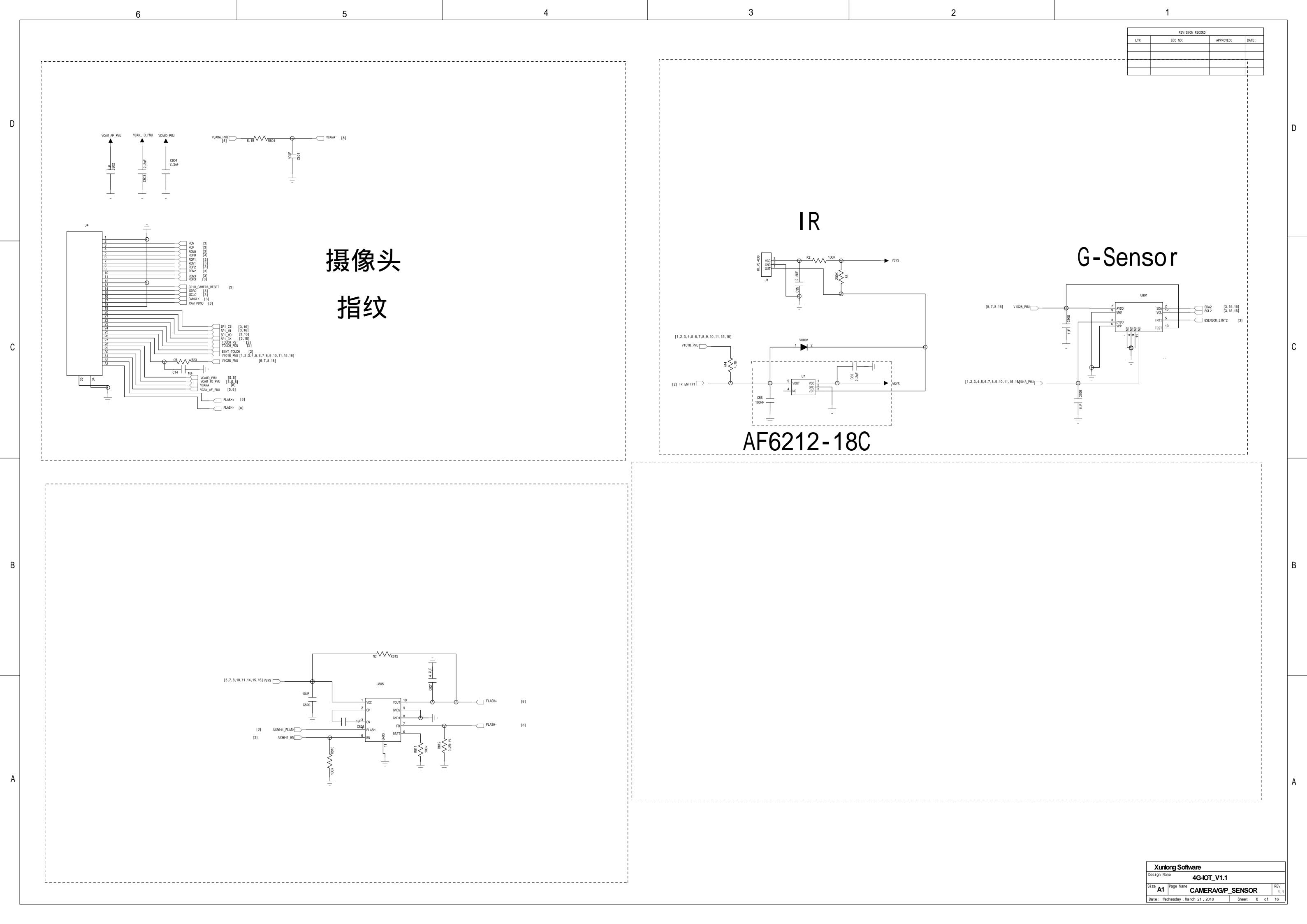
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 REV 1.

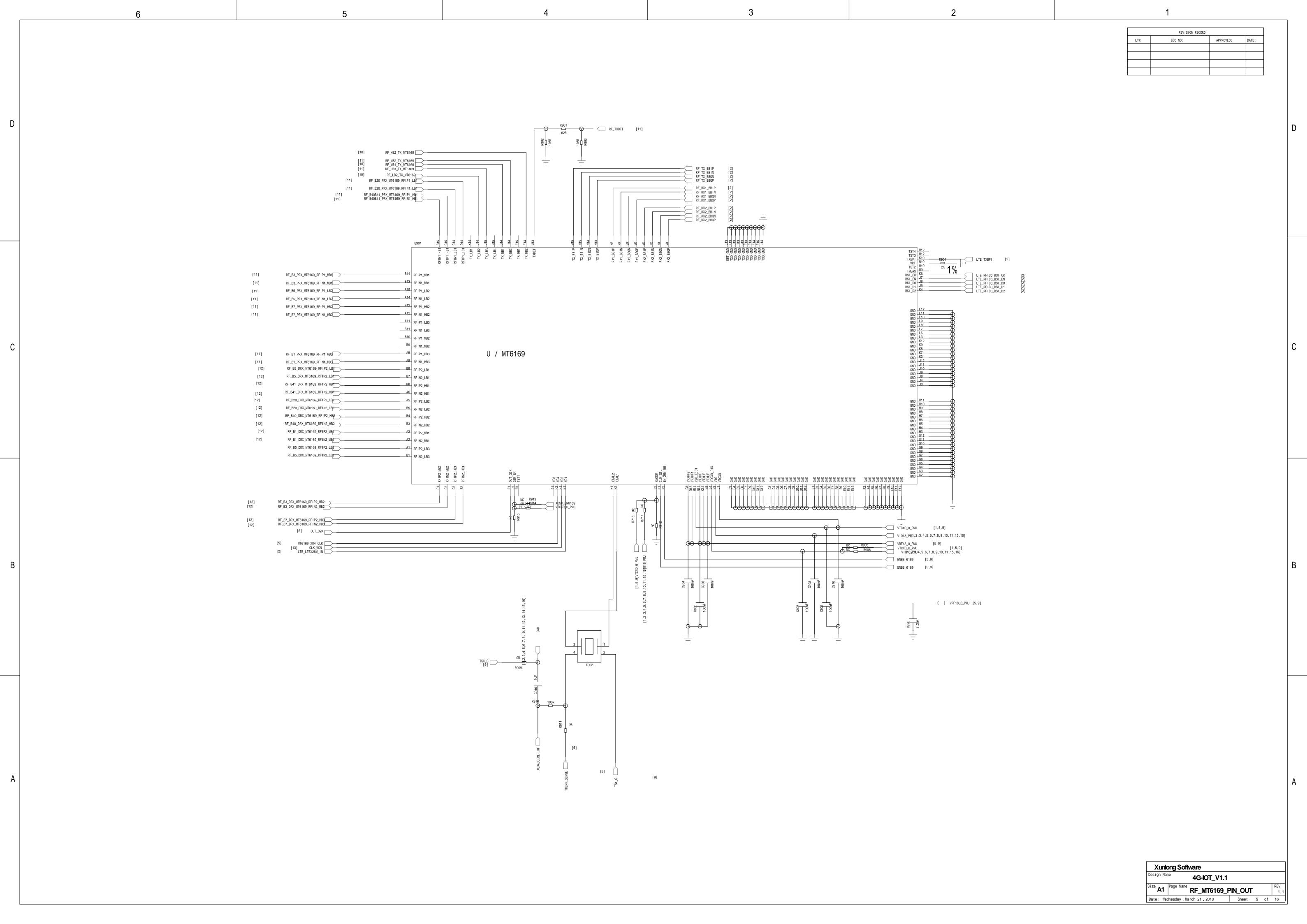
 Date:
 Wednesday , March 21 , 2018
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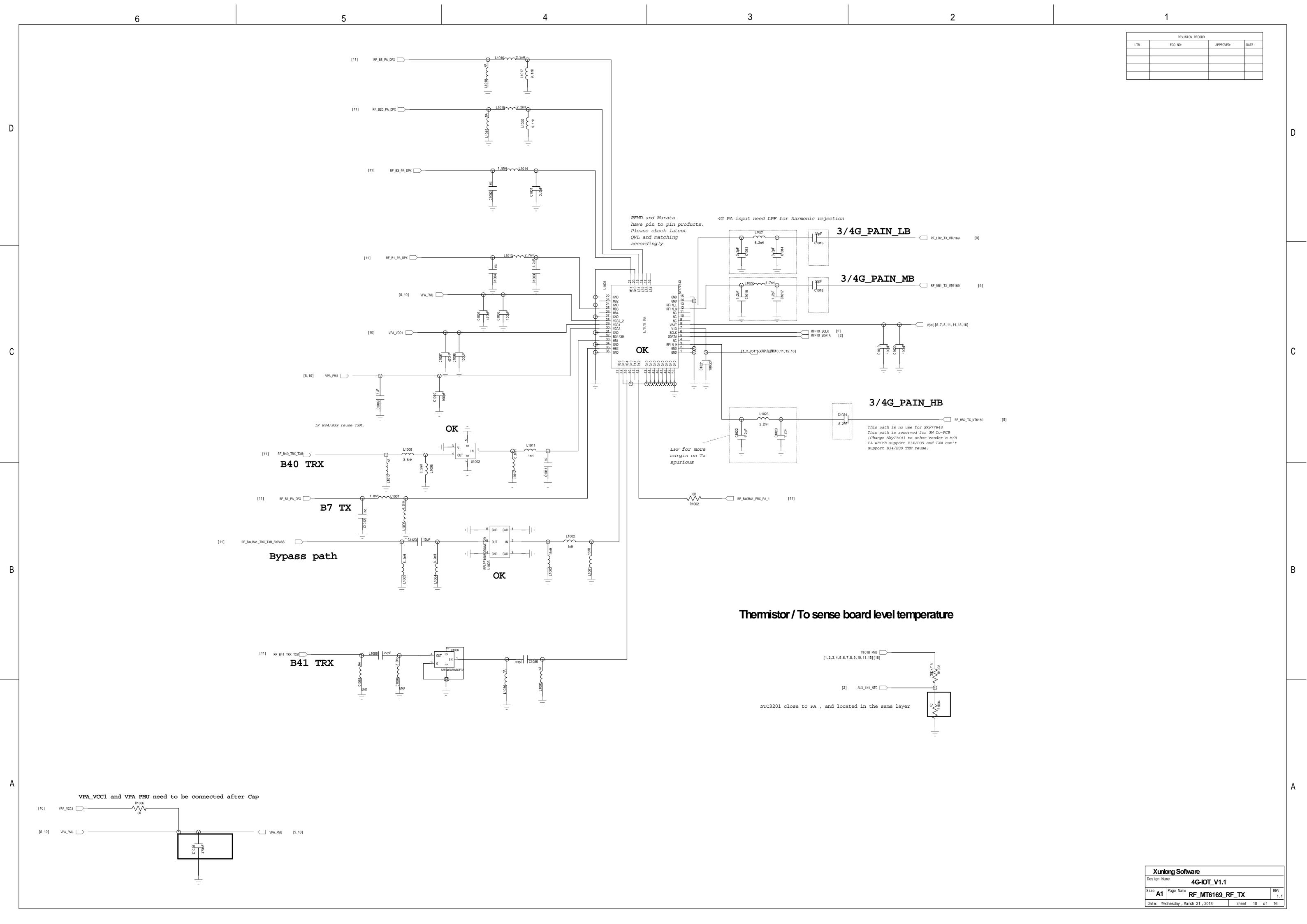


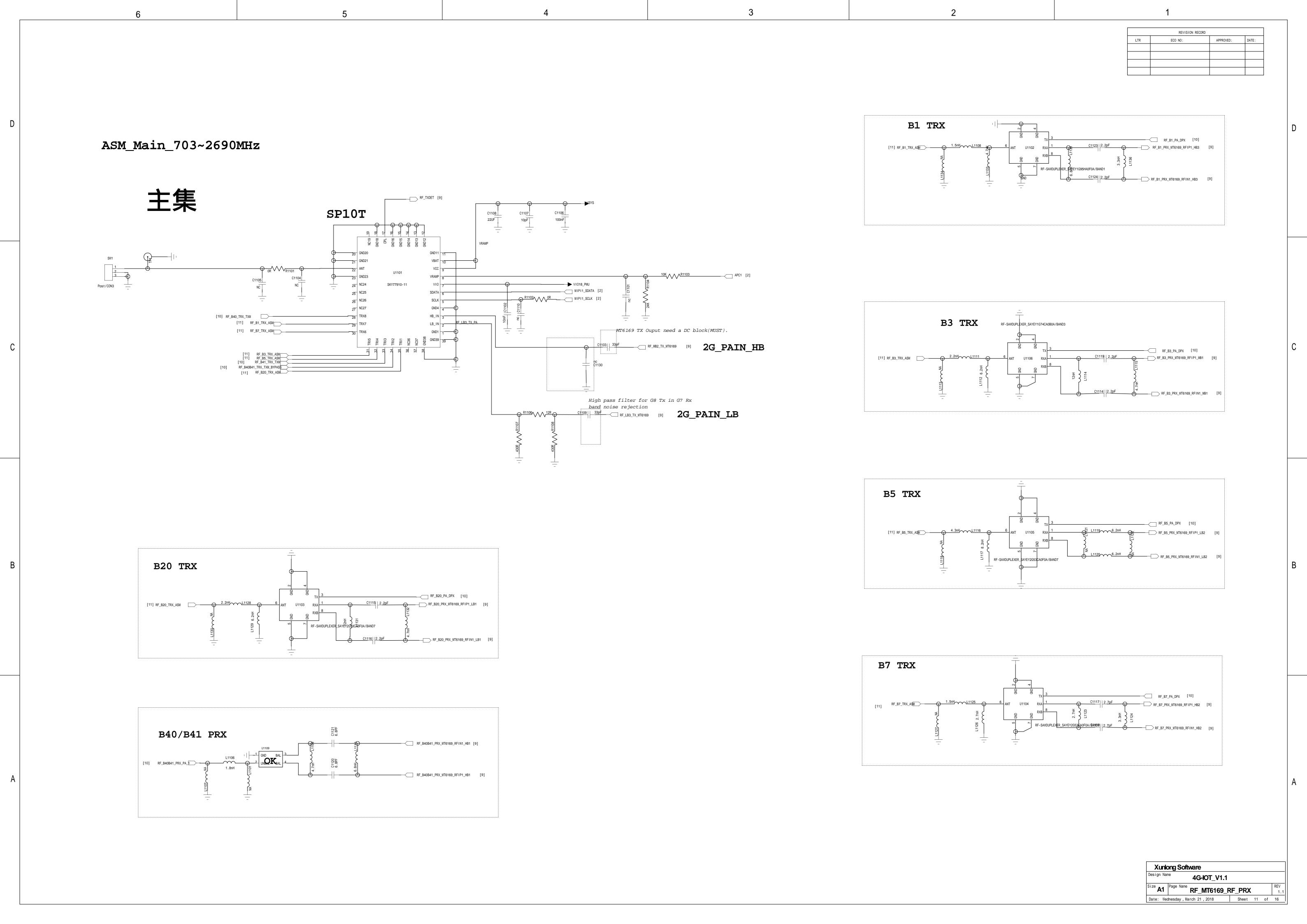


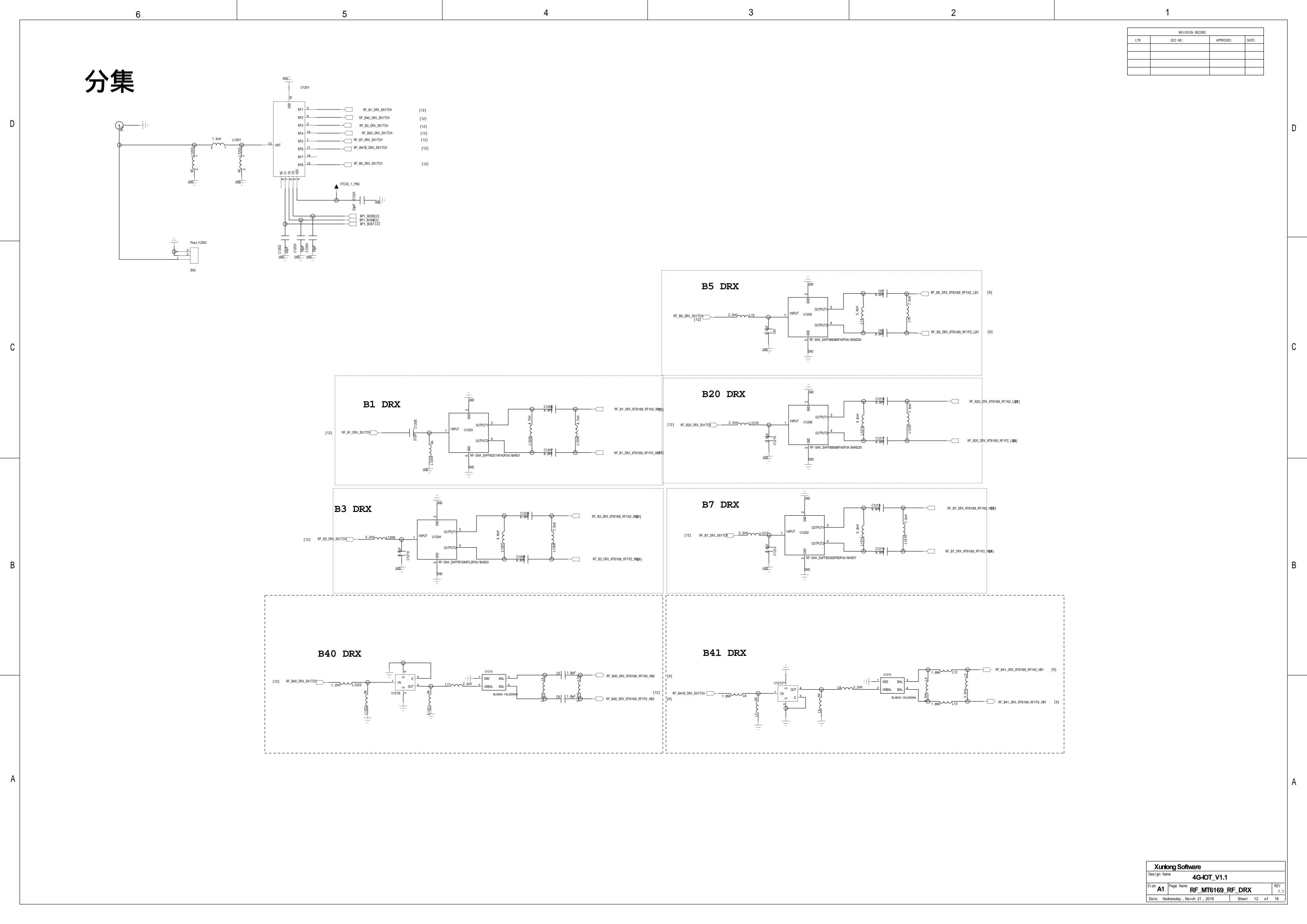


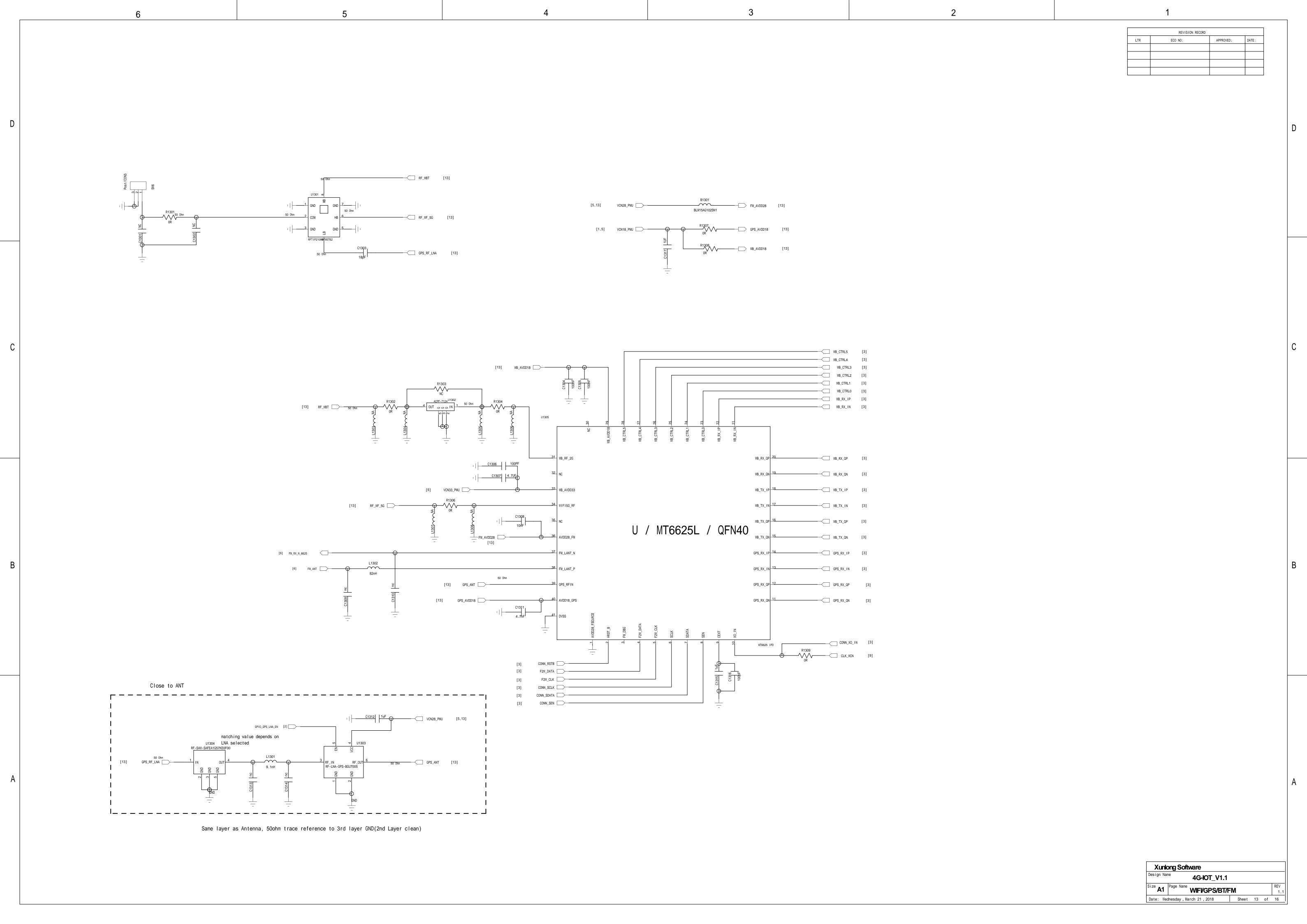


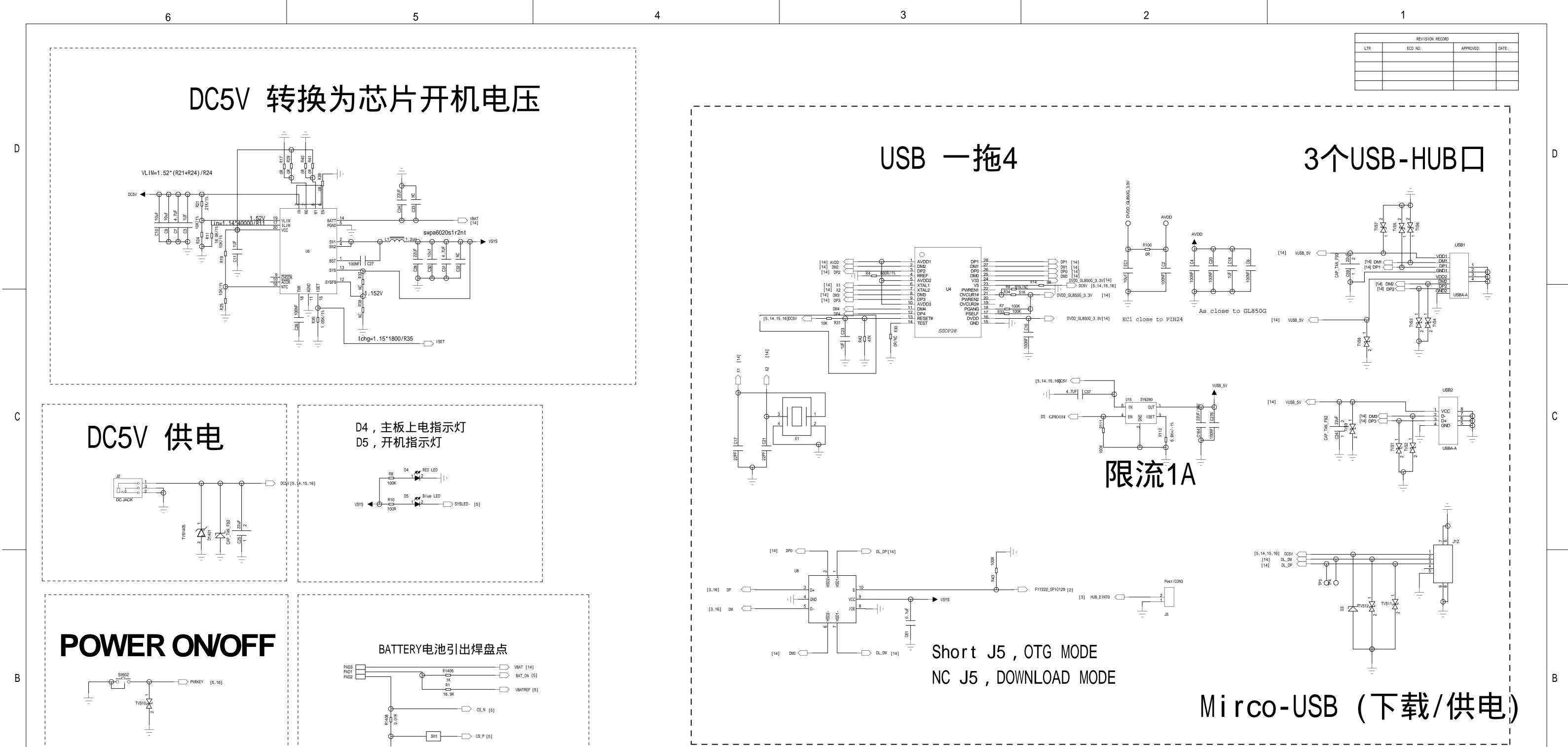












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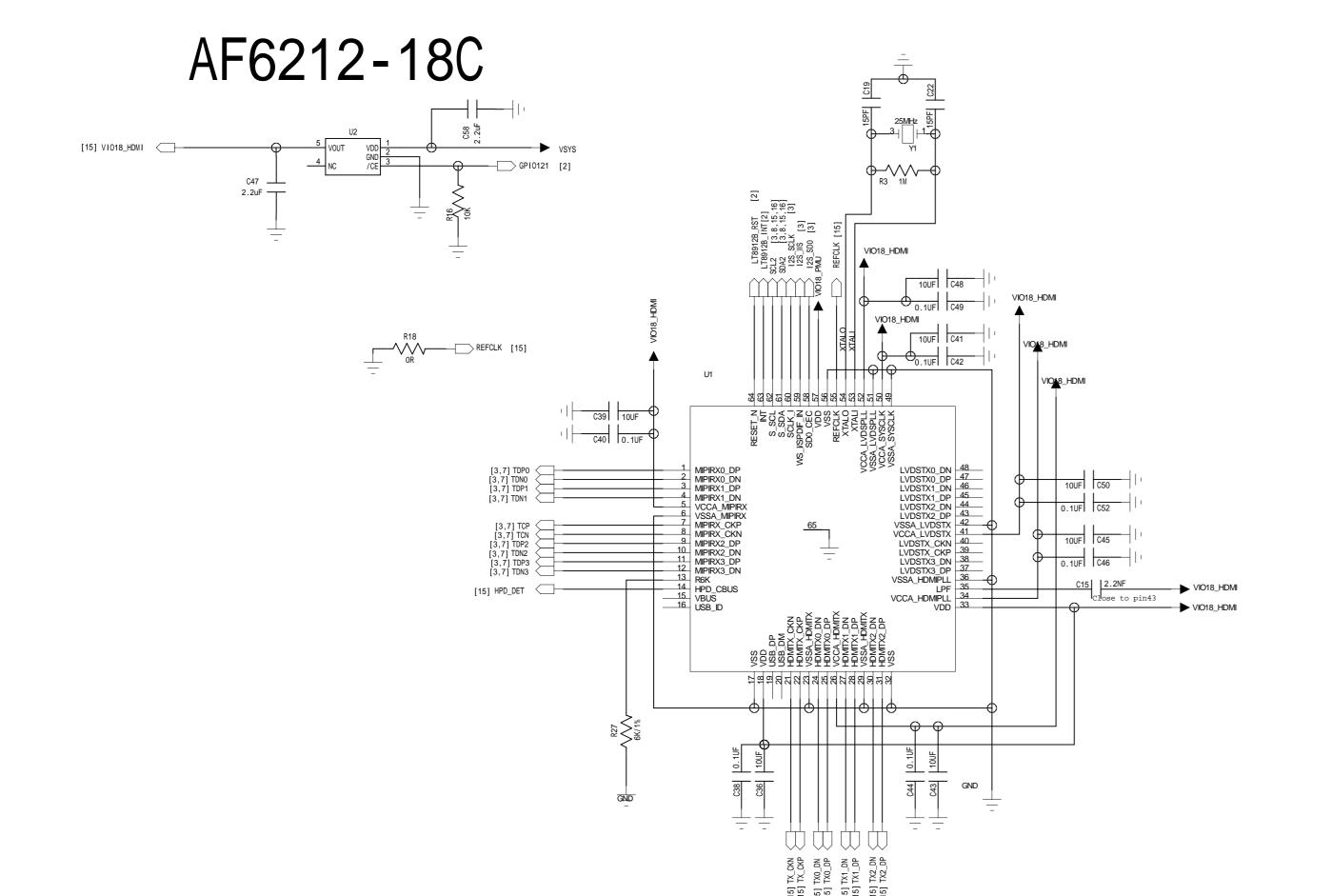
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 4G-IOT\_V1.1

 Size A1
 Page Name
 USB-HUB/POWER
 REV 1.1

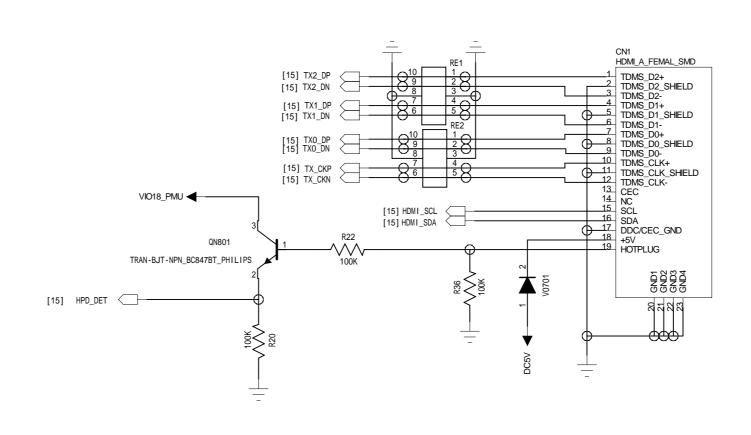
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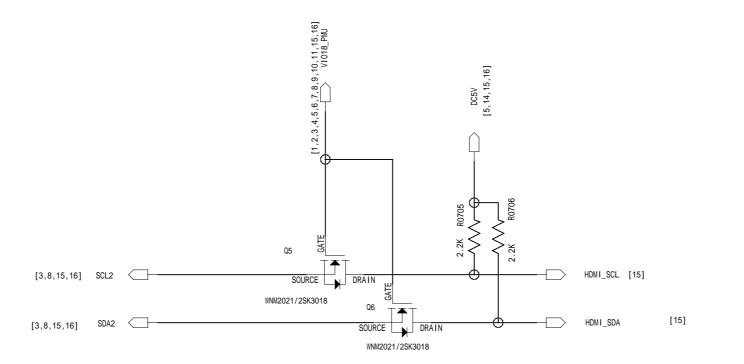
REVISION RECORD

LTR ECO NO: APPROVED: DATE:



## DC5V 由给主板供电的接口供电





| Xunic      | ng Software              |       |    |    |           |
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| Design Nam | e 4G-IOT_V1.1            |       |    |    |           |
| Size A1    | Page Name <b>HDMI</b>    |       |    |    | REV<br>1. |
| Date: Wed  | nesday , March 21 , 2018 | Sheet | 15 | of | 16        |

