

(including label & variable names) for use in Pass 2 (deal with forward ref.).

- perform some processing of assembler directives (these can affect address assignment).

• Pass 2 (assemble instruc<sup>n</sup>'s & generate obj. pgm):

- assemble instruc<sup>n</sup>'s (translate opcode & look up addresses)
- generate data values def. by BYTE, WORD.
- perform processing of assembler dir. not done in Pass 1.
- write the obj. pgm. to the assembly listing.

\* Main DS:

- Oper<sup>n</sup> code table (OPTAB): table used to lookup mnemonic opcodes & their m.l. equivalent.
- Symbol table (SYMTAB): store values (addr.) assigned to labels
- Loc<sup>n</sup> counter (LOCCTR): a var. helps in the assignment of addresses.
- LOCCTR - (i) initialize with beg. addr. in START stmt.  
(ii) after each source stmt. is processed, length of assembled instruc<sup>n</sup>. / data area to be gen. <sup>ex: BYTE, RESW etc.</sup> is added to LOCCTR.
- (iii) ∴, when we reach a label in the source pgm., current value of LOCCTR



## \* OP TAB -

- Content - mapping b/w mnemonic opcode & their machine code, also include the instruc<sup>n</sup> format, available addressing modes & length info.
- Characteristics - Static table. The content will never change.
  - info. is predefined when the assembler is written.
- Implement<sup>n</sup> - hash table with opcode as key.
  - it provide fast retrieval \* with min. search.
- Pass 1 - look up & validate mnemonics in s. form.
- Pass 2 -

## \* SYMTAB -

- Chara:
  - dynamic table (symb. may be inserted / deleted / searched in the table).
- Content:
  - label name & val.
  - flags.
- Pass 1 - fill labels & assigned addr. (from LOC TR) as and when they are encountered.
- Pass 2 - symbols used as operands are looked up to obtain their addresses for the assembled instruc<sup>n</sup>.
- Both passes of the assembler can read the org. s. form as 4p.
- Exam info - should be comm. b/w 2 passes.



(LOCCTR value, error flag for stmts.)

Pass 1 writes an intermediate file.  
ip for Pass 2.

\* (omitted) Pass 1 of assembler;

(. in the label field  
⇒ comment)

Pass 1:

begin

read first ip line

if OPCODE = 'START' THEN

begin

save #{OPERAND} as starting address.

initialize LOCCTR to " "

write line to intermediate file.

read next ip line

end { if START }

else.

initialize LOCCTR to 0

while OPCODE ≠ 'END' do

begin

if this is not a comment line then

begin

if there is a symbol in SYMTAB field  
then

begin

search SYMTAB for label.

if found then

set error flag {duplicate symbol}

else

insert { LABEL, LOCCTR } into SYMTAB

end { if symbol }