Accelerating AI with Semiconductor RTL Frontend Services

VENUE: 673 South Milpitas Blvd,

Milpitas, CA 95035, USA.

OBJECTIVE:

Provide participants with practical insights and hands-on experience in semiconductor Register Transfer Level (RTL) frontend services tailored for emerging AI markets and applications.

AGENDA:

- 1. Registration and Networking (9:00 AM 9:30 AM)
 - ➤ Registration Check-in
 - Networking Coffee Session
- 2. Opening Session (9:30 AM 10:00 AM)
 - Welcome and Introduction to the Workshop
 - > Overview of AI Trends and Semiconductor's Role
- 3. Understanding RTL Frontend Services in AI (10:00 AM 11:00 AM)
 - ➤ Importance of RTL in AI Chip Design
 - Overview of RTL Frontend Services
 - ➤ Key Considerations for AI-Specific RTL Design
- 4. Challenges and Solutions (11:00 AM 12:00 PM)
 - ➤ Identifying Challenges in AI-Specific RTL Design
 - Strategies and Innovations in Overcoming Challenges
 - Case Studies and Best Practices
- 5. Coffee Break and Networking (12:00 PM 12:15 PM)

- 6. Hands-on Workshop: RTL Design for Al (12:15 PM 1:15 PM)
 - Interactive Session with Design Tools
 - ➤ Step-by-Step RTL Design Exercises
 - Q&A with Workshop Facilitators
- 7. Networking Lunch (1:15 PM 2:15 PM)
- 8. Industry Panel: Real-world Applications and Insights (2:15 PM 3:15 PM)
 - Panelists from Semiconductor Companies and AI Solution Providers
 - Sharing Experiences in Implementing RTL for AI
 - Panel Discussion and Audience Q&A
- 9. Tea Break and Networking (3:15 PM 3:30 PM)
- 10. Emerging Trends and Future Directions (3:30 PM 4:30 PM)
 - Latest Innovations in RTL Frontend Services
 - ➤ Industry Trends Shaping the Future
 - > Interactive Discussion on Future Directions
- 11. Closing Remarks and Certificates (4:30 PM 5:00 PM)
 - Summary of Key Takeaways
 - Thanking Participants, Speakers, and Sponsors
 - Certificate Distribution

MATERIALS:

- Workshop guide with detailed agenda and exercises
- > Presentation slides for each session
- ➤ Hands-on workshop materials
- Certificates of participation

We welcome sponsors as part of this workshop