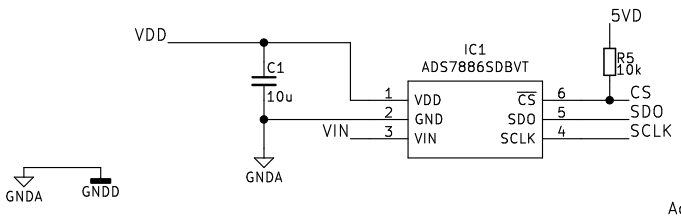
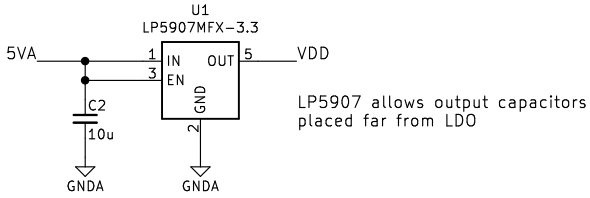
ADC



Connect grounds at ADC

Accepts 5V digital input on 3.3V VDD

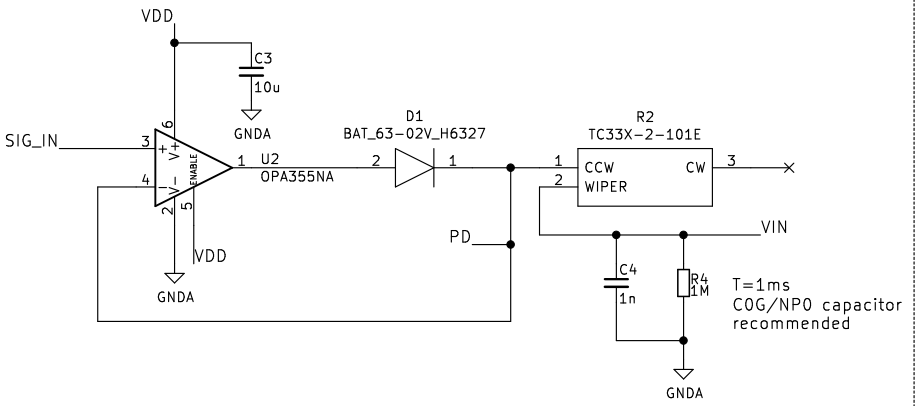
POWER



LP5907 allows output capacitors placed far from LDO

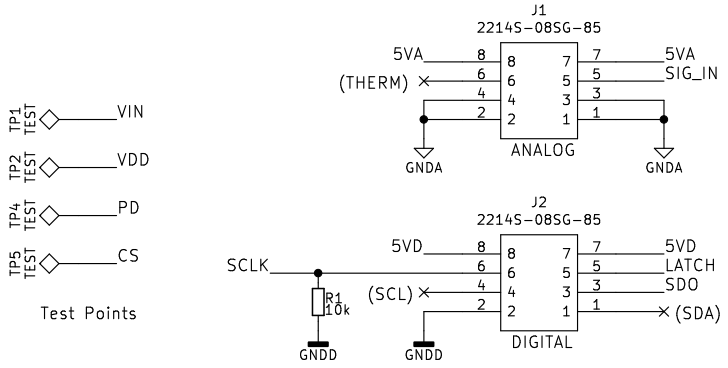
Boring LDO internally set to 3.3V output.
(the LP5907 is pretty cool but the implementation is boring)

ANALOG INPUT



Peak detect with adjustable Rs

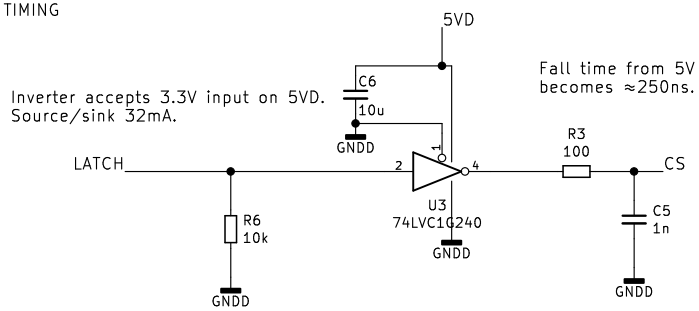
INTERFACE



Some spots around the board that might be useful for debugging but hard to reach otherwise.

"Not connected" pins are I2C.

CS TIMING



CS is delayed inverted latch.

Notes:
The ADC itself pulls 2mA.
The Pi has configurable pull up/down resistors. As a result, no output resistors are included in this design.
Johnson noise equivalent to the LSB would be >1MΩ.

Rev Notes:
Added compatiblity for 1G14 and 1G240 inverters

Sheet: / File: ads7886.sch		
Title: ADS7886 Mezzanine Board		
Size: A4	Date: 2/24/20	**Rev:** 0.5
KiCad E.D.A. kicad (5.1.4)-1		Id: 1/1