

Bansilal Ramnath Agarwal Charitable Trust's

# **Vishwakarma Institute of Information Technology, Pune-48**

(An Autonomous Institute affiliated to Savitribai Phule Pune University)



## **Curriculum for S.Y.B.Tech. (Pattern 2018)**

## **Department of Information Technology**



Bansilal Ramnath Agarwal Charitable Trust's  
**Vishwakarma Institute of Information Technology, Pune-48**  
(An Autonomous Institute affiliated to Savitribai Phule Pune University)  
**Department of Information Technology**

## Vision and Mission of the Department

- **Vision**

“To create professionally competent and globally acceptable IT engineers with social awareness”.

- **Mission**

- Educating budding engineers for, **industry, academia, research** and **entrepreneurial** pursuit through rigorous implementation of IT curriculum
- Inculcating IT skills to develop **innovative solutions** relevant to **global issues**
- **Imparting values** to practice social and **professional ethics**.

## Program Specific Outcomes (PSOs)

At the end of program, students should be able to

- **PSO a:** An ability to understand, analyze and develop computer programs in the areas related to algorithms, web development and database management
- **PSO b:** An ability to apply knowledge of software engineering principles and practices for multidisciplinary applications to meet the needs of the industry and society

## Program Outcomes (POs)

At the end of program, students should be able to:

1. **Engineering knowledge:** Apply the knowledge of mathematics, science, engineering fundamentals and an engineering specialization to the solution of complex engineering problems.
2. **Problem analysis:** Identify, formulate, review research literature and analyze complex engineering problems reaching substantiated conclusions using first principles of mathematics, natural sciences and engineering sciences.
3. **Design/development of solutions:** Design solutions for complex engineering problems and design system components or processes that meet the specified needs with appropriate consideration for the public health and safety and the cultural, social and environmental considerations.
4. **Conduct investigations of complex problems:** Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions.



5. **Modern tool usage:** Create, select and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modeling to complex engineering activities with an understanding of the limitations.
6. **The engineer and society:** Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal and cultural issues and the consequent responsibilities relevant to the professional engineering practice.
7. **Environment and sustainability:** Understand the impact of the professional engineering solutions in societal and environmental contexts and demonstrate the knowledge of, and need for sustainable development.
8. **Ethics:** Apply ethical principles and commit to professional ethics and responsibilities and norms of the engineering practice.
9. **Individual and team work:** Function effectively as an individual and as a member or leader in diverse teams and in multidisciplinary settings.
10. **Communication:** Communicate effectively on complex engineering activities with the engineering community and with society at large, such as, being able to comprehend and write effective reports and design documentation, make effective presentations and give and receive clear instructions.
11. **Project management and finance:** Demonstrate knowledge and understanding of the engineering and management principles and apply these to one's own work, as a member and leader in a team, to manage projects and in multidisciplinary environments.
12. **Life-long learning:** Recognize the need for and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change.



Bansilal Ramnath Agarwal Charitable Trust's

**Vishwakarma Institute of Information Technology, Pune-48**

(An Autonomous Institute affiliated to Savitribai Phule Pune University)

**Department of Information Technology**

# S.Y.B.Tech.

## 2018 Pattern

## Syllabus Structure



Bansilal Ramnath Agarwal Charitable Trust's  
**Vishwakarma Institute of Information Technology, Pune-48**  
 (An Autonomous Institute affiliated to Savitribai Phule Pune University)  
**Department of Information Technology**

**Second Year B. Tech. Information Technology (SYBT) - Semester III (Pattern 2018)**

Course Code	Course Title	Course Type	Teaching Scheme			Examination Scheme					Total	Credits
			L	T	P	CIE	ISE	SCE	ESE	PR/OR/TW		
ITUA21181	Analog and Digital Electronics	TH	3	1	-	20	30	20	30	25	125	4
ES21182IT	Discrete Mathematics	TH	3	-	-	20	30	20	30	-	100	3
ITUA21183	Data Communication	TH	3	-	-	20	30	20	30	-	100	3
ITUA21184	Computer Organization	TH	3	-	-	20	30	20	30	-	100	3
ITUA21185	Fundamentals of Data Structures	TH	3	-	-	20	30	20	30	-	100	3
ITUA21186	Lab Practice – I*	CE PR/OR	-	-	6	50	-	-	-	50	100	3
ITUA21187	Object Oriented Programming *	CE	2	-	2	-	-	50	-	50	100	3
M2	Mandatory Course	AU	-	-	-	-			-	-	-	-
	<b>Total</b>		<b>17</b>	<b>1</b>	<b>8</b>	<b>150</b>	<b>150</b>	<b>150</b>	<b>150</b>	<b>125</b>	<b>725</b>	<b>22</b>



Bansilal Ramnath Agarwal Charitable Trust's

**Vishwakarma Institute of Information Technology, Pune-48**  
(An Autonomous Institute affiliated to Savitribai Phule Pune University)  
**Department of Information Technology**

Theory: 1Hr. = 1 Credit, Practical: 2 Hrs. = 1 Credit, #1 hr. = 1 Credit, Audit Course: No Credits

\*These Courses have PR/OR.

(Lab practice I components are (FDS-4hrs/week, DC-2 hrs /week)

**Mandatory Courses:** Environmental Sciences, Induction training, Indian Constitution, Essence of Indian Traditional Knowledge.

**BoS Chairman**

**Dean Academics**

**Director**



Bansilal Ramnath Agarwal Charitable Trust's  
**Vishwakarma Institute of Information Technology, Pune-48**  
 (An Autonomous Institute affiliated to Savitribai Phule Pune University)  
**Department of Information Technology**

**Second Year B. Tech. Information Technology (SYBT) - Semester IV (Pattern 2018)**

Course Code	Course Title	Course Type	Teaching Scheme			Examination Scheme					Total	Credits
			L	T	P	CIE	ISE	SCE	ESE	PR/OR/TW		
ES22181IT	Engineering Mathematics – III	TH	3	1	-	20	30	20	30	25	125	4
ITUA22182	Data Structure and Files	TH	3	-	-	20	30	20	30	-	100	3
ITUA22183	Computer Networks	TH	3	-	-	20	30	20	30	-	100	3
ITUA22184	Software Engineering	TH	3	-	-	20	30	20	30	-	100	3
ES22185IT	Social Science & Engg. Eco.	TH	3	-	-	20	30	20	30	-	100	3
ITUA22186	Lab Practice – II*	CE PR/OR	-	-	6	50	-	-	-	50	100	3
ITUA22187	IT Workshop in Java*	CE	2	-	2	-	-	50	-	50	100	3
ITUA22188	Cyber Security	CE	2	-	-	50	-	50	-	-	100	2
M2	Mandatory Course	AU	-	-	-	-	-	-	-	-	-	-
	<b>Total</b>		<b>19</b>	<b>1</b>	<b>8</b>	<b>200</b>	<b>150</b>	<b>200</b>	<b>150</b>	<b>125</b>	<b>825</b>	<b>24</b>



Bansilal Ramnath Agarwal Charitable Trust's

**Vishwakarma Institute of Information Technology, Pune-48**  
(An Autonomous Institute affiliated to Savitribai Phule Pune University)  
**Department of Information Technology**

Theory: 1Hr. = 1 Credit, Practical: 2 Hrs. = 1 Credit, #1 hr. = 1 Credit, Audit Course: No Credits

\*These Courses have PR/OR.

Lab practice components are (DSF - 4hrs/week, CN-2 hrs / week)

**Mandatory Courses:** Environmental Sciences, Induction training, Indian Constitution, Essence of Indian Traditional Knowledge

**BoS Chairman**

**Dean Academics**

**Director**





Bansilal Ramnath Agarwal Charitable Trust's

**Vishwakarma Institute of Information Technology, Pune-48**

(An Autonomous Institute affiliated to Savitribai Phule Pune University)

**Department of Information Technology**

**S.Y. B.Tech**

**2018 Pattern**

**Semester - I**

**Syllabus Curriculum**



Bansilal Ramnath Agarwal Charitable Trust's  
**Vishwakarma Institute of Information Technology, Pune-48**  
(An Autonomous Institute affiliated to Savitribai Phule Pune University)  
**Department of Information Technology**

**Analog and Digital Electronics (ITUA21181)**

Teaching Scheme	Examination Scheme						
Credits: 4 Lecture (L): 3 hrs./week Tutorial (T): 1 hr./week	CIE	ISE	SCE	ESE	PR/OR	TW	Total
	20	30	20	30	-	25	125
<b>Prerequisite course(s):</b> Basic Electronics Engineering							
<b>Course Objective(s):</b> <ul style="list-style-type: none"><li>• To learn basic digital circuit design techniques.</li><li>• To study the implementation of digital circuits using combinational logic.</li><li>• To explain and implement circuits using sequential logic.</li><li>• To illustrate the concept of PLD's &amp; ASM.</li><li>• To show the implementation of digital circuits using VHDL.</li><li>• To explain the basics of Logic Families.</li></ul>							
<b>Course Outcomes:</b> Upon completion of the course, students will be able to <ol style="list-style-type: none"><li>1. Simplify Boolean algebraic expressions for designing digital circuits using K-Maps. (Analyzing)</li><li>2. Apply digital concepts in designing combinational circuits. (Applying)</li><li>3. Apply digital concepts in designing sequential circuits. (Applying)</li><li>4. Design digital circuits using PLA and PAL. (Creating)</li><li>5. Develop digital circuits using VHDL. (Creating)</li><li>6. Design and implement Mini digital circuits. (Creating)</li></ol>							
<b>Unit I: Number System and Logic Minimization Techniques</b>							
<b>Number System:</b> Introduction, Binary number System, Sign-Magnitude representation, One's and Two's complement representation, Binary arithmetic, 2's complement arithmetic, Octal number System, Hexadecimal number System, Basic theorems and properties. <b>Logic minimization:</b> Representation of truth-table, Sum of Product (SOP) form, Product of Sum (POS) form, Simplification of logical functions, Minimization of SOP and POS forms using K-Maps up to 4 variables and Quine-McCluskey Technique, realization of logic gates.							
<b>Unit II - Combinational Logic</b>							



**Design of Combinational Logic:** Code converter - BCD, Excess-3, Gray code, Binary Code. Half-Adder, Full Adder, Half Subtractor, Full Subtractor, Binary Adder (IC 7483), BCD adder, Look ahead carry generator, Multiplexers (MUX): MUX (IC 74153, 74151), MUX tree, DE multiplexers (DEMUX)-Decoder. (IC 74138, IC 74154). DMUX Tree, Implementation of SOP and POS using MUX, DMUX, Comparators, Parity generators and Checker, One bit, Two bit, 4-bit Magnitude Comparator

### **Unit III: Sequential Logics**

**Flip-flop:** SR, JK, D, T; Preset & Clear, Master and Slave Flip Flops, Truth Tables and Excitation tables, Conversion from one type to another type of Flip Flop. **Registers:** Buffer register, shift register (SISO, SIPO, PISO & PIPO), Applications of shift registers.

### **Unit IV: Counters**

**Counters:** Asynchronous counter. Synchronous counter, ring counters, Johnson Counter, Modulus of the counter (IC 7490). **Synchronous Sequential Circuit Design:** Models – Moore and Mealy, State diagram and State Tables, Design Procedure, Sequence generator and detector. **Asynchronous Sequential Circuit Design:** Difference with synchronous circuit design, design principles and procedure, applications.

### **Unit V: Logic Families**

**Classification of logic families:** Unipolar and Bipolar Logic Families, Characteristics of Digital ICs: Speed, power dissipation, figure of merits, fan-out, Current and voltage parameters, Noise immunity, operating temperature range, power supply requirements. **Transistor-Transistor Logic:** Operation of TTL, TTL characteristics. **CMOS:** CMOS Inverter, CMOS characteristics, CMOS configurations-Wired Logic, Open drain outputs. **Interfacing:** TTL to CMOS and CMOS to TTL. **Digital Circuit Implementation using IOT:** Introduction to soldering techniques and its types, Mounting of electronic components on PCB. Introduction and applications of Raspberry pi and Arduino architecture.

### **Unit VI: Introduction to PLD's & VHDL**

**Introduction to PLD's:** - PAL, PLA, Applications of PLAs to implement combinational and sequential logic circuits, FPGA and CPLD. **VHDL:** Introduction to HDL, Data Objects & Data Types, Attributes., VHDL- Library, Design Entity, Architecture, Modeling Styles, Concurrent and Sequential Statements. Design **Examples:** VHDL for Combinational Circuits-Adder, MUX, VHDL for Sequential Circuits, Synchronous and Asynchronous Counter.

### **Textbooks:**

1. R. P. Jain, "Modern Digital Electronics", 3rd Edition, Tata McGraw-Hill, 2003, ISBN0 – 07 – 049492 – 4.
2. J. Bhaskar, "VHDL Primer" 3rd Edition, Pearson Edition.



Bansilal Ramnath Agarwal Charitable Trust's  
**Vishwakarma Institute of Information Technology, Pune-48**  
(An Autonomous Institute affiliated to Savitribai Phule Pune University)  
**Department of Information Technology**

3. Stephen Brown, Zvonko Vranesic, "Fundamentals of Digital Logic with VHDL Design", McGraw Hill, ISBN-13:978-1-25-902597-6.
4. G.K.Kharate, "Digital Electronics", Oxford University Press.3.

**Reference Books:**

1. John Yarbrough, "Digital Logic applications and Design" Thomson.
2. Flyod "Digital Principles", Pearson Education.
3. Malvino, D.Leach "Digital Principles and Applications", 5th edition, Tata Mc- Graw Hill.
4. Douglas L. Perry, "VHDL Programming by Example", Tata McGraw Hill.
5. G. K. Kharate, "Digital Electronics", Oxford University Press.

**Tutorial List: Analog and Digital Electronics laboratory**

**Course Objectives:**

- To understand the functionality and design of Combinational and Sequential Circuits.
- To learn designing and implementing digital circuits using VHDL.

**Course Outcomes:**

After completion of the course, student will be able to

1. Realize and simplify Boolean Algebraic assignments for designing & implementing Sequential and Combinational digital circuits using K Maps.
2. Design simple digital systems using VHDL.

1	Number system conversion and 2's complement arithmetic.
2	Realize Full Adder and Sub tractor using a) Basic Gates and b) Universal Gates.
3	Design and implement Code converters-Binary to Grey and BCD to Excess-3.
4	Design and Realization of BCD Adder using 4-bit Binary Adder (IC 7483).
5	Design of Ripple Counter using JK-Flip Flops.
6	Design 3 bit Synchronous Up/Down Counter using JK-Flip Flop.
7	Design and implement Mod -N counter using IC-7490.
8	Simulation of - Full adder using behavioural modelling style of VHDL.
9	Simulation of - 4:1 MUX using data flow modelling style of VHDL.
10	Mini project: Nowadays digital electronics deals with the logic gates, flip-flops, CMOS – the foundation for modern computers and digital communications. Students are expected to apply their knowledge to design and develop a simple digital system as a part of this project.



**Discrete Mathematics (ES21182IT)**

Teaching Scheme	Examination Scheme						
Credits: 3 Lecture (L): 3 hrs./week	CIE	ISE	SCE	ESE	PR/OR	TW	Total
	20	30	20	30	-	-	100
<b>Prerequisite course(s):</b> Basic Algebra							
<b>Course Objective(s):</b> <ul style="list-style-type: none"><li>To study basic of logic and set theory</li><li>To understand notion of functions and relations</li><li>To study fundamental principles of graph</li><li>To study fundamental principles of trees elementary combinational processes</li><li>To study fundamental principles of probability theory</li><li>To study basic of logic and set theory</li></ul>							
<b>Course Outcomes:</b> <p>Upon completion of the course, students will be able to</p> <ol style="list-style-type: none"><li>Demonstrate use of logical arguments, proof techniques and set theory principles</li><li>Determine type, properties and solution of relations and functions</li><li>Solve the problems using graph methods and algorithms</li><li>Apply tree models and methods to obtain solutions of applications involving searching, prefix code and vertex connectivity</li><li>Apply principles of counting to obtain solution to counting problems.</li><li>Apply probability theory, principles and distributions in problem</li></ol>							
<b>Unit I: Mathematical Logic and Set Theory</b>							
Propositional Logic, Applications of Propositional Logic, Propositional Equivalences, Predicates and Quantifiers, Nested Quantifiers, Rules of Inference, Introduction to Proofs, Proof Methods and Strategy Sets, Combination of sets, Venn Diagrams, Finite and Infinite sets, Uncountably infinite sets, Principle of inclusion and exclusion, multisets.							
<b>Unit II: Functions and Relations</b>							
Functions, Composition of functions, Invertible functions, Discrete Numeric functions and Generating functions, Job scheduling Problem. Properties of Binary Relations, Closure of relations, Warshall's algorithm, Equivalence Relations and Partitions, Partial ordering relations and Lattices, Chains and Anti-chains. Recurrence Relations: Recurrence Relation, Linear Recurrence Relations with constant Coefficients, Homogeneous Solutions, Total solutions, Solutions by the method of generating functions.							



**Unit III: Graphs**

Basic terminology, Representation of graph in computer memory, Multigraphs and Weighted graphs, Subgraph, Isomorphic graph, Complete, Regular and Bipartite graphs, Operation on graph, Paths and Circuits, Hamiltonian and Euler paths and circuits, Shortest path in weighted Graph (Dijkstra's algorithm), Factors of a graph, Planar graph and Travelling salesman problem, Graph coloring.

**Unit IV: Trees**

Trees, rooted trees, Path length in rooted trees, Prefix codes and optimal prefix codes, Binary search trees, Tree traversals, Spanning trees, Fundamental circuits and cut set, Minimal spanning trees, Kruskal's and Prim's algorithms for minimum spanning tree, The Max flow –Min cut theorem (transport network).

**Unit V: Counting**

Permutations and Combinations: Rule of sum and product, Pigeonhole principle, Permutations, Combinations, Binomial Coefficients and identities, Algorithms for generation of Permutations and Combinations.

**Unit VI: Discrete Probability**

Discrete Probability, Conditional Probability, Bayes' Theorem, Random variable, Random process, Mean, Mode, Variance, Probability distributions: Binomial trials and Distribution, Binomial probability distribution, Poisson probability distribution, Gaussian probability distribution.

**Textbooks:**

1. Kenneth H. Rosen, "Discrete Mathematics and its Applications", 6th edition, McGraw-Hill, 2007. ISBN 978-0-07-288008-3.
2. C. L. Liu and D. P. Mohapatra, "Elements of Discrete Mathematics", Second Edition, TataMcGraw-Hill, 2008, ISBN 10:0-07-066913-9.

**Reference Books:**

1. R. Johnsonbaugh, "Discrete Mathematics", 5th Edition, Pearson Education, 2001 ISBN 81 – 7808 – 279 – 9.
2. B. Kolman, R. Busby and S. Ross, "Discrete Mathematical Structures", 4<sup>th</sup> Edition, Pearson Education, 2002, ISBN 81-7808-556-9
3. N. Deo, "Graph Theory with application to Engineering and Computer Science", Prentice Hall of India, 1990, 0 – 87692 – 145 – 4
4. Eric Gossett, "Discrete Mathematics with proof", 2nd edition, Wiley Student Edition, ISBN: 978-81-265-2758-8



Bansilal Ramnath Agarwal Charitable Trust's  
**Vishwakarma Institute of Information Technology, Pune-48**  
(An Autonomous Institute affiliated to Savitribai Phule Pune University)  
**Department of Information Technology**

**Data Communication (ITUA21183)**

Teaching Scheme	Examination Scheme						
Credits: 3 Lecture (L): 3 hrs./week	CIE	ISE	SCE	ESE	PR/OR	TW	Total
	20	30	20	30	-	-	100
<b>Prerequisite course(s):</b> Nil							
<b>Course Objective(s):</b> <ul style="list-style-type: none"><li>• To understand the concepts of data communication.</li><li>• To learn the transmission media and its use.</li><li>• To study the functions of TCP/IP and OSI layers.</li></ul>							
<b>Course Outcomes:</b> <p>Upon completion of the course, students will be able to</p> <ol style="list-style-type: none"><li>1. Differentiate the analog and digital signals and their characteristics.</li><li>2. Categories various modulation and multiplexing techniques.</li><li>3. Identify and use various transmission media in various network scenarios.</li><li>4. Understand OSI layers, TCP/IP protocol stack and describe various network topologies.</li><li>5. Exercise error correction techniques.</li><li>6. Compare and describe multiple access methods.</li><li>7. Work in a group to understand, prepare and present a given case study.</li></ol>							
<b>Unit I: Fundamentals of Signals</b>							
Analog and Digital: Analog and Digital Data, Analog and Digital Signals, Periodic and Non-Periodic Signal Periodic Analog Signals: Sine Wave, Phase, Wavelength, Time and Frequency Domains, Composite Signals Bandwidth Digital Signals: Bit Rate, bit Length, Digital Signal as a Composite Analog Signal, Transmission of Digital Signals Transmission Impairment: Attenuation, Distortion, Noise Data Rate Limits: Noiseless Channel: Nyquist Bit Rate, Noisy Channel: Shannon Capacity Performance: Bandwidth, Throughput, Latency (delay)							



**Unit II: Modulation and Multiplexing Techniques**

Digital-to-digital Conversion: Line Coding, Line Coding Schemes, Block Coding, Scrambling, Analog to digital Conversion: Pulse Code Modulation (PCM), Delta Modulation (DM), ADM Transmission modes: parallel transmission, serial transmission, Analog-to-analog Conversion: Amplitude Modulation, Frequency Modulation, Phase Modulation, Multiplexing: Frequency-Division Multiplexing (FDM), Wavelength-Division Multiplexing Synchronous Time-Division Multiplexing, Statistical Time-Division Multiplexing Spread Spectrum: Frequency Hopping Spread Spectrum (FHSS), Direct Sequence Spread Spectrum.

**Unit III: Transmission Media and Switching**

Guided Media: Twisted-Pair, Coaxial and Fiber-Optic Cable. Unguided Media: Radio Waves, Microwaves, Infrared. (RJ45, RJ11, BNC, SC/ST etc.) Circuit-switched Networks: Three Phases, Packet switching: Datagram networks, Virtual circuit networks  
Brief introduction of Digital Subscriber Line: ADSL, HDSL, SDSL, VDSL (DMT).

**Unit IV: Layer Model and Topologies**

The OSI Model: Layered Architecture, peer-to-peer Processes, Encapsulation of Layers in the OSI Model, TCP/IP Protocol Suite, LAN, MAN, WAN, Topologies like star, mesh, bus, hybrid etc. Addressing: Physical & logical Addresses, Connecting devices: Hubs, Repeaters, Active hubs, Bridges, Switches, Routers, Gateway.

**Unit V: Error Control and Data Link Control**

Types of errors: Redundancy, detection versus correction, forward error correction versus retransmission, Block coding: error detection, error correction, CRC, polynomial, checksum, hamming code, hamming distance DLC Services: Framing, Flow and error control DLL Protocols: Simple protocol, stop n wait, go back to N, Selective repeat HDLC Protocol: configurations and transfer modes, frames, control field. Point-to-point Protocol (PPP): Framing, Transition Phases, Multiplexing, Multilink PPP.

**Unit VI: Multiple Access**

Random access: Aloha, Slotted Aloha, Carrier Sense Multiple Access (CSMA), Carrier Sense Multiple Access with Collision Detection (CSMA/CD), Carrier Sense Multiple Access with Collision Avoidance (CSMA/CA).  
Controlled access: reservation, polling, token passing. Channelization: Frequency Division Multiple Access (FDMA), Time-Division Multiple Access (TDMA), Code Division Multiple Access (CDMA).





Bansilal Ramnath Agarwal Charitable Trust's

## **Vishwakarma Institute of Information Technology, Pune-48**

(An Autonomous Institute affiliated to Savitribai Phule Pune University)

### **Department of Information Technology**

<b>Textbooks:</b>
1. Fourauzan B., "Data Communications and Networking", 5th edition, McGraw-Hill Publications 2. Stallings William., "Data and Computer Communications", Sixth Edition, Prentice Hall of India
<b>Reference Books:</b>
1. Andrew S. Tenenbaum," Computer Networks", Pearson 2. Douglas E. Comer, "Computer Networks and Internets", Pearson Education.



Bansilal Ramnath Agarwal Charitable Trust's  
**Vishwakarma Institute of Information Technology, Pune-48**  
(An Autonomous Institute affiliated to Savitribai Phule Pune University)  
**Department of Information Technology**

**Computer Organization (ITUA21184)**

Teaching Scheme	Examination Scheme						
	CIE	ISE	SCE	ESE	PR/OR	TW	Total
Credits: 3 Lecture (L): 3 hrs./week	20	30	20	30	-	-	100
<b>Prerequisite course(s):</b> Fundamentals of Programming Languages-I & II and Basics of Electronics Engineering.							
<b>Course Objective(s):</b> <ul style="list-style-type: none"><li>• To understand the structure, function and characteristics of computer systems</li><li>• To understand the design of the various functional units and components of digital computers</li><li>• To identify the elements of modern instructions sets and explain their impact on processor design,</li><li>• To explain the function of each element of a memory hierarchy, identify and compare different methods for computer Input Output,</li><li>• To compare simple computer architectures and organizations.</li></ul>							
<b>Course Outcomes:</b> Upon completion of the course, students will be able to <ol style="list-style-type: none"><li>1. Explain processor structure &amp; its functions.</li><li>2. Solve problems based on computer arithmetic.</li><li>3. Obtain knowledge about micro-programming of a processor.</li><li>4. Discover Instruction level processing</li><li>5. Understand concept related to memory and IO organization.</li><li>6. Understand concepts related to multicore Computers.</li></ol>							
<b>Unit I: Computer Evolution and Performance Measures</b>							
Computer Organization and Architecture, Designing for Performance (performance assessment), Evolution of Intel x86 Architecture, A brief history of computers, CISC vs RISC: Speed, Throughput. A top level view of Computer function and interconnection- Computer Components, Computer Function, Interconnection structure, bus interconnection.							
<b>Unit II: Computer Arithmetic</b>							
The Arithmetic and Logic Unit, Addition and subtraction of signed numbers, Multiplication of positive numbers, Signed operand multiplication, Booths algorithm, Fast multiplication, Integer division, Floating point representation and operations – IEEE standard.							



**Unit III: The Control Unit**

Instruction level parallelism and superscalar processors - Super scalar verses super pipelined, constraints, Design Issues- Instruction level and machine parallelism, Instruction issue policy, Register renaming, Machine parallelism, Hardwired control, Micro-programmed control- micro instructions, Micro program sequencing, Wide branch addressing, Microinstruction with next address field, Prefetching microinstructions and emulation.

**Unit IV: Processor Structure**

Architecture of 8086, Processor organization, Register organization- User visible registers, control and status registers, Instruction Cycle- The indirect cycle and data flow, Instruction pipelining- pipelining strategy, Types of operands, Data transfer, arithmetic, logical, Conversion, input-output, addressing modes and formats - Immediate, Direct, indirect, register, register indirect, Displacement and stack, Instruction Formats- length, variable length instructions and pseudo codes.

**Unit V: Computer Memory System**

Characteristics of memory system, The memory hierarchy, Cache Memory- Cache memory principles, Elements of cache design- Cache address, Size, Mapping functions, Replacement algorithms, Write policy, Line size, Number of cache, One level and two level cache, Performance characteristics of two level cache- locality & operations, Case Study- pentium4 cache organization.

**Unit VI: Multicore Processors**

Why Multicore? Architecture: Multicore & Multiprocessor, Hardware Performance Issues, Software Performance Issues, Multicore Organization, Applications, Advantages and challenges, Examples, Intel x86 Multicore Organizations, Recent trends in Multicore.

**Textbooks:**

1. W. Stallings, Computer Organization and Architecture: Designing for performance, Pearson Education/ Prentice Hall of India, 2003, ISBN 978-93-325-1870-4, 7th Edition.
2. Zaky S, Hamacher, Computer Organization, 5th Edition, McGraw-Hill Publications, 2001, ISBN- 978-1-25-900537-5, 5th Edition.

**Reference Books:**

1. John P Hays, Computer Architecture and Organization, McGraw-Hill Publication, 1998, ISBN:978-1-25-902856-4, 3rd Edition.
2. Miles Murdocca and Vincent Heuring, Computer Architecture and Organization- an integrated approach, Wiley India Pvt. Ltd, ISBN:978-81-265-1198-3, 2nd Edition.
3. A. Tanenbaum, Structured Computer Organization, Prentice Hall of India, 1991 ISBN: 81 – 203 – 1553 – 7, 4th Edition.



Bansilal Ramnath Agarwal Charitable Trust's

**Vishwakarma Institute of Information Technology, Pune-48**  
(An Autonomous Institute affiliated to Savitribai Phule Pune University)  
**Department of Information Technology**

- |   |
|---|
| <p>4. Patterson and Hennessy, Computer Organization and Design, Morgan Kaufmann Publishers Inc., ISBN 978-0-12-374750-1, 4th Edition.</p> |
|---|



**Fundamentals of Data Structures (ITUA21185)**

Teaching Scheme	Examination Scheme						
Credits: 3 Lecture (L): 3 hrs./week	CIE	ISE	SCE	ESE	PR/OR	TW	Total
	20	30	20	30	-	-	100
<b>Prerequisite course(s):</b> Computer Fundamentals							
<b>Course Objective(s):</b> <ul style="list-style-type: none"><li>• To understand the different ways of data representation.</li><li>• To develop the ability to analyze algorithms.</li><li>• To enable students to write algorithms for solving problems with the help of fundamental data structures.</li><li>• To understand concepts about searching and sorting techniques.</li><li>• To study the implementation and applications of linear data structures.</li></ul>							
<b>Course Outcomes:</b> <p>Upon completion of the course, students will be able to</p> <ol style="list-style-type: none"><li>1. Summarize appropriate constructs of a programming language including pointers and file handling.</li><li>2. Recognize algorithmic foundations and basics of analysis of algorithms.</li><li>3. Select appropriate searching and/or sorting techniques in the application development.</li><li>4. Apply various linear data structures using sequential organization for problem solving and programming.</li><li>5. Apply various linear data structures using linked organization for problem solving and programming.</li><li>6. Use advanced linear data structures stack and queue for problem solving and programming.</li></ol>							
<b>Unit I: Pointers in C and File Handling</b>							
Introduction to Pointers, Dynamic memory allocation, Pointer to pointer, Pointer to single and multidimensional arrays, Array of pointers, String and structure manipulation using pointers, Pointer to functions. Pointer to file structure and basic operations on file, File handling in C.							
<b>Unit II: Introduction to Data Structures and Analysis of Algorithms</b>							
Concept of data, Data object, Data structure, Abstract Data Types (ADT), Concept of primitive and non-primitive, Linear and non-linear, Static and dynamic, Persistent and ephemeral data structures. Use of data structures in coding, <b>Analysis of algorithm:</b> frequency count and its importance in analysis of an algorithm, Time complexity & Space complexity of an algorithm, Big 'O', 'Ω' and 'Θ' notations, Best, Worst and Average case analysis of an algorithm.							



**Unit III: Searching and Sorting Techniques**

Need of searching and sorting, Linear & Binary search, Bubble sort & complexity Analysis, Selection sort & complexity analysis, Insertion Sort & Complexity Analysis, Merge Sort & complexity Analysis, Quick Sort & complexity Analysis.

**Unit IV: Linear Data Structures using sequential Organization**

Concept of Linear data structures and sequential organization, Concept of ordered list, Multidimensional arrays and their storage representation: row major and column major form and address calculation. Representation of sparse matrix using arrays, algorithms for sparse matrix addition, simple and fast transpose, polynomial representation using arrays. Analysis of these algorithms

**Unit V: Linear Data Structures using Linked Lists**

Concept of linked organization, singly linked list, doubly linked list, Circular linked list. Linked list as an ADT. Representation and manipulations of polynomials using linked lists, Comparison of a sequential and linked memory organization, Concept of Generalized Linked List, Representation polynomial using GLL.

**Unit VI: Linear Data Structures-Stack and Queue**

Stacks: Concept of stack, Push and pop operation, Stack implementation using array and linked list, Application of stack for expression conversion and evaluation, Recursion concept and use of internal stack. Concept of queue & its application, Priority queue concepts and operations, Doubly ended queue concepts and operations, Circular queue concepts and operations, Multi-stack and Multi-queue concepts.

**Textbooks:**

1. R. Gilberg, B. Forouzan, "Data Structures: A pseudo code approach with C", Cenage Learning, ISBN 9788131503140.
2. E. Horowitz, S. Sahani, S. Anderson-Freed, "Fundamentals of Data Structures in C"

**Reference Books:**

1. A. Aho, J. Hopcroft, J. Ulman, "Data Structures and Algorithms", Pearson Education, 1998, ISBN-0-201-43578-0
2. Y. Langsam, M. Augenstein and A. Tannenbaum, "Data Structures using C and C++", 2<sup>nd</sup> Edition, Prentice Hall of India, 2002, ISBN-81-203-1177-9
3. J. Tremblay, P. Soresan, "An introduction to data structures with Applications", 2<sup>nd</sup> edition, Tata McGraw-Hill International Editions, 1984, ISBN-0-07-462471-7.



Bansilal Ramnath Agarwal Charitable Trust's  
**Vishwakarma Institute of Information Technology, Pune-48**  
 (An Autonomous Institute affiliated to Savitribai Phule Pune University)  
**Department of Information Technology**

### Lab Practice -I (ITUA21186)

Teaching Scheme		Examination Scheme						
Credits: 3 Practical (P): 6 hrs./week		CIE	ISE	SCE	ESE	PR/OR	TW	Total
		50	-	-	-	50	-	100
Prerequisite course(s):								
Course Objective(s): <ul style="list-style-type: none"><li>To understand the different ways of data representation.</li><li>To understand concepts about searching and sorting techniques.</li><li>To study the implementation and applications of linear data structures.</li></ul>								
Course Outcomes: Upon completion of the course, students will be able to <ol style="list-style-type: none"><li>Summarize appropriate constructs of a programming language including pointers and file handling.</li><li>Select appropriate searching and/or sorting techniques in the application development.</li><li>Apply various linear data structures using sequential and linked organization for problem solving and programming.</li><li>Use advanced linear data structures stack and queue for problem solving and programming.</li></ol>								
PART I-Programming Laboratory								
1	a) In a class of n students, some students like Math, some like science and some like both the subjects. Find the students who like ( i) Either Math or Science (ii) Both Math and Science							
	b) Find the students who like (iii) Math only (iv) Science only							
2	a) Perform matrix operations without pointers.							
	b) Perform matrix operations with pointers.							
3	a) Create a database for inventory system of a shop using array of structures and perform following operations on it: i. Add record ii. Display Database							
	b) In the inventory system database iii. Search record iv. Delete record v. Sort records							
4	a) Create database for employees of an organization using sequential file and perform following operations: i. Display ii. Add records iii. Search record							
	b) Perform following operations on employee database iv. Modify record v. Delete record vi. Sort using Bubble sort							
5	a) Sort the set of strings in ascending order using Insertion sort and descending order by using Selection sort. (Display pass by pass output)							
	b) Search a particular string using binary search with and without recursion.							



**Department of Information Technology**

6	a) Implement Quick Sort / Merge Sort to sort the given list of numbers. Display corresponding list in each pass. <b>(with recursion)</b>
	b) Implement Quick Sort / Merge Sort to sort the given list of numbers. Display corresponding list in each pass. <b>(without recursion)</b>
7	a) Accept conventional matrix and convert it into sparse matrix using structure and Perform <b>addition</b> of two sparse matrices.
	b) Implement simple and fast transpose algorithms on sparse matrix.
8	a) Implement a singly linked list(SLL) with following options i. Insertion of a node at any location ii. display a list
	b) For SLL perform ii. Deletion of a node from any location iv. Display in reverse
9	a) Implement any database using doubly linked list with following options i. Insert a record ii. Display list forward iii. Display list backward
	b) iii. Modify a record iv. Delete a record
10	a) Implement polynomial using CLL and perform i. Addition of Polynomials
	b) Implement polynomial using CLL and perform ii. Multiplication of polynomials and iii. Evaluation of polynomial
11	a) Implement stack ADT using linked list. Write a program for Expression Evaluation using stack.
	b) Write a program for Expression Conversion using stack.
12	Implement a circular Queue.
13	Implement a priority Queue for a list of patients using linked list.
<b>*All assignments to be implemented using C/C++ on Linux platform.</b>	
<b>PART-II Data Communication Laboratory</b>	
1	Network Representation on Network Simulator tool
2	Configuration of initial Switch setting using secure Passwords and SSH
3	Study of DCE, DTE, straight and cross cables
4	Implementation of basic connectivity in a small network.
5	Identify MAC and IP addresses.
6	To examine the ARP table.
7	Implementation of wireless LAN.
8	Study of Basic router configuration and introduction of iOS.
9	To design subnets as per given requirement.
10	Study of campus wide network and prepare a report.





**Object Oriented Programming (ITUA21187)**

Teaching Scheme	Examination Scheme						
Credits: 3 Lecture (L): 2 hrs./week Practical (P): 2 hrs./week	CIE	ISE	SCE	ESE	PR/OR	TW	Total
	-	-	50	-	50	-	100
<b>Prerequisite course(s):</b> Basics of Computer programming.							
<b>Course Objective(s):</b> <ul style="list-style-type: none"><li>• To understand object-oriented concepts such as data abstraction, encapsulation, inheritance, dynamic binding, and polymorphism.</li><li>• To use the object-oriented paradigm in program design.</li><li>• Provide programming insight using OOP constructs.</li><li>• To lay a foundation for advanced programming.</li></ul>							
<b>Course Outcomes:</b> Upon completion of the course, students will be able to <ol style="list-style-type: none"><li>1. Comprehend the concept of OOP.</li><li>2. Apply concept of reusability to solve the problems</li><li>3. Implement programs using generic programming concepts and exception handling</li><li>4. Perform file operations using streams and file concepts.</li></ol>							
<b>Unit I: Classes and Objects</b>							
Need of object oriented programming, Introduction OOP, Procedural Vs. object oriented programming, Principles of OOP, Benefits and applications of OOP, Program structure: namespace, identifiers, variables, constants, enum, operators, typecasting, control structures, object oriented programming language, data types, array and string, class, object, class & data abstraction, class scope & accessing data members, controlling access to members.							
<b>Unit II: Inheritance and Polymorphism</b>							
Base class, derived class, public, private & protected keywords, types of inheritance, ambiguity in multiple inheritance, classes within classes, Polymorphism concept, types of polymorphism, function overloading, operator overloading, Unary operator overloading & binary operator overloading. friend function, static function, this pointer, virtual function, dynamic binding.							
<b>Unit III: Exception Handling &amp; Templates</b>							



Fundamentals, exception handling using try-throw-catch, standard exceptions, User defined exceptions. Generics, Generic programming, Class templates, function templates. Introduction to STL, Components of STL – Containers, Iterators and Algorithms, List, Vector, set, minmax, algorithm header files. Smart pointers concept, shared pointers concept, memory leak problem.

#### **Unit IV: Streams and Files**

Stream concept, File pointers, File I/O classes, File I/O functions, and insertion and Extraction operators in file streams.

#### **Textbooks:**

1. Behrouz A. Forouzan, Richard F. Gilberg, “COMPUTER SCIENCE – A Structred Programming approach using C”, Indian Edition, Thomson, 3rd edition.
2. Bjarne Stroustrup, — The C++ Programming languagel, Third edition, Pearson Education. ISBN 9780201889543

#### **Reference Books:**

1. Kernighan, Ritchie, “The C Programming Language”, Prentice Hall of India
2. Robert Lafore, —Object-Oriented Programming in C++, fourth edition, Sams Publishing, ISBN:0672323087 (ISBN 13: 9780672323089)
3. Herbert Schildt, —C++ The complete referencel, Eighth Edition, McGraw Hill Professional, 2011, ISBN:978-00-72226805
4. E. Balagurusamy-- Object-oriented programming with C++, fourth edition, Mc Hill Professional,2008, ISBN 978-0-07-066907-9

#### **Assignments : Object oriented programming laboratory**

- |   |  |
|---|--|
| 1 | Create a class named weather report that holds a daily weather report with data members day_of_month, hightemp, lowtemp,a mount_rain and amount_snow. Use different types of constructors to initialize the objects. Also include a function that prompts the user and sets values for each field so that you can override the default values. Write a menu driven program in C++ with options to enter data and generate monthly report that displays average of each attribute.  |
| 2 | <p><b>a.</b> Design a class ‘Complex ‘with data members for real and imaginary part. Provide default and parameterized constructors. Write a program to perform arithmetic operations of two complex numbers using operator overloading. Addition and subtraction using friend functions</p> <p><b>b.</b> Design a class ‘Complex ‘with data members for real and imaginary part. Provide default and parameterized constructors. Write a program to perform arithmetic operations of two complex numbers using operator overloading. Multiplication and division using member functions</p> |



3	<p>Design a base class with name, date of birth, blood group and another base class consisting of the data members such as height and weight. Design one more base class consisting of the insurance policy number and contact address. The derived class contains the data members' telephone numbers and driving license number.</p> <p>a. Write a menu driven program to carry out the following operations:</p> <p>i. Build a master table ii. Display iii. Insert a new entry</p> <p>b. Write a menu driven program to carry out the following operations:</p> <p>i. Delete entry ii. Edit iii. Search for a record</p>
4	<p>Create a base class shape with two double type values and member functions to input the data and compute_area() for calculating area of figure. Derive two classes' triangle and rectangle. Make compute_area() as a virtual function and redefine this function in the derived class to suit their requirements. Write a program that accepts dimensions of triangle/rectangle and display calculated area.</p>
5	<p>Write a program in C++ which includes the code for following operations:</p> <p>i. A function to read two double type numbers from keyboard</p> <p>ii. A function to calculate the division of these two numbers</p> <p>iii. A try block to detect and throw an exception if the condition "divide-by-zero" occurs</p> <p>iv. Appropriate catch block to handle the exceptions thrown</p>
6	<p>Write a program in C++ using function/class template to read two matrices of different data types such as integers and floating point values and perform simple arithmetic operations on these matrices separately and display it.</p>
7	<p>a. Write a program in C++ to implement sequential file for students' database and perform following operations on it i. Create Database ii. Display Database iii. Add a record</p> <p>b. Write a program in C++ to implement sequential file for students' database and perform following operations on it i. Delete a record ii. Modify a record</p>



Bansilal Ramnath Agarwal Charitable Trust's

**Vishwakarma Institute of Information Technology, Pune-48**

(An Autonomous Institute affiliated to Savitribai Phule Pune University)

**Department of Information Technology**

# S.Y.B.Tech

## 2018 Pattern

### Semester - II

# Syllabus Curriculum



**Engineering Mathematics-III (ES22181IT)**

Teaching Scheme	Examination Scheme						
Credits: 4 Lecture (L): 3 hrs./week Tutorial (T): 1 hr./week	CIE	ISE	SCE	ESE	PR/OR	TW	Total
	20	30	20	30	-	25	125
<b>Prerequisite course(s):</b> Basics of Derivatives, Integration, Trigonometry, Vector algebra & complex number.							
<b>Course Objective(s):</b> <ul style="list-style-type: none"> <li>• To study the Linear Differential equations and their applications</li> <li>• To impart knowledge of Fourier transform and Z- transform</li> <li>• To apply Numerical techniques for data analysis.</li> <li>• To apply statistical techniques for data analysis.</li> <li>• To solve probability distributions.</li> <li>• To solve complex variables and complex integrations.</li> </ul>							
<b>Course Outcomes:</b> Upon completion of the course, students will be able to <ol style="list-style-type: none"> <li>1. Solve the Linear Differential equations, modelling of mass spring systems, free and forced damped and undamped systems.</li> <li>2. Solve Fourier Transform and Z-Transform.</li> <li>3. Solve Numerical technique to analyses the data.</li> <li>4. Solve statistical technique to analyze the data.</li> <li>5. Solve Different Probability Distributions.</li> <li>6. Solve Complex variables and Applications of CR Equations.</li> </ol>							
<b>Unit I: Linear Differential Equations</b>							
LDE of nth order with constant coefficients, Method of Variation of Parameters, Cauchy's & Legendre's DE, Solution of Simultaneous Differential Equations, Modeling of Electrical circuits.							
<b>Unit II: Transforms</b>							
Fourier Transform (FT): Fourier Integral Theorem, Sine & Cosine Integrals, Fourier Transform, Fourier Sine and Cosine Transform and their Inverses. Introductory Z-Transform (ZT): Definition, Standard Properties, ZT of Standard Sequences and their Inverses. Solution of Simple Difference Equations.							



**Unit III: Computer oriented Numerical Methods**

Solution of Simultaneous equations by Gauss Jacobi method, Gauss Seidel method, Solutions of Differential equations by Euler method, Euler modified method, Rungekutta 4<sup>th</sup> order method.

**Unit IV: Statistics**

Measures of Central Tendency, Standard Deviation, Moments, Skewness and Kurtosis, Correlation and Regression, Reliability of Regression Estimates.

**Unit V : Probability**

Theorems and Properties of Probability, Probability Distributions: Binomial, Poisson, Normal and Hyper geometric; Test of Hypothesis: Chi-Square test. Applications of statistics in Data Analysis.

**Unit VI: Complex Variables.**

Complex Variables Functions of Complex Variables, Analytic Functions-R Equations, Conformal Mapping, Bilinear Transformation, Cauchy's Integral Theorem, Cauchy's Integral formula, Residue Theorem.

**Textbooks:**

1. A Text book of Applied Mathematics by P.N. Wartikar, U.N.Wartikar (Pune Vidyarthi Griha Prakashan ,Pune) (Volume II-ISBN 81-85825-07-6)(Volume III-ISBN 81-85825-01-7)
2. Advanced Engineering Mathematics by Erwin Kreyszig (Wiley Eastern Ltd.).(ISBN 978-0-470-45836-5.)

**Reference Books:**

1. Higher Engineering Mathematics by B.S.Grewal (Khanna Publication, Delhi)(ISBN-13. 978-81-7409-195-5. ISBN-10. 81-7409-195-5)
2. Advanced Engineering Mathematics by Wylie C.R &Barrett L.C.(McGraw-Hill,INC)(ISBN 0 - 07 -463841 – 6)
3. Advanced Engineering Mathematics by Peter V.O'Neil (ISBN-13: 9781111427429 / ISBN-10: 1111427429)



Bansilal Ramnath Agarwal Charitable Trust's  
**Vishwakarma Institute of Information Technology, Pune-48**  
(An Autonomous Institute affiliated to Savitribai Phule Pune University)  
**Department of Information Technology**

<b>Tutorial Assignment List :</b>
Tutorial No.1 Linear Differential Equations
Problems on Differential Equations
Tutorial No.2 Fourier Transform
Problems on Fourier Transform
Tutorial No.3 Z Transform
Problems on Z Transform
Tutorial No.4 Statistics and Probability
Problems on Statics and Probability
Tutorial No.5 Numerical methods
Problems on Numerical methods
Tutorial No.6 Vector differential Calculus
Problems on Vector Diffraction.
Tutorial No.7 Vector Integral Calculus
Problems on Vector Integration
Tutorial No.8 Complex variables
Problems on Complex variables.
<b>Text books:</b> <ol style="list-style-type: none"><li>1. A Text book of Applied Mathematics by P.N. Wartikar, U.N.Wartikar (Pune Vidyarthi Griha Prakashan ,Pune) (Volume II-ISBN 81-85825-07-6)((Volume III-ISBN 81-85825-01-7)</li><li>2. Advanced Engineering Mathematics by Erwin Kreyszig (Wiley Eastern Ltd.).(ISBN 978-0-470-45836-5.)</li></ol>
<b>Reference Books:</b> <ol style="list-style-type: none"><li>1) Higher Engineering Mathematics by B.S.Grewal (KhannaPublication,Delhi) (ISBN-13. 978-81-7409-195-5. ISBN-10. 81-7409-195-5)</li><li>2) Advanced Engineering Mathematics by Wylie C.R &amp;Barrett L.C.(McGraw-Hill,INC)(ISBN 0 - 07 - 463841 – 6)</li><li>3) Advanced Engineering Mathematics by Peter V.O’Neirol (ISBN-13: 9781111427429 / ISBN-10: 1111427429)</li></ol>



Bansilal Ramnath Agarwal Charitable Trust's  
**Vishwakarma Institute of Information Technology, Pune-48**  
(An Autonomous Institute affiliated to Savitribai Phule Pune University)  
**Department of Information Technology**

**Data Structures and Files (ITUA22182)**

Teaching Scheme	Examination Scheme						
Credits: 3 Lecture (L): 3 hrs./week	CIE	ISE	SCE	ESE	PR/OR	TW	Total
	20	30	20	30	-	-	100
<b>Prerequisite course(s):</b> Fundamentals of Data Structures, Discrete Mathematics, Object oriented programming							
<b>Course Objective(s):</b> <ul style="list-style-type: none"><li>• To study non-linear data structures and their implementations.</li><li>• To study some advanced data structures such as tables.</li><li>• To choose the appropriate data structure for modeling a given problem.</li><li>• To learn different file organizations.</li></ul>							
<b>Course Outcomes:</b> Upon completion of the course, students will be able to <ol style="list-style-type: none"><li>1. Use tree data structure for problem solving and programming.</li><li>2. Use graph data structure for problem solving and programming.</li><li>3. Use table data structure for problem solving and programming.</li><li>4. Illustrate heap data structure and its working.</li><li>5. Summarize different multi way trees.</li><li>6. Select appropriate file organization techniques.</li></ol>							
<b>Unit I : Trees</b>							
Difference in linear and non-linear data structure, Basic tree concepts, Binary trees and their properties, Representation using sequential and linked organization, Full and complete binary trees, Conversion of general tree to binary tree, Binary tree as an ADT. Recursive and non-recursive algorithms for binary tree traversals, Binary search trees, and Binary search tree as ADT, Concept of threaded binary tree, Creation and Traversal of in-order threaded binary tree, Applications of trees							
<b>Unit II : Graphs</b>							
Graph as an ADT, Representation of graphs using adjacency matrix and adjacency list, Depth First Search and Breadth First Search traversal. Prim's and Kruskal's algorithms for minimum spanning tree, Shortest path using Dijkstra's algorithm, Topological sorting, Applications of Graphs.							





**Unit III: Symbol Tables using Hashing technique**

**Symbol Table** :Notion of Symbol Table, Static & dynamic tree table, **Hash tables**: Basic concepts, Hash function, Characteristics of good hash function, Different key-to-address transformations techniques, Synonyms or collisions, Collision resolution techniques- linear probing, Quadratic probing, Rehashing, Chaining without replacement and Chaining with replacement, Applications.

**Unit IV: Symbol Tables using other techniques**

Heap: Heap definition, Heap properties, Types of heap , Heap data structure, Applications of heap: heap sort implementation , Priority queue, Huffman algorithm, Concept of OBST , AVL Trees and algorithms

**Unit V : Multi way trees**

Basic of m-way search tree, Concept of red and black trees, B tree implementation, Concept of B+ -tree, Splay trees, R-Trees, Quad tree, Applications.

**Unit VI: File Organization**

External storage devices, file organization - Sequential file organization, Direct file organization, Index sequential file organization, their implementation and comparison, Multi-indexed Files, Inverted Files.

**Textbooks:**

1. Horowitz and Sahani, —Fundamentals of Data Structures in C++, University Press, ISBN 10: 0716782928 ISBN 13: 9780716782926.
2. R. Gillberg, B. Forouzan, —Data Structures: A Pseudo code approach with C++, Cenage Learning, ISBN 9788131503140.

**Reference Books:**

1. Yedidyah Langsam, Moshe J Augenstein, Aron M Tenenbaum, —Data Structures using C and C++, Pearson Education, ISBN 81-317-0328-2.
2. A Michael Berman, —Data Structures via C++: Objects by Evolution, Oxford University Press, ISBN:0-19-510843-4.
3. M. Weiss, —Data Structures and Algorithm Analysis in C++, 2nd edition, Pearson Education, 2002, ISBN-81-7808-670-0.
4. Brassard & Bratley, —Fundamentals of Algorithmics, Prentice Hall India/Pearson Education, ISBN 13-9788120311312.
5. Goodrich, Tamassia, Goldwasser, —Data Structures and Algorithms in C++, Wiley publication, ISBN-978-81-265-1260-7



Bansilal Ramnath Agarwal Charitable Trust's  
**Vishwakarma Institute of Information Technology, Pune-48**  
(An Autonomous Institute affiliated to Savitribai Phule Pune University)  
**Department of Information Technology**

**Computer Networks (ITUA22183)**

Teaching Scheme	Examination Scheme						
Credits: 3 Lecture (L): 3 hrs./week	CIE	ISE	SCE	ESE	PR/OR	TW	Total
	20	30	20	30	-	-	100
<b>Prerequisite course(s):</b> Digital Electronics, Fundamentals of Data Communication							
<b>Course Objective(s):</b> <ul style="list-style-type: none"><li>• To study the fundamentals of networking</li><li>• To understand functionalities of Physical and Data link layer</li><li>• To understand the functionalities of Network Layer</li><li>• To study various protocols at Transport and Application Layer</li><li>• To learn different techniques for routing and routing configuration.</li><li>• To learn and demonstrate VLAN, ACL and NAT in networking</li></ul>							
<b>Course Outcomes:</b> Upon completion of the course, students will be able to <ol style="list-style-type: none"><li>1. Explore network design issues</li><li>2. Understand the functions of OSI layers &amp; TCP/IP protocol stack</li><li>3. Understand the functionality of network layer</li><li>4. Understand the functionality of Transport and Application Layer</li><li>5. Analyze the routing requirements for a given network/LAN and decide the most appropriate routing strategy.</li><li>6. Design Switched networks and demonstrate the concepts of VLAN and ACL for switched networks.</li></ol>							
<b>Unit I – Explore the Network:</b>							
LANs, WANs, and the Internet, The Network as a Platform, Network Components, Network connecting devices, IEEE standards. Addressing: Physical & logical Addresses, Port Addresses, Specific Addresses. Rules of Communication: Communication Fundamentals, Rule Establishment, Message Encoding, Message Formatting and Encapsulation, Message Size, Message Timing, Message Delivery options.							
<b>Unit II - Network Protocols, Physical Layer and Data Link Layer</b>							
Network Protocols and Standards: Protocols, Protocol Suites, Standard Organization, Benefits of using layered Model, OSI Reference Model, TCP/IP Protocol Model, Data Transfer in the Network: Data Encapsulation and Data Access. Physical Layer Protocols, Network Media, Data Link Layer Protocols, Media Access Control. Types of Errors: Redundancy, Detection Versus Correction, Forward Error Correction Versus Retransmission. Ethernet IEEE standards, Standard Ethernet, Fast Ethernet, Gigabit Ethernet. Ethernet Protocol, LAN Switches, Address Resolution Protocol.							



**Unit III – Network Layer**

Network Layer Protocols, Routing, Routers, Configuring a Cisco Router. IP Addressing: IPv4 Network Addresses, IPv6 Network Addresses, Connectivity Verification. sub netting IP Networks: sub netting an IPv4 Network, Addressing Schemes, Address Schemes, Design consideration for IPv6

**Unit IV - Routing Essentials**

Introduction to Routing, Router Functions and Configuration, Routing Decisions and Operations, Routing Table, Static Routing and Default Routing, Dynamic Routing and Protocols, The Routing table, Static vs. Dynamic Routing.

**Unit V – Switched Networks**

Introduction to Switching, Switched and Converged Networks, Switching Domains, Switch Configuration and Security, Virtual LANs, Inter-VLAN Routing, Access Control Lists, NAT for IPv4.

**Unit VI – Broadband Networks**

Basic overview of various technologies: MPLS, VPN, B-ISDN, Frame Relay: Architecture and layers, ATM: Cell format, Architecture, Layers, VCC, VPC, Fast Ethernet and Gigabit Ethernet: concepts and details.

**Textbooks:**

1. S. Tanenbaum : "Computer, Networks", PHI Publication, 4th edition, ISBN: 8178087855
2. Fourauzan B., "Data Communications and Networking", 5th Edition, Tata McGraw- Hill, Publications, ISBN: 0 – 07 – 058408 – 7
3. Kurose, Ross —Computer Networking a Top Down Approach Featuring the Internet, Pearson, ISBN-10: 0132856204

**Reference Books:**

1. Routers and Routing Basics CCNA 2 Companion Guide- Webdell Odom, Rick McDonald
2. CCNA Routing and Switching 200-125 Official Cert. Guide Library
3. Cisco CCNA Command Guide- An introductory Guide for complete beginners



**Software Engineering (ITUA22184)**

Teaching Scheme	Examination Scheme						
Credits: 3 Lecture (L): 3 hrs./week	CIE	ISE	SCE	ESE	PR/OR	TW	Total
	20	30	20	30	-	-	100
<b>Prerequisite course(s):</b> Basic programming skill							
<b>Course Objective(s):</b> <ul style="list-style-type: none"><li>• To learn and understand the principles of Software Engineering</li><li>• To understand the nature of software complexity in various application domains, disciplined way of software development and software lifecycle process models.</li><li>• To introduce principles of agile software development, the SCRUM process and agile practices.</li><li>• To be acquainted with methods of capturing, specifying, visualizing and analyzing software requirements.</li><li>• To know methods of capturing, specifying, visualizing and analyzing software requirements.</li><li>• To understand concepts and principles of software design and architecture.</li><li>• To apply Test Cases and Design principles to S/W project development.</li></ul>							
<b>Course Outcomes:</b> <p>Upon completion of the course, students will be able to</p> <ol style="list-style-type: none"><li>1. Compare and chose a process model for a software project development</li><li>2. Identify unique features of various software applications to write in SRS also to identify domains and classify software applications.</li><li>3. Describe principles of agile development, discuss the SCRUM process and distinguish agile process model from other process models.</li><li>4. Understand importance of risk management and configuration management &amp; apply risk management strategies and configuration management techniques to various projects.</li><li>5. Describe project management technique &amp; apply software testing strategies.</li><li>6. Apply advanced software engineering techniques for global software development.</li></ol>							
<b>Unit I - Introduction to Software Engineering</b>							
Nature of Software, The Software Process, Software Myths, A Generic Process Model, Prescriptive Process Models, The Waterfall Model, Incremental Process Models, Evolutionary Process Models, Concurrent Models, Specialized Process Models, Unified Process.							



**Unit II - Agile Methodology**

**Agile Development:** Agile manifesto, agility and cost of change, agility principles, myth of planned development, toolset for the agile process.

**Extreme Programming:** XP values, process, industrial XP, SCRUM - process flow, scrum roles, scrum cycle description, product backlog, sprint planning meeting, sprint backlog, sprint execution, daily scrum meeting, maintaining sprint backlog and burn-down chart, sprint review and retrospective.

**Agile Practices:** test driven development, refactoring, pair programming, continuous integration, exploratory testing versus scripted testing

**Unit III – Requirement analysis**

**Requirements Capturing:** requirements engineering (elicitation, specification, validation, negotiation, prioritizing requirements (Kano diagram) - real life application case study.

**Requirements Analysis:** basics, scenario based modeling, UML models: use case diagram and class diagram, data modeling, data and control flow model, behavioral modeling using state diagrams - real life application case study, software Requirement Specification.

**Unit IV - Risk Management , Configuration Management**

**Project Risk Management:** Risk Analysis & Management: Reactive versus Proactive Risk Strategies, Software Risks, Risk Identification, Risk Projection, Risk Refinement, Risk Mitigation, Risks Monitoring and Management, The RMMM plan for case study project.

**Software Configuration Management:** SCM basics, SCM repository, SCM process, SCM tools such as GitHub, CASE – taxonomy, tool-kits, workbenches, environments, components of CASE, categories (upper, lower and integrated CASE tools).

**Unit V – Testing & Software Quality Assurance**

**Software Quality, Achieving Software Quality:** Software engineering methods, Project Management techniques. Quality control and quality assurance. Software Reliability, SQA Tools, Goals and Metrics,

**Introduction to Software Testing:** Principles of Testing, Testing Life Cycle, Phases of Testing, Types of Testing, Verification & Validation, Defect Management, Defect Life Cycle, Bug Reporting, GUI Testing, Test Management and Automation.

**Software Process Improvement (SPI):** What is SPI, SPI Process, The CMMI, The People CMM, Case study: SPI frameworks.

**Unit VI - Advanced Software Engineering**

**Maintenance & Reengineering:** Software Maintenance, Software Supportability, Reengineering, Business Process Reengineering, Software Reengineering, Reverse Engineering, Restructuring, Forward



Bansilal Ramnath Agarwal Charitable Trust's

## **Vishwakarma Institute of Information Technology, Pune-48**

(An Autonomous Institute affiliated to Savitribai Phule Pune University)

### **Department of Information Technology**

Engineering. **Emerging software engineering trends:** technology evolution, process trends, collaborative development, test-driven development, global software development challenges.

#### **Textbooks:**

1. Roger S Pressman "Software Engineering: A Practitioner's Approach" 7th Edition McGraw-Hill ISBN:0073375977
2. Ian Sommerville "Software Engineering" 9th edition Pearson Education SBN-13: 978-0-13-703515-1, ISBN-10: 0-13-703515-2, pdf downloadable
3. Hong Zhu "Software Design Methodology", Elsevier ISBN: 978-81-312-0356-9

#### **Reference Books:**

1. Rajib Mall, "Fundamentals of Software Engineering", Prentice Hall India, ISBN-13: 978-8120348981
2. Pankaj Jalote, "An Integrated Approach to Software Engineering", Springer, ISBN 13: 9788173192715.
3. Joseph Phillips, IT Project Management –On Track From Start to Finish, Tata Mc Graw-Hill, ISBN13: 978-0-07106727-0, ISBN-10: 0-07-106727-2.
4. C. Michael Pilato, Ben Collins-Sussman and Brian Fitzpatrick, Version Control with subversion, O'Reilly, Shroff publishers, ISBN: 978-81-8404-728-8.



Bansilal Ramnath Agarwal Charitable Trust's

## **Vishwakarma Institute of Information Technology, Pune-48**

(An Autonomous Institute affiliated to Savitribai Phule Pune University)

### **Department of Information Technology**

#### **Social Science and Engineering Economics (ES22185IT)**

Teaching Scheme	Examination Scheme						
Credits: 3 Lecture (L): 3 hrs./week	CIE	ISE	SCE	ESE	PR/OR	TW	Total
	20	30	20	30	-	-	100
<b>Prerequisite course(s):</b> Nil							
<b>Course Objective(s):</b> <ul style="list-style-type: none"><li>• Human and social development.</li><li>• Contemporary national and international affairs.</li><li>• Emergence of Indian society and Economics.</li><li>• Sectoral development and Economic development and related issues (such as international economics, WTO, RBI, etc).</li></ul>							
<b>Course Outcomes:</b> <p>Upon completion of the course, students will be able to</p> <ol style="list-style-type: none"><li>1. Understand various issues concerning human and society.</li><li>2. Realize social, cultural, economic and human issues, involved in social changes</li><li>3. Understand the nature of the individual and the relationship between the self and the community</li><li>4. Express their opinion about national health and education policies.</li><li>5. Understand major ideas, values, beliefs, and experiences that have shaped human history and cultures.</li><li>6. Understand the fundamental concepts in engineering economics.</li></ol>							
<b>Unit I - Indian Society</b>							
Structure of Indian Society, Indian Social Demography– Social and Cultural, Differentiations: caste, class, gender and tribe; Institutions of marriage, family and kinship- Secularization –Social Movements and Regionalism- Panchayatraj Institutions; Affirmative Action Programme of the Government-various reservations and commissions.							
<b>Unit II - Social Development</b>							
Scientific approach to the study of human beings. Evolution of human kind, social change and evolution. Industrial revolution. National policy on education, health and health care and human development.							



Bansilal Ramnath Agarwal Charitable Trust's  
**Vishwakarma Institute of Information Technology, Pune-48**  
(An Autonomous Institute affiliated to Savitribai Phule Pune University)  
**Department of Information Technology**

**Unit III – Sectoral Development**

Agriculture: Technology changes, Green revolutions, Employment Rural and Urban, Government Schemes. Industrial Development: Strategies, Public and Private Sectors, Categories, infrastructure, transport and communication, Consumer Awareness.

**Unit IV - Economic Development**

Need for planned economic development – Law of demand and supply. Planning objective, five years plan, priorities and problems. Population and development. Indian Economics – basic features, natural resources population size and composition, national income concepts, micro economics of India, inflation, GDP.

**Unit V - Banking and Trades**

Financial Analysis, Ratios, Cost Analysis, financial Institutions, Finance Commissions, Budget Analysis. Indian Banking, Role of Reserve bank of India International Economy, WTO, International aid for economic growth.

**Unit VI - Understanding Cash Flow and Taxes**

Accounting for Depreciation and Income Taxes, Project Cash-Flow Analysis, Understanding Financial Statements, Case Studies - cash flow analysis done in start-up companies.

**Textbooks:**

1. Krugman, International Economics, Pearson Education.
2. Prakash, The Indian Economy, Pearson Education.
3. Thursen Gerald, Engineering Economics, Prentice Hall.
4. C. S. Rao, Environmental Pollution Control Engineering, New Age International Pvt. Ltd.

**Reference Books:**

1. Rangarajan, Environmental Issues in India, Pearson Education.
2. University of Delhi, The Individual & Society, Pearson Education.
3. Wikipedia.org / wiki /social studies.
4. M. N. Srinivas, Social change in modern India, 1991, Orient Longman.
5. David Mandelbaum, Society in India, 1990, Popular





Bansilal Ramnath Agarwal Charitable Trust's  
**Vishwakarma Institute of Information Technology, Pune-48**  
(An Autonomous Institute affiliated to Savitribai Phule Pune University)  
**Department of Information Technology**

**Lab Practice II (ITUA22186)**

Teaching Scheme		Examination Scheme						
Credits: 3 Practical (P): 6 hrs./week		CIE	ISE	SCE	ESE	PR/OR	TW	Total
		50	-	-	-	50	-	100
Prerequisite course(s):								
Course Objective(s): <ul style="list-style-type: none"><li>To understand various network command and configuration of network equipment's</li><li>To understand various network application protocols</li><li>To study non-linear data structures and their implementations.</li><li>To study some advanced data structures such tables.</li><li>To choose the appropriate data structure for modeling a given problem.</li><li>To learn different file organizations.</li></ul>								
Course Outcomes: Upon completion of the course, students will be able to <ul style="list-style-type: none"><li>1. Know various commands and utilities related to networking</li><li>2. Configure various application protocols on the server and check them through clients</li><li>3. Use tree data structure for problem solving and programming. (Application)</li><li>4. Use graph data structure for problem solving and programming. (Application)</li><li>5. Use table data structure for problem solving and programming. (Application)</li><li>6. Illustrate heap data structure and its working. (Application)</li></ul>								
PART I- Computer Network Laboratory								
1	Study of basic TCP/IP network commands and utilities (eg: ping, ifconfig, tracert, arp, tcpdump, whois, host, netsat, nslookup, ftp, telnet etc...)							
2	Configure a router (Ethernet & Serial Interface) using router commands including access lists on any network simulator (eg. Packet Tracer)							
3	Network design and implementation for small network using Packet Tracer							
4	Network design & implementation of VLAN on Packet Tracer							
5	Network analysis as well as packet header study with the help of any protocol analyzer/ packet sniffer.(eg: wireshark)							



6	Socket Programming in C Language on Linux. a) TCP Client , TCP Server b) UDP Client , UDP Server
7	Installation and Configuration of Remote Login Service Telnet/SSH and access it through Telnet/SSH client
8	Installation and Configuration of FTP server and access it through FTP Client
9	Installation and configuration of DHCP Server in Wireless Environment using an Access Point (Packet Tracer)
10	Case Study of existing College network with IP Address Scheme.
<b>PART II- Data Structure and Files Laboratory</b>	
1	a) Construct an expression tree from postfix/prefix expression and perform recursive inorder, preorder and post order traversals.
	b) For expression tree, perform non-recursive inorder, preorder and post order traversals.
2	a) Create a binary search tree (BST) of and perform following operations: i) Insert ii) Display inorder iii) Search a node
	b) Perform following operations on BST ii) Find height of the tree iv) level wise display
	c) Perform following operations on BST i) Delete iii) Find mirror image
3	a) Construct an inorder threaded binary search tree.
	b) Traverse threaded binary tree it in inorder and preorder.
4	a) Consider a friends' network on face book social web site. Model it as a graph to represent each node as a user and a link to represent the friend relationship between them. Store data such as date of birth, number of comments for each user. i) Find who is having maximum friends Hint: Use adjacency list representation
	b) For a friends' network, ii) Find who has post maximum and minimum comments (using BFS) iii) Find users having birthday in this month (using DFS)
5	a) Use the map of the area around the college as the graph. Identify the prominent land marks as nodes and find minimum distance to various land marks from the college as the source. Represent this graph using adjacency matrix
	b) find the shortest path using Dijkstra's algorithm.
6	a) A business house has several offices in different countries; they want to lease phone lines to connect them with each other and the phone company charges different rent to connect different pairs of cities. Business house want to connect all its offices with a minimum total cost.



	i) Represent using appropriate data structure.
	b) Apply suitable algorithm to find minimum total cost. (Prim's algorithm)
7	a) Represent any real world graph using adjacency list /adjacency matrix
	b) Find minimum spanning tree using Kruskal's algorithm.
8	a) Store data of students using hashing function for roll number and implement linear probing using chaining without replacement algorithm.
	b) Store data of students using hashing function for telephone number and implement linear probing using chaining with replacement algorithm.
9	a) The internship is offered to students based on rank obtained in second year of graduation. Create suitable non-linear data structure to identify next topper student for internship.
	b) Also, the extra academic input is to be given to the 10 students with low grades. Sort the student data in ascending order of grades.
10	a) Implement an index sequential file for any Database and perform following operations on it i) Create Database ii) Display Database iii) Add a record
	b) On index sequential file, perform iv) Delete a record v) Modify a record.
11	a) Implement direct access file for any Database and perform following operations on it i) Create Database ii) Display Database
	b) For direct access file perform iii) Search a record
	<b>*All assignments to be implemented using C/C++ on Linux platform.</b>



**IT Workshop in Java (ITUA22187)**

Teaching Scheme	Examination Scheme						
Credits: 3 Lecture (L): 2 hrs./week Practical (P): 2 hrs./week	CIE	ISE	SCE	ESE	PR/OR	TW	Total
	-	-	50	-	50	-	100
<b>Prerequisite course(s):</b> Data Structure and Files, Object Oriented Programming, Basic knowledge of database management system							
<b>Course Objective(s):</b> <ul style="list-style-type: none"><li>To learn and understand fundamentals object oriented programming using core java</li><li>To learn and understand client side programming of a small project.</li><li>To learn and understand server side programming of a small project.</li></ul>							
<b>Course Outcomes:</b> Upon completion of the course, students will be able to <ol style="list-style-type: none"><li>Apply fundamental concepts Object Oriented programming in Core Java.</li><li>Design and Develop the user interface and validate it.</li><li>Develop server side programming skills and database handling.</li></ol>							
<b>Unit I – Introduction to the Java Environment and Java</b>							
Installing Java, Java Program Development, Class Fundamentals , Object & Object reference , Object Life time & Garbage Collection, Constructor, Access Control, Modifiers, methods Nested , ,Abstract Class & Interfaces, Method Overloading, Dealing with Static Members, Array, Inheritance, Polymorphism in inheritance.							
<b>Unit II - GUI Programming in Java</b>							
Introduction to HTML, List, Creating table, Linking documents, Frames, style sheet and Introduction JavaScript, Advantage of java script, Java script syntax, Data type- Variable, Array, Operator and Expression looping constructor, Function, Dialog box, Java script document object model, Introduction object in HTML, Event handling, Window object, Document object, Browser object, Form object, Navigator object, Screen object, Built in object, User defined object, Cookies							
<b>Unit III - Database Programming and Java Server Technologies</b>							



Bansilal Ramnath Agarwal Charitable Trust's

## **Vishwakarma Institute of Information Technology, Pune-48**

(An Autonomous Institute affiliated to Savitribai Phule Pune University)

### **Department of Information Technology**

Database Programming using JDBC Introduction to JDBC, JDBC Drivers & Architecture, CURD operation Using JDBC

Servlet Web Application Basics, Architecture and challenges of Web Application, Introduction to servlet, Servlet life cycle, Developing and Deploying Servlets, Exploring Deployment , Descriptor (web.xml), Handling Request and Response, JSP

#### **Textbooks:**

1. Jeff Friessen, "Beginning java 6 platform from Novice to Professional", Apress/Springer, 9788181288769
2. Kogent Learning Solutions Inc," Web Engineering: HTML, Javascript, PHP, Java, JSP, ASP.net, XML and Ajax", Wiley Black Book, 9788177229974
3. Gallardo, Burnette, McGrover ,“ Eclipse in action: a guide for java Developers”, Manning, 9788177224986

#### **Reference Books:**

1. Khalid A Mughal, "A programmer's guide to Java SCJP Certification", Third Edition, Pearson Education ,978-81-317-2688-4



Bansilal Ramnath Agarwal Charitable Trust's  
**Vishwakarma Institute of Information Technology, Pune-48**  
(An Autonomous Institute affiliated to Savitribai Phule Pune University)  
**Department of Information Technology**

## Lab Assignments:

<b>Prerequisites:</b> Data Structure and Files, Object Oriented Programming, Basic knowledge of database management system
<b>Course Objectives:</b> <ul style="list-style-type: none"><li>• To learn and understand fundamentals object oriented programming using core java</li><li>• To learn and understand client side programming of a small project.</li><li>• To learn and understand server side programming of a small project.</li></ul>
<b>Course Outcomes</b> After completion of the course, student will be able to <ol style="list-style-type: none"><li>1. Apply fundamental concepts Object Oriented programming in Core Java.</li><li>2. Design and Develop the user interface and validate it.</li><li>3. Develop server side programming skills and database handling.</li></ol>
<b>Assignment 1-Core Java Programming</b> Develop mini-project in Core Java using any IDE. Following points should be covered: <ol style="list-style-type: none"><li>1. Object oriented programming Principle.</li><li>2. Effective Use of IDE and customization to improve productivity and accuracy.</li><li>3. add Set/ Get Methods,</li><li>4. Overriding methods from base class</li><li>5. Add constructors</li><li>6. Customization, commenting javadoc etc.</li></ol>
<b>Assignment 2- Client Side Technologies</b> <ol style="list-style-type: none"><li>1. Develop User interface using HTML and client side scripting JavaScript and simple Applets.</li><li>2. Focus on changing the behavior of the pages and Form Validations through Client side scripting.</li></ol> For this, reuse the mini project developed in core java in Unit I
<b>Assignment 3– Server side Programming</b> <ol style="list-style-type: none"><li>1. Develop a Server side mini-project using Servlets. Preferably this mini-project should reuse the part of the mini project developed for Unit-I and Unit-II.</li></ol> Focus in this mini-project should be to understand <ol style="list-style-type: none"><li>1. How to use an IDE environment for web application development</li><li>2. How to use tomcat through an IDE for running web applications.</li><li>3. HTTPRequest, HTTPResponse, Session etc.</li><li>4. Data persistence through JDBC</li></ol>



**Cyber Security (ITUA22188)**

Teaching Scheme	Examination Scheme						
Credits: 2 Lecture (L): 2 hrs./week	CIE	ISE	SCE	ESE	PR/OR	TW	Total
	50	-	50	-	-	-	100
<b>Prerequisite course(s):</b> Data Structure and Files, Object Oriented Programming, Basic knowledge of database management system							
<b>Course Objective(s):</b> <ul style="list-style-type: none"><li>To understand the importance of cyber security</li><li>To know various laws and codes of ethics of information security</li><li>To know different types of cyber-attacks and related tools to overcome it</li><li>To know the issues related to cyber forensics</li></ul>							
<b>Course Outcomes:</b> Upon completion of the course, students will be able to <ol style="list-style-type: none"><li>Summarize various issues in cyber security</li><li>Explain applicable laws and codes of ethics in information security</li><li>Distinguish various tools used in preventing cyber attacks</li><li>Understand cyber forensics issues.</li></ol>							
<b>Unit I – Introduction to Cyber Security</b>							
Definition and origin, Cyber Crime and information security , Classification of Cyber Crime, The Legal Perspective-Indian/Global Perspective , Categories of Cyber Crime, Types of Attack , Social Engineering, Cyberstalking , Cyber Crime, Cloud Computing.							
<b>Unit II - Legal, Ethical Issues in Information security Risk Management</b>							
Overview, Risk identification (Vulnerability Scanning) , Risk assessment (Vulnerability Assessment) , Risk control strategies (Penetration Testing), Quantitative vs Qualitative , Risk control practice, Risk management, Law and ethics in information security , Code of ethics, Protecting programs and data.							
<b>Unit III - Tools and Methods used in Cyber Crime</b>							
Introduction, Proxy server and anonymizers, Phishing password cracking, Key logger and spyware, Types of virus- worms, DoS and DDoS, SQL Injection, Cyber Crime and Legal perspective, Cyber law – Indian Context, The Indian act-amendments, challenges to India law and cybercrime scenario in India, Indian IT act, Digital signature.							



Bansilal Ramnath Agarwal Charitable Trust's

**Vishwakarma Institute of Information Technology, Pune-48**

(An Autonomous Institute affiliated to Savitribai Phule Pune University)

**Department of Information Technology**

<b>Unit IV :Cyber Forensics</b>
Introduction to Cyber forensics, Handling Preliminary Investigations, Controlling an Investigation, Conducting disk-based analysis, Investigating Information hiding, Scrutinized E-mail, Validating E-mail header information, Tracking Internet access, Tracing memory in real-time.
<b>Text Books:</b>
Nima Godbole, Sunit Belapure, Cyber Security- Understand Cyber Crimes, Computer Forensics and Legal Perspectives, Wiely India Pvt. Ltd, ISBN- 978-81-265-2179-1.