ANAND SUNDERRAJAN

A motivated young professional with an amalgam of unique experiences, looking to further knowledge and experience while contributing to the society.

CONTACT

6

217-904-8054



www.linkedin.com/in/AnandSunderrajan



anands3@illinois.edu



anandsunderrajan.github.io



github.com/AnandSunderrajan

NATIONALITY

American

SKILLS

Technical skills:

Assembly (LC3) Adobe Suite
TensorFlow PyTorch
C/C++ Quartus
C for CUDA Git

JavaScript Jetbrains Suite

Node-Red Keras
Python KiCad
System Verilog Linux
MS Office Suite LaTeX
React HTML/CSS

EAGLE

Personal skills:

Communication Creativity
Teamwork Organisation
Leadership Management
Writing Public Speaking
Photography Graphic Art

LANGUAGES

English - Fluent Hindi - Fluent

Spanish - Intermediate

INTERESTS

Artificial Intelligence, Consumer Technology, Computer Hardware, Data Management, Digital System Design, Gaming, Information Assurance, Internet of Things, Machine Learning, Management, Photography, UI/UX Design, Web Design

AWARDS

Dean's List (College of Engineering) - 2020

T.E.A.M University Challenge (Portfolio Management) - Fall 2020

1st - University of Illinois Urbana-Champaign

11th - Nationwide

Best Engineered Design and Project (ECE445) - Fall 2020

Out of 38 teams

EDUCATION

University of Illinois Urbana-Champaign

Bachelor of Science in Computer Engineering

International School of Havana, Cuba

International Baccalaureate Diploma

) WORK EVI

WORK EXPERIENCE

University of Illinois Urbana-Champaign

(Present)

Teaching Assistant - ECE445 (Senior Design)
Lead and manage multiple project teams through the engineering design process - including design review, testing, demo, and professional documentation of each step. Create and present course lectures. Coordinate multiple teams with the course staff for technical and design assistance.

University of Illinois Urbana-Champaign

(August 2020 - Present)

(September 2017 - May 2021)

(June 2015 - May 2017)

Teaching Assistant - ECE385 (Digital System Design)

Assist teams in laboratory experiments involving FPGA design. Conduct office hours for students to resolve their queries and further their understanding of digital systems design.

Hendrick House (May 2018-June 2019)

Receptionist (September 2018 - June 2019) Resident Advisor (May 2018 - September 2018)

Collaborated with a team of 6 Resident Advisors to establish a united leadership team. Addressed resident issues and concerns in a professional and timely manner. Maintained a database of services used by residents and sold to visitors. Created accurate and detailed reports for each shift.

International School of Havana - Havana, Cuba

(October 2016 - May 2017)

IT Intern

Created a program to maintain a student database and assist in inputting values for various categories for each student. Assisted in the implementation of the IT infrastructure for the new building built on Calle 21.

UNICEF - Havana, Cuba

(May 2016 -September 2016)

Intern

Developed a program for maintaining an expense database. Allowed user to efficiently parse through accounts locally on their machine and visualize expense categories.



RELATED COURSE-WORK

CS225: Introduction to Data Structures and Algorithms with C++

Grade A

Experience with data storage structures.

ECE385: Digital Systems Laboratory

Grade A

Experience designing and building digital systems using transistor-transistor logic, System Verilog and field programmable gate arrays.

ECE498ICC: IoT and Cogitive Computing

Grade B+

Experience with CNN creation using Keras and Low Level APIs. Experience using Node-RED, GPUs, Edge devices, Cloud devices.

ECE445: Senior Design

Grade A+

Experience with project management and collaboration. Experience creating android apps and PCBs.

Grade A+

Experience with linear classifiers, multilayer neural networks, computer vision and reinforcement learning.



PROJECTS

CS498DL: Deep Learning

Event Attendance Tracker - Team 13

View Project

C, Java, EAGLE

Project for ECE 445, the capstone course for the ECE department at UIUC. A system that tracks event attendees at a booth through both hardware and software solutions. Utilizes a custom distance determining algorithm.

Pipelined LC3-b Microprocessor

View Code

System Verilog, FPGA Development, Quartus Prime, TTL Logic, LaTeX

A pipelined version of the LC3-B Microprocessor with features such as cache, branch prediction etc. Pipelined version built as a final project for ECE385 (Digital Systems Laboratory) with additional features added subsequently.

Object Detection System

Python, TensorFlow, NumPy, Pandas

View Code

An object detection and classification system trained using Fashion-MNIST, built using two separate methods - Keras and Low-Level APIs.