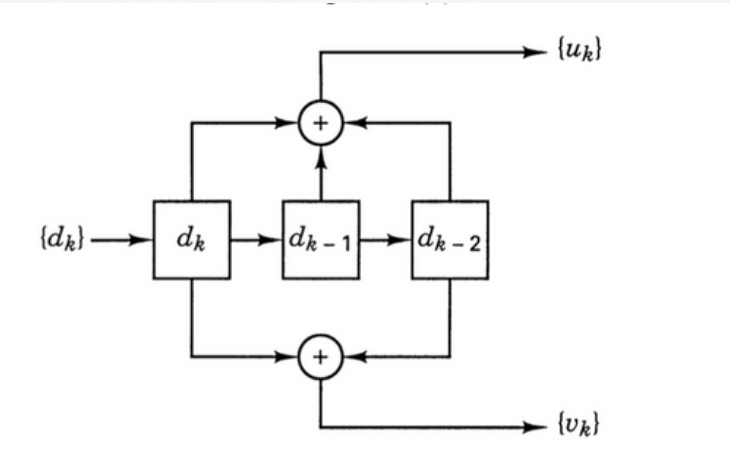


# TURBO ENCODER

## Non Systematic code

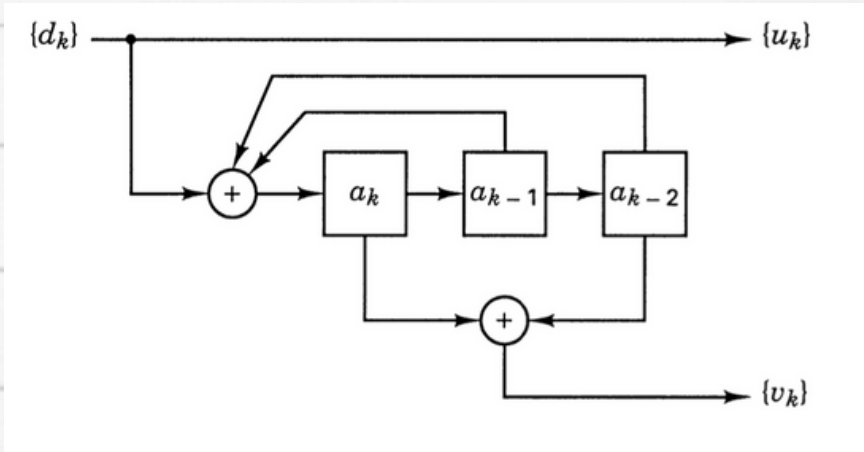
$$u_k = \sum_{i=0}^{K-1} g_{1i} d_{k-i} \mod 2 \quad g_{1i} = 0,1$$
$$v_k = \sum_{i=0}^{K-1} g_{2i} d_{k-i} \mod 2 \quad g_{2i} = 0,1$$



## Recursive Systematic code

$$a_k = d_k + \sum_{i=1}^{K-1} g'_i a_{k-i} \mod 2$$

equal to  $g_{1i}$  if  $u_k = d_k$ , and to  $g_{2i}$  if  $v_k = d_k$ .



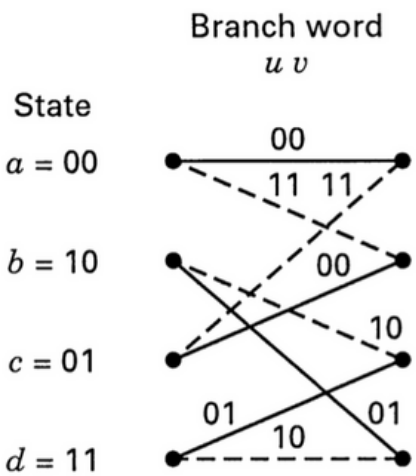
Constraint length  $k$   
memory  $k-1$   
shift register-state- like a conveyer belt  
 $d_k$ - input to encoder at time  $t$   
 $g$ 's- code generator  
 $u_k, v_k$ - outputs  
 $a_k$ - similar to  $d_k$  but are recursively calculated

$G_1 = \{g_{1i}\}$  and  $G_2 = \{g_{2i}\}$

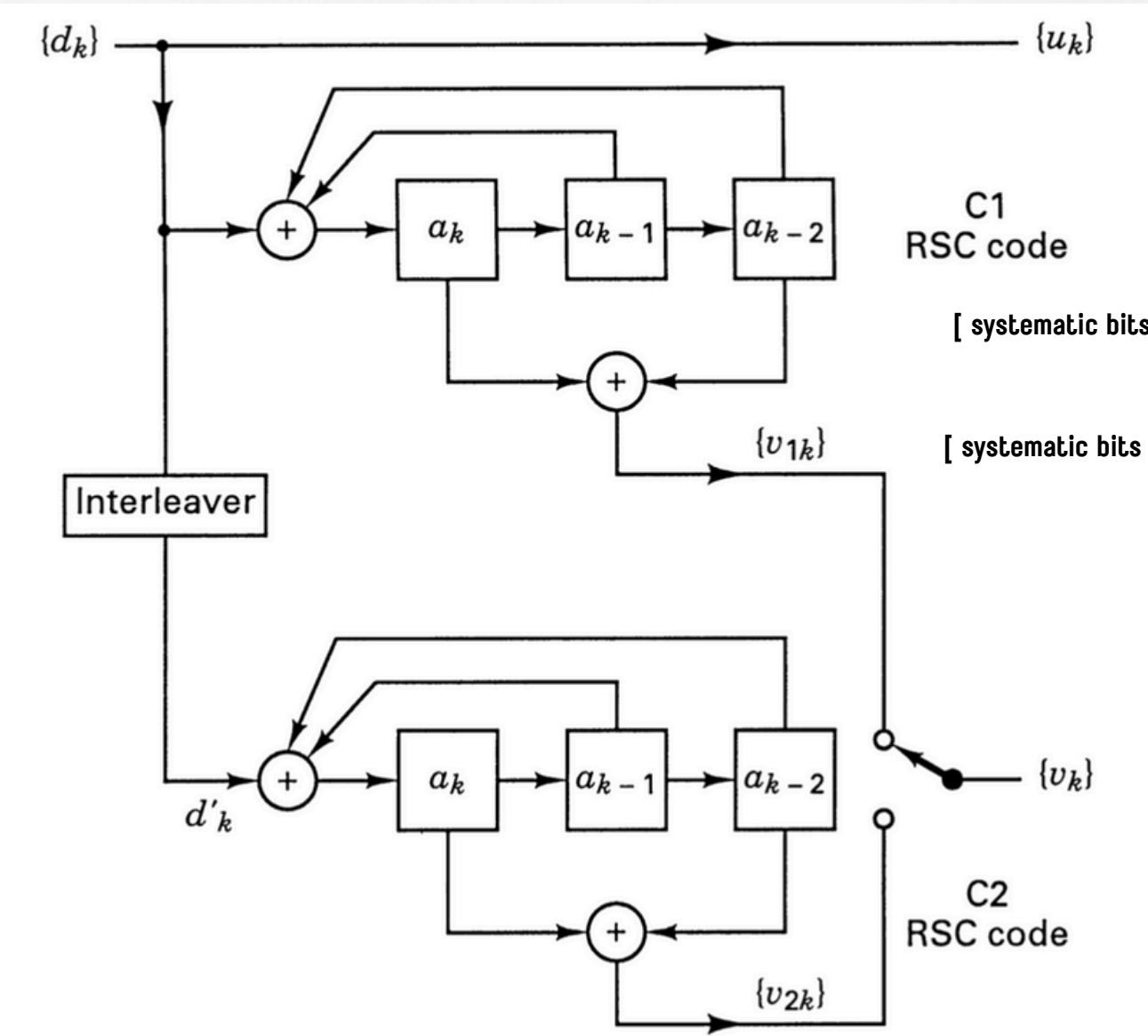
**How  $G_1$  and  $G_2$  work:**  
Each generator tells you which bits to XOR:

- For  $G_1 = \{1\ 1\ 1\}$ :  
 $u_k = d_k \text{ XOR } d_{k-1} \text{ XOR } d_{k-2}$
- For  $G_2 = \{1\ 0\ 1\}$ :  
 $v_k = d_k \text{ XOR } d_{k-2}$

Notice  $G_2$  skips  $d_{k-1}$  because the middle bit is 0.



## Concatenation of RSC codes



We now parallelly run two encoders as shown in figure there is a switch yielding  $v_k$  which provides puncturing thus overall code rate is still  $\frac{1}{2}$  instead of  $\frac{1}{3}$

Final output if overall rate was  $\frac{1}{3}$

[ systematic bits | parity bits from encoder 1 | parity bits from encoder 2 ]

Final output if overall rate is  $\frac{1}{2}$

[ systematic bits | parity bits from encoder 1 or parity bits from encoder 2 ]

Why use interleaver- interleaver shuffles the output receiver from receiver(input for encoder).

If the component encoders are not recursive, the unit weight input sequence  $0\ 0\ \dots\ 0\ 0\ 1\ 0\ 0\ \dots\ 0\ 0$  will always generate a low-weight codeword at the input of a second encoder for any interleaver design. In other words, the interleaver would

not influence the output-codeword weight distribution if the component codes were not recursive. However if the component codes are recursive, a weight-1 input sequence generates an infinite impulse response (infinite-weight output).

$[d_k] \rightarrow \text{Convolutional Encoder} \rightarrow [u_k, v_k] \rightarrow \text{**Channel**} \rightarrow [r_{1k}, r_{2k}] \rightarrow \text{Decoder} \rightarrow [\text{estimated } d_k]$

Time	$d_k$	Past bits ( $d_{k-1}, d_{k-2}$ )	$u_k = d_k \oplus d_{k-1} \oplus d_{k-2}$	$v_k = d_k \oplus d_{k-2}$
0	1	(0, 0)	$1 \oplus 0 \oplus 0 = 1$	$1 \oplus 0 = 1$
1	0	(1, 0)	$0 \oplus 1 \oplus 0 = 1$	$0 \oplus 0 = 0$
2	1	(0, 1)	$1 \oplus 0 \oplus 1 = 0$	$1 \oplus 1 = 0$
3	1	(1, 0)	$1 \oplus 1 \oplus 0 = 0$	$1 \oplus 0 = 1$

Time ( $k$ )	Input $d_k = u_k$	First Stage $a_k$	State at Time $k$		Output $u_k v_k$
			$a_{k-1}$	$a_{k-2}$	
1	1	1	0	0	1 1
2	1	0	1	0	1 0
3	1	0	0	1	1 1
4	0	0	0	0	0 0
5			0	0	

- Weight = number of 1s.
- Low-weight inputs are dangerous because they can create low-weight codewords.
- Recursive encoders fix weight-1 inputs by spreading them into high-weight outputs.
- Interleavers help by making sure bad patterns (weight-2 or weight-3 inputs) don't show up aligned in both encoders at the same time.
- Turbo codes work so well mainly because of recursion + interleaving.