



J4

C4

C1

C5

J3

VDDIO

C3

VSSIO

J2

VDD

VSS

VDDIO = 2.5V
VDD = 1.2V

SERB SUPRA

Designed by Ananya Balike, IITGN

J4

C4

C1

C2

C5

C3

J3

VDDIO

VSSIO

J2

VDD

VSS

VDDIO = 2.5V
VDD = 1.2V

SERB SUPRA
Designed by Ananya Balike, IITGN