

A

B

C

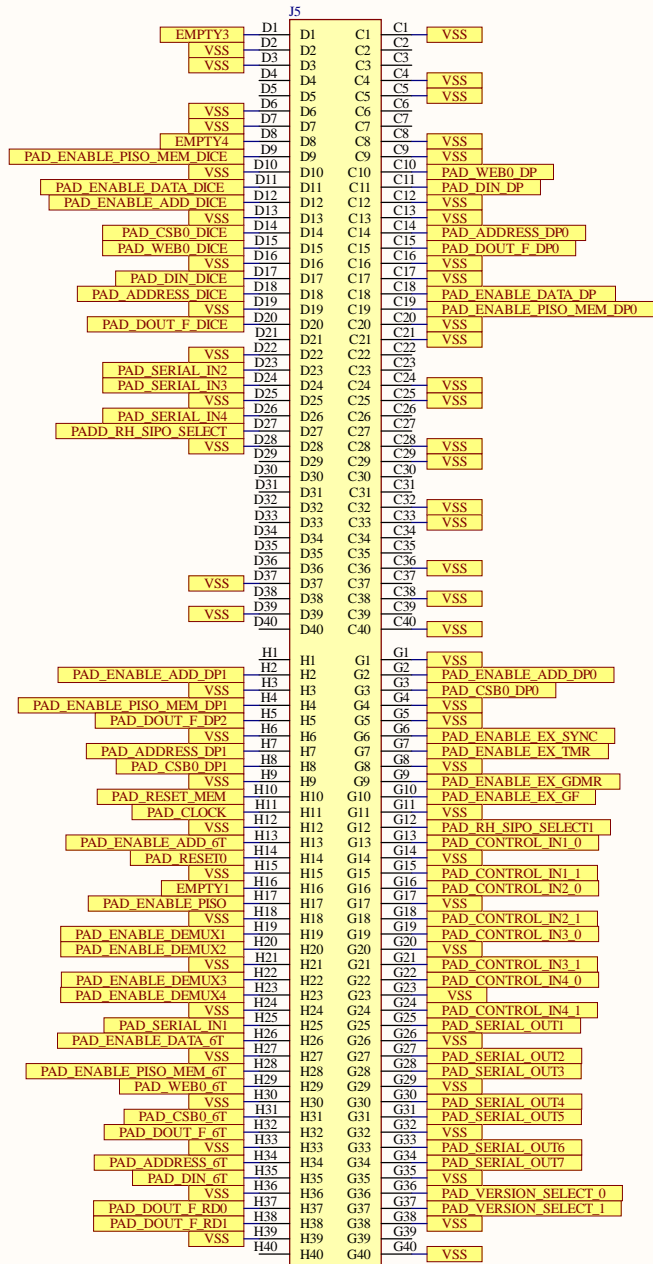
D

A

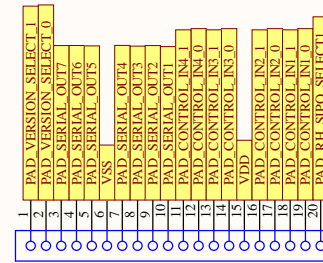
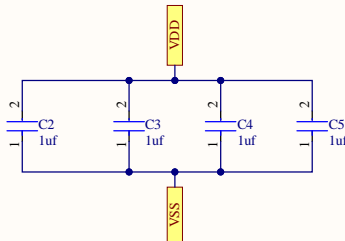
B

C

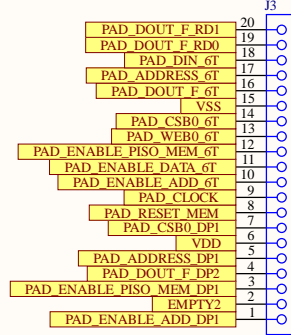
D



ASP-134604-01

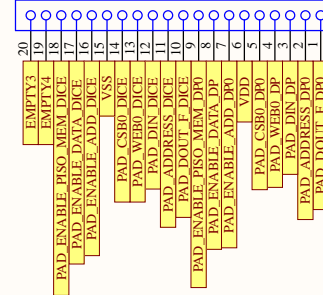


61302011821

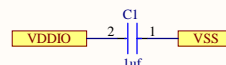
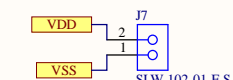
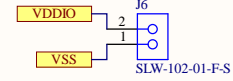
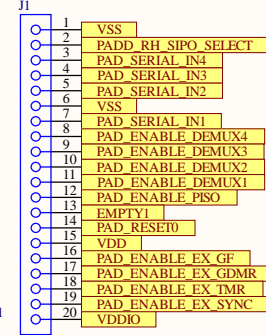


61302011821

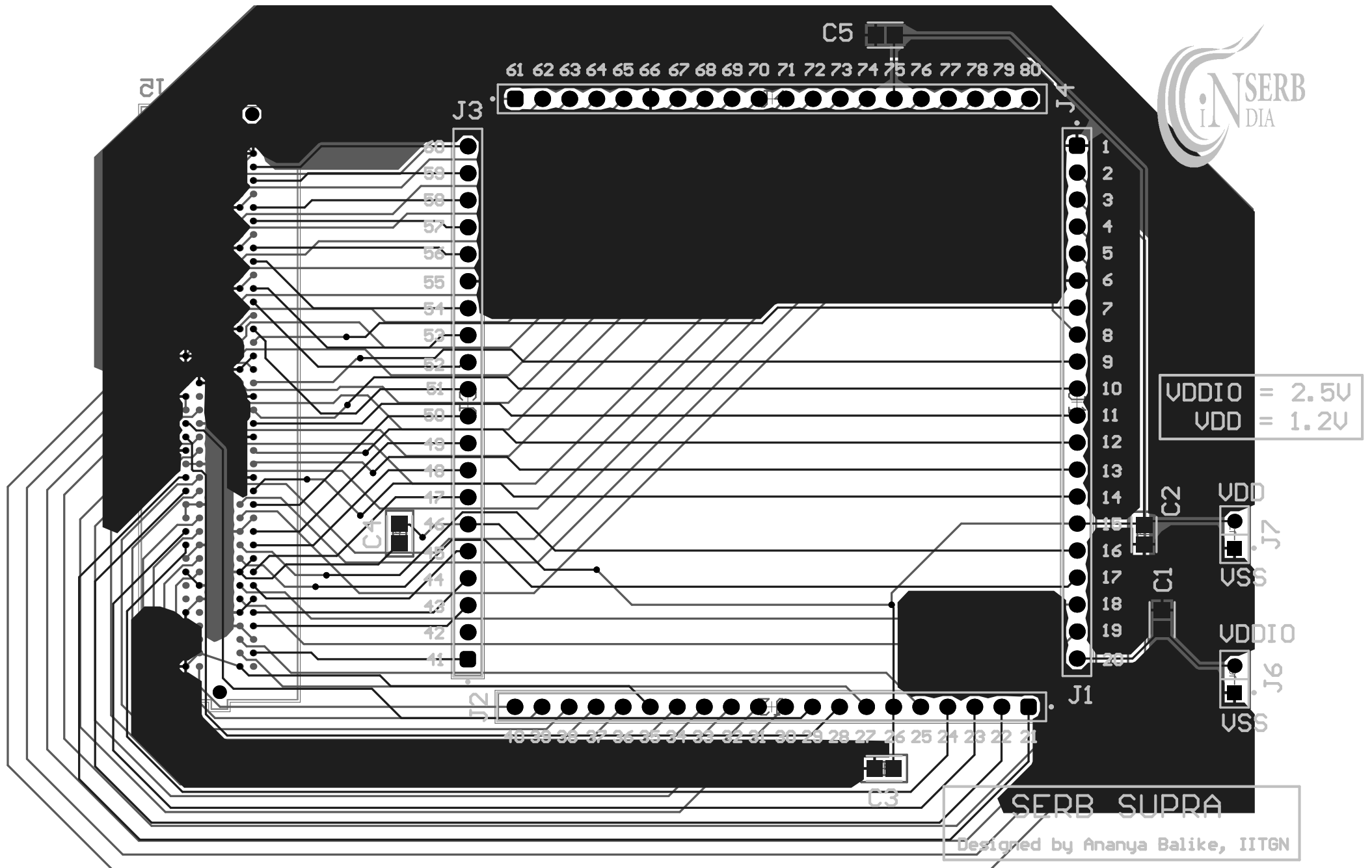
J2 61302011821



61302011821



Title 65nm QFN80		
Size A3	Number	Revision 1
Date: 7-23-2025	Sheet of	
File: C:\Users\...\PCB_65nm_2020.SchDoc	Drawn By: Ananya	





C5

61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80

J3

60  
59  
58  
57  
56  
55  
54  
53  
52  
51  
50  
49  
48  
47  
46  
45  
44  
43  
42  
41

C4

J2

40 39 38 37 36 35 34 33 32 31 30 29 28 27 26 25 24 23 22 21

C3

J4

1  
2  
3  
4  
5  
6  
7  
8  
9  
10  
11  
12  
13  
14  
15  
16  
17  
18  
19  
20

VDDIO = 2.5V  
VDD = 1.2V

C2

VDD  
VSS

C1

VDDIO  
VSS

J1

SERB SUPRA

Designed by Ananya Balike, IITGN