

## EE314 Digital Circuits Laboratory

### Experiment 3 Preliminary

1)

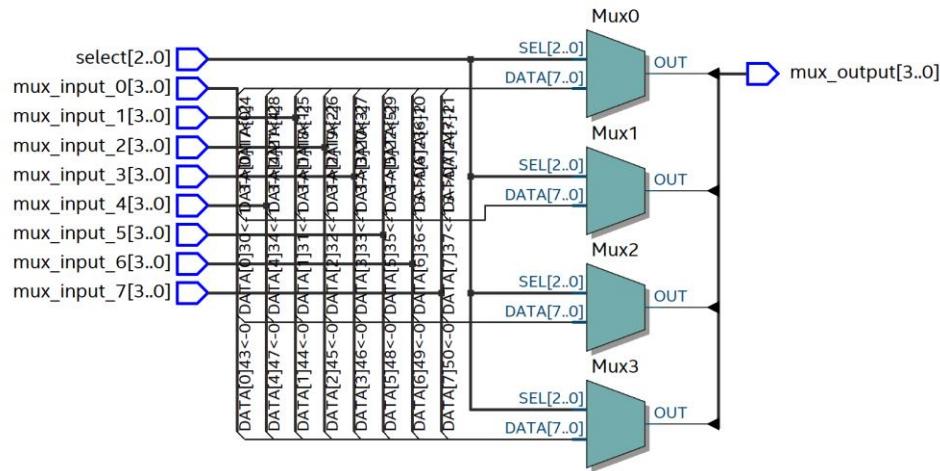


Figure 1: RTL Schematic for MUXx.v

2)

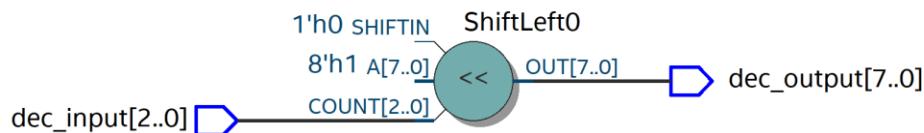


Figure 2: RTL Schematic for Decoder.v

3)

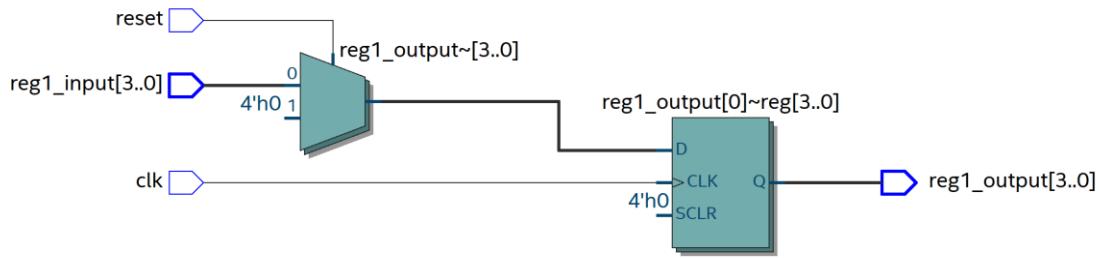


Figure 3: RTL Schematic for Reg1.v

4)

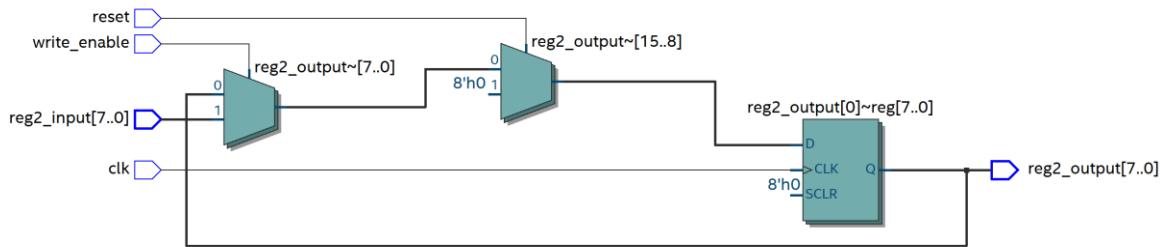


Figure 4: RTL Schematic for Reg2.v

5)

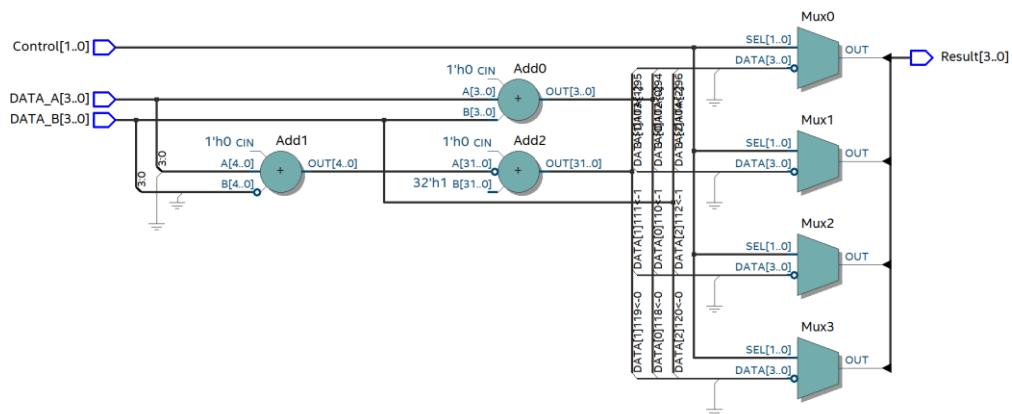


Figure 5: RTL Schematic for ALU.v

```

85000.00ns INFO cocotb.ALU
86000.00ns INFO cocotb.ALU
87000.00ns INFO cocotb.ALU
88000.00ns INFO cocotb.ALU
89000.00ns INFO cocotb.ALU
90000.00ns INFO cocotb.ALU
90000.00ns INFO cocotb.regression
90000.00ns INFO cocotb.regression
***** TEST ***** STATUS SIM TIME *****
(ns) REAL TIME (s) RATIO (ns/s) **
***** TEST ***** STATUS SIM TIME *****
.00 0.03 3526304.40 **
***** TEST ***** STATUS SIM TIME *****
.00 0.19 482692.63 **
***** TEST ***** STATUS SIM TIME *****
make[1]: Leaving directory '/d/METU-EE/EE-Sem-6/EE314/EXP3/ALUTest/Tests'
(base) D:\METU-EE\EE-Sem-6\EE314\EXP3\ALUTest\Tests>

```

Figure 6: Testbench Results for ALU.v

6)

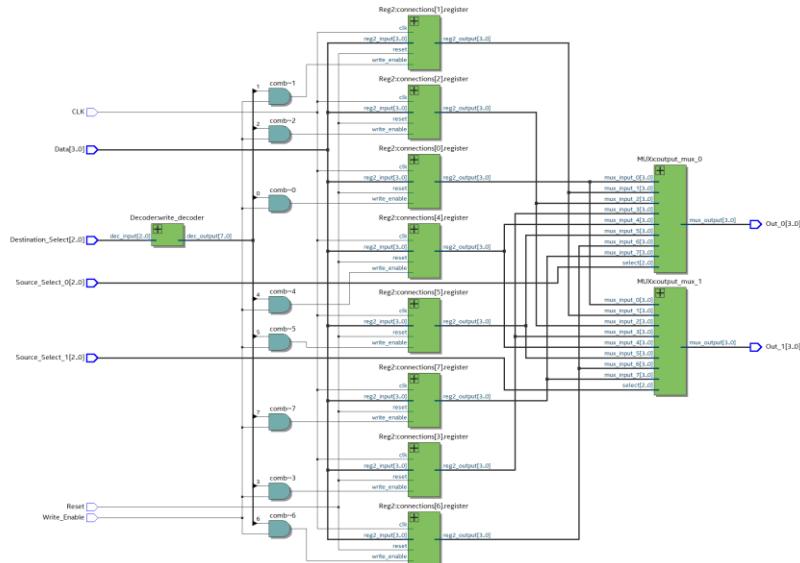


Figure 7: RTL Schematic for Register\_File.v

```
not detect Python virtual environment. Using system-wide Python interpreter
    .--ns INFO      gpi
registered
    0.00ns INFO      cocotb
    0.00ns INFO      cocotb
ing\Python\Python312\site-packages\cocotb
    0.00ns INFO      cocotb
    0.00ns INFO      cocotb_regression
    0.00ns INFO      cocotb_regression
928000.00ns INFO      cocotb
929000.00ns INFO      cocotb_regression
929000.00ns INFO      cocotb_regression
*****
REAL TIME (s) RATIO (ns/s) **
*****
0.05 19567661.41 **
*****
2.00 464948.29 **
*****
make[1]: Leaving directory '/d/METU-EE/EE-Sem-6/EE314/EXP3/Experiment3Materials-20250502/RegisterFileTest/Tests'
(base) D:\METU-EE\EE-Sem-6\EE314\EXP3\Experiment3Materials-20250502\RegisterFileTest\Tests>
```

Figure 8: Testbench Results for Register\_File.v