

Faculty of Engineering and Technology Department of Electrical and Computer Engineering

ENCS 2110

EXP 8 Post-Lab: Introduction to QUARTUSII Software

Student's name: Anas Al Sayed

Student's No.: 1221020

Section: 10

Instructor: Hanya Radwan

✓ Modules in the Design:

4-bit Adder → Adds two 4-bit numbers and outputs sum + carry.

4-bit AND Array → Performs bitwise AND on two 4-bit numbers.

4-bit OR Array → Performs bitwise OR on two 4-bit numbers.

Quad 2-to-1 Multiplexer → Selects between different operations based on control signals.

Overflow Detection → Detects overflow in addition.

