Bia 4, 9 Motivation for Concurrency in software.
Here are the Key motivations for embracing Confusions. O leyonmana Boos Multicone Processons enables structureous execution of tasks, wignificantly enhancing Computional throughput (2) Salability Adding more Comes allows for horizontal scaling, meeting increasing Computional demands more effectively the tereditional single Come systems (3) Resource Efficiency! Concusioned execution optimizes hardware susounce utilization like clu cycle and memory bandwidth imperoving overall efficiency. Home Processing Enables Landling of multiple if P Concusivently, Coucle) for tasks such as multimedia Processing (5) Energy Efficiency designs can potentially seedice power consumption task through dynamic laver management technique Computing Patform. sufore to a handware theftware infrastructure disigner Simultaneous execution of tasks across multiple PMorching units multiple instruction sigle data mimo

Components
1) Havidurare Components:
· Multicone Perocessons - CPU with a multiple Come on a single
chip allowing Confusered execution of tasks
· Accelerations: specialized hardware like Graphics fromthing
units (GPU) optimized for Parallel Computation
- Inter Connects - High speed Communication Chamnels & Couses,
switches, network) that facilitate data exchange blow
Processing units.
B) Solling Conferents:
· Parallel Brogeramming Model: Abstractions & Frameworks (MPI)
den Ml) that enable developer to expenses and manage
Parellel tasks of effectively.
. OS subbont: optimizations that enhance Performance 4
generale utilization in parallel envisionments
· Middle pone + soltware layers (noviding services Aucha
job scheduling, load balancing & fault tolerance to marage
distributed Computation
characteristics & benefits
· Performance Scalability: Dintaileten tasks across Comes on
Professions to handle langer workload efficiently
· Improved Throughput overlaps Computations to intrease
overall processing speed to reduce idle time
- Besource efficiency:
· support sual time inology;
· fault tolerance - Redundancy t comes handling mechanism

Apply Cottons O scientific simulations: for complex Computations in Physics Chemistry & engineering. 2) Big data Analytics: Accelerates data Processing for insights in finance, healthcase + marketing 6 AI/ML: facilitates tocalning and influence tests in deep learning model (9 HPC & (High Performance Computing): Paver mescasches In weather forecasting + molecular dynamics Could Combuting in micropero cessoss It sefers to the Capability of modern Brocessous execute multiple tasks on instructions simultaneously thereby increasing overall Computational throughput and Peuformance · Multicone Anchitecturie Come operate Independently, allowing Conquerrent execution of tapks · Simutaneous Multithereading (SMT) specialized units purform operations on multiple data elements simultaneously, beneficial for task like multimolia Boxessing Abblications! 1 Real time system tused in automotive system, mobatics interactive application 6 MPC! Aupport Scientific simulations, numerical Combutetions

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moota	Analytics + As :- Acco	elenates date Processing & ML task
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60 C.	um Application - only	ance Performance + scalability
1	web servers, do f (loud Combuting
10		
feature	Multicone Anchitecture	Hyper-Threeding Technologs
Definition	- Multiple Physical Core of	on Theis simultaneous multithreading
0	a single chil	1 technoloss.
Come Notes	independent Physical con	
		1 logical Processors
Execution	· Executes multiple thread	- share mesources blu multiple threads
	Simultaneously	or a single come
Scalubility	· Scales linearly with	· improves utilization of existing core
7	additional comes	nesources without additional cores
Perphonense	- Provide Significant Porpor	" in releases come utilization, enlancing
TO GOTTING TO	gains with each added Come	Performance but with diminishing
		neturne Compared to Physical Core
Resource	· Each Come has its own	· Threads share execution units &
	(1 - 1 -	
99	· Intel come it, AMD.	Intel Cone ia, xeon Processore
y	Ryzen Processon,	
	Sever grade elu	
	3000	
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theoreads i are the smallest unit of a hosnam. . They are Individual sequences of programmed instructions that an be executed independently by the os schedules They enable concurrent execution within a program allowing multiple thereads to sun simultaneously and share the same memory space. · Thoreads are lighter than processes and nequire fewer resources to create 4 manages · Threads can be categorized into una level threads Command by the application and Kernel level threads (marged by the OS) . I Program of atteast 1 thread (main thread). Though lifecycle in OS 1 News The theread is in this state when it's created but pasn't get streeted executing. At this point, the OS has allocated suspounces (like memory) but hasn't initiated execution. D Runnable/Rody: Once the thread is seeady to execute and waiting for the CPU time, it enters the hunnable on neady state. This means the thread is eligible to our, but the CPU scheduloes of the OS hasn't yet selected it for Mecution, 10 Running In this state, the thread is actively executing its instruction on the CPU. Only one thread can be in this state from CRU lost at any given time

9 Blocked Iwaiting A thread enters this state when I needs to wait for som event to occur on a susounce @ Dead Terminated A thread enters this state when it completes its execution on is explicitly terminated by the Program. Once a thread in terminated, its sussources guleased by the OS Execution of thereads in case o 10 Processon Cones-Each Physical Come in a CPU is instauctions independently 2) Theread Context Hardware threads allows a love nopidly, appearing as if multiple threeds we executing Almoltaneously. @ Simultaneous Multi-Threading (SMI) SMI technology allows each physical love to headle multipleads Concurrently by sharing lentain resources, such as execution unit + cache 9 05 Intraction The OS schedules Roftman threads onto available hendrione thereads. It manages theread exerction, ensuring that neceive of time based on their Phiosity on systems scheduling policies

Thread Country O Thread Greating Mechanism. Thereads are typically recented using a system calls on liberary fur Penovided by the os on lenggeromming long, gwitime Theread Affecter when Creating a thorard, attailutes such as stock size, periosity, and initial state can often be specified. These affairbates defermine how the thoread behaves and Interacts with other other theread and the system. (3) Memory Allocation upon (neation, the os allocate necessary nesources ton the thread, including memory space for stack and other thread specific data structure (4) Thoread Entery Point Thereads have a steerting point typically a fun or method that defines the initial code to execute when the thread begain execution. This entry Point is specified I during theread recention Thread Management 1 Scheduling: The os schedulen decides when and for how long each thread gives on the CPU 1 State tempition: Thereads transition blue diff states (eg. sunning, enoug, blacked) booked on their execution needs and Interaction with Agrilla gunounces

3 Confusionery Conterol Mechanism such as mutexes, semaphores, and condition Variables are und to 19ncheoniz ecources among thereads, kneventing date counciption and ensuring thought safety. (4) Thomas Teaching Hon's Theread Can terminate either voluntarily by Completing their task on Involuntarily due to curous explicit termination grequests Resources allocated the thorast are elevated upon termination. Thereads isside OS · Threads inside on OS refus to the fundamental units of execution managed directly by execution, scheduling them Provides mechanisms for synchronization t Communicat . The os Konnel manages thousand execution, scheduling them on 'Clu comes, and fravides mechanisms synchronization & Communication the threads

Handware thereads They are viortual execution units within a processor Cone that allow multiple thoreads to execute simutaneously on a single core - Achieved thorough technologia UKe hypen-thoroughny on simultaneous multithereading, which duplicate Core Components to hardle multiple instruction streams Concuparently · Improve CPU utilization by areducing idle time on Cones, enhancing multitasking Penformence across. applice Hon.

Grocess Synconomization. The main objective of process synchronization is to ensure that multiple process access shared susoures without interfering with each other 1 and to pocevent the possibility of inconsistent data done to Continent access Type of baces on the basis of synchronization -· Independent forocon; The execution of one Person process don not affect the execution of other Process. · Coopnative mores ! If process that can affect & on be affected by ohn Processes executing in the system processes des bios resources one shared in this when more than one mores is executing the same code on accessing the same memory on any stond variable In that Condition there is a possibility that the 0/1 is wrong. So for that all the process doing to the siace to say that my off in Carrent thin Condition in known on grace Condition. . It is a situation that may occur insid a louifical not section This happens when the occourt of multiple thought multiple execution in the the Critical section differs a cronding to the ander in which

Callical Section " It is a lade regress that Car be arrested by only one receive fororess of a time. . It seefers to a posit of a program's code that access should surpunces such as variable, of son files which must be accessed in an exclusive manner to avoid state Condition and maintain dato integrity. " only one known or thread In allowed to exercte & within the Critical rection at any given time to prevent Conflicts and ensure that the shared resources on not accessed simultaneously by multiple tu Hitie Binary somaphosic It is a type of semaphore that Can have only two Status: locked on unlocked. It acts as a simple flag that Portrols access to a shared resources on Cousial section by allowing only on process on theread to access it at a time when a process acquises the seraphone (locks it), it danger its state to locked, preventing other prolesses from accessing the suscences when the process successes the sumphase (unlock is) the stage charges back to unlocked, allowing other process to acquire it.

Schophone IL Is ar Integer variable which is used in mulus exclusive manner by various Concourent Cooperation Simpley Birary Syme Tourty Some - 00 to p Country Semaphone It is like a Counter that Controls access to multiple Instances of a suscences. It can have a scarge of values, typically starting from a posithe integer. Each three a process or threat weart to access the guesouries, it decrement the remaphorevalue. If the value is greater than process Continues. However ill the value is zero on regative, the process is forced to wait until the remaphore value increases again other process can increment the remaphore value when they are done uning the purounce This way, the semaphon effectively Count the available instances of the resource and regulate access accordingly

Cinery Semaphone 7 Entry cods P(s) + wait, strel, down. -) Exit code US) + stynel, workered, wit - Hody + Codi P(Simelphone S) while (s = =0); S = S - 1; -) (nitial section 1 v (Semephone S) 1 5 = St1) + runainly Section Gods Country Seniphane volue of 5 0 is the Court of susounces awarlable -1 1001 Pl Semaphon S) . . . Scholar 5 = 5-1', 11 (5<0) of Put Process in Suspended Ust steel (); cise (norma); Critical Section V(Simely More S) Y 8= 5+1; 14 (5(0) < select locus from unpended Ust ware us();

Senaphen mutex · It is an Integer. · it is an obj · It unes stynaling · It wasks upon the locking mechanism mechanism. · operation - wait, signal. · openhon - Lock, unlock · Typy: Birary, Country . It downt have any publish " It works with two . . H mutex Can only be atomic operations losait, modified by the process highall which can modify that is suggesting on sullaring a surome · If the process needs a susource, · If the mutex is locked then the process needs to and no susounce is free, So, the process needs to prefix in wait in the process quele a wait opereation until the and motex can only be reneiphone value in greater accessed once the lock then zero. in sulleared