Weekly Status Report

Name of the Engineer: Anas Khan

Working at Client Place: No **Name of Client: BB Training Centre**

Working in-house: No **Reporting to: Mr. Ajay Sagdeo/Dr. Praveen**

Duration: 02 FEB to 09 Feb 2024 **Number of hours Invested/week: 40 h/w**

Date	Duration	Description of work	Outcomes
2 February 2024	10 am to 1 pm	Introduction to Training programme	Understood Training plan.
	2 pm to 5pm	Introduction to System, Classifications, Design Level, and methodologies.	Understood basic digital concepts. Understood methodologies and flow of design.
	5pm to 6:30pm	Number System & Code Conversion and its C Lab.	Done practice in C language and Logic building
5 February 2024	10 am to 1 pm	Revision on Logic Gates.	Understood the concept and its applications.
	2 pm to 5 pm	Boolean Algebra and k-map. Comparator, adder, sub, Combinational Array Multiplier, and booth's algorithm (Sequential Multiplier)	Understood the Concept. Got additional knowledge on K-map.
	5 pm to 6:30 pm	Revision/Self-study and Discussion.	Doubts are cleared
6 February 2024	10 am to 1 pm	Restoring & Non-Restoring Division.	Understood division algorithm.
	1 pm to 5 pm	Quine Mc-cluskey's Tabulation Method	Get more understanding on concept discussion.
	5 pm to 6:30 pm	Practice questions.	Able to solve k map and tabulation method problems.
	10 am to 1 pm	Combinational MSI Blocks-Mux, Demux and its application, Two rail logic.	Able to implement any function using MSI blocks. MUX-Demux.

7 February 2024	2 pm to 3 pm	Encoder, Decoder and Expansion of MSI Blocks .	Able to implement any function using MSI blocks. Encoder-Decoder.
	3 pm to 6:30 pm	Practice Question Bank on MSI Blocks.	Got additional knowledge on MSI Blocks.

Date	Duration	Description of work	Outcomes
8 February 2024	10 am to 1 pm	Introduction to ALU. Functional Decomposition.	Understood Designing approach.
	2 pm to 5pm	Flip-Flops and latches. Characteristics equation, excitation table.	In-depth understanding of concepts and applications of flip flops & latches achieved.
	5pm to 6:30pm	Practice on Flip Flop Conversions and Self-Study.	Done practice in Conversions.
9 February 2024	10 am to 1 pm	Different gates using Demux.	Understood applications.
	2 pm to 5 pm	Learning PLDs-PLA,PROM & PAL. Programmable Switches.	Understood the Concept.
	5 pm to 6:30 pm	Revision/Self-study and Discussion.	Doubts are cleared