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Company: NXP

Questions:

* Introduce yourself.
* Write behavioral model for differential amplifier.
* What happens when multiple contributions are done to same signal inside analog block.
* What is transition filter, laplace\_nd filter.
* What do you know about connect module? What happens inside E2R connect module.
* What is absdelta
* Write behavioral model for LDO.
* What are checkers. How was the checker for Power on reset implemented.
* Write wreal model for comparator.
* Do you know the difference between SV and wreal modelling.
* Write SV code for clock divider by 2 circuit.