Syntax.in

```
<list instructions> ::= <instruction> | <instruction><list instructions>
<instruction> ::= <decl instruction> |<rw instruction> |<assign instruction> |
<if instruction> | <for instruction>
<decl instruction> ::= <var decl instruction> | <arr decl instruction>
<var decl instruction> ::= var space <identifier> <var type> ;
<arr decl instruction> ::= var <identifier> [ <unsigned integer> ] <var type>;
<var type> ::= real | integer | long integer
<rw instruction> ::= <rw keywords> ( <identifier> );
<rw keywords> ::= read | write
<assign instruction> ::= <identifier> := <expression>
<list conditions> )  otherwise <if instruction> | condition ( <list conditions> )
<list conditions> :== <condition> | <condition> && <list conditions>
<condition> ::= <expression> <relation> <expression>
<relation> ::= :< | :> | :== | :!= | :>= | :<=
```

Syntax.in 1

```
<operator_gr1> ::= +|-
<operator_gr2> ::= *|/
<op_const> ::= <real>|<identifier>

<expression> ::= <expression> <operator_gr1> <term> | <term>
<term> ::= <term> <operator_gr2> <factor> | <factor>
<factor> ::= ( <expression> ) | <op_const>

<for_instruction> ::= step_loop ( <identifier>, <identifier>| <constant>, <constant>, , <identifier>|
```

Syntax.in 2