PROPRIETARY NOTICE: REVISIONS THIS DOCUMENT CONTAINS PROPRIETARY INFORMATION DATE APPROVED OF INTEL CORP. NEITHER THE DOCUMENT NOR SAID PROPRIETARY INFORMATION SHALL BE PUBLISHED, REPRODUCED, COPIED, DISCLOSED OR USED FOR ANY PURPOSE OTHER THAN CONSIDERATION OF THIS DOCUMENT WITHOUT THE EXPRESS WRITTEN PERMISSION OF A DULY AUTHORIZED REPRESENTATIVE OF ALTERA CORPORATION NOTES: UNLESS OTHERWISE SPECIFIED 1. ASSEMBLY HOUSE TO VISUALLY VERIFY BOARD BEING ASSEMBLED HAS ALTERA P/N 100-0321004-B1 REV B IN ETCH ON BOTTOM SIDE. 2. BOARDS MUST BE BAKED FOR A DURATION OF 8 HOURS AT 250 DEGREES FARENHEIT IMMEDIATELY PRIOR TO ASSEMBLY. 3. ASSEMBLE BOARD PER IPC-A-610(LATEST REVISION) STANDARD. 4. ALL MATERIALS USED MUST BE ROHS COMPLIANT. 5. HANDLE MOISTURE SENSITIVE DEVICES(MSD) PER MANUFACTURE'S CALL OUT ON MSD LABEL. IF MSD LABEL ÍS MISSING OR NOT LEGIBLE THEN ALL PLASTIC PACKAGED COMPONENTS ARE TO BE CONSIDERED MSD LEVEL 6 AND USE EIA/JEP 113-A FOR HANDLING. 6. SQUARE PIN PAD DENOTE PIN 1 OF ICs/CONNECTORS, POSITIVE END 7. REWORK MUST BE APPROVED BY CUSTOMER BEFORE BEING ACCEPTIBLE USE IPC-R-700(LATEST REVISION) FOR ALL PCA REWORK APPROVED. 8 INSTALL BAR CODE LABEL APPROX WHERE SHOWN, INCLUDING S/N. 9. VENDOR TO KEEP QUALITY CONTROL INK STAMPINGS OFF SIGNAL TRACES AND PADS EVEN IF COVERED BY SOLDERMASK. PRIMARY SIDE SHOWN DWN PSG Board Design Team 11-Sep-17 DSGN ENGR - XX-XXX-XXX PSG Board Design Team DASH NEXT ASSY USED ON PCB ASSEMBLY APPLICATION CYCLONE 10 GX DEVELOPMENT KIT BOARD TOLERANCES: DEC .X ± THIRD ANGLE DEC .XX ± .01 PROJECTION UNLESS OTHERWISE SPECIFIED: DWG NO. DEC .XXX ± .005 DIMENSIONS ARE IN INCHES IN DEC .XXXX ± .005 | CONTRACT NO. 130-0321329-A1 ACCORDANCE WITH ANSI Y14.5. FRACTIONS ± ----DRILL TOLERANCES ARE PER 10387 X X X X X X X X XANGLES ± 5° SCALE: 1/1 SHEET 1 OF