



1. Product Features

Excellent spurious-free dynamic range (SFDR) performance

SFDR at Nyquist frequency

83 dBc @5 MHz output frequency

80 dBc @10 MHz output frequency

73 dBc @20 MHz output frequency

The signal-to-noise ratio (SNR) is 77dB (at an output frequency of 5MHz and a sampling rate of 125MSPS)

The data format is two's complement or straight binary

Differential current output: 2 mA to 20 mA

Power consumption: 135 mW @ 3.3 V

Power-down mode power consumption: 15mW @3.3V

On-chip 1.2V reference source

CMOS compatible digital interface

28-pin TSSOP package

Edge-Triggered Latch

2. Product Overview

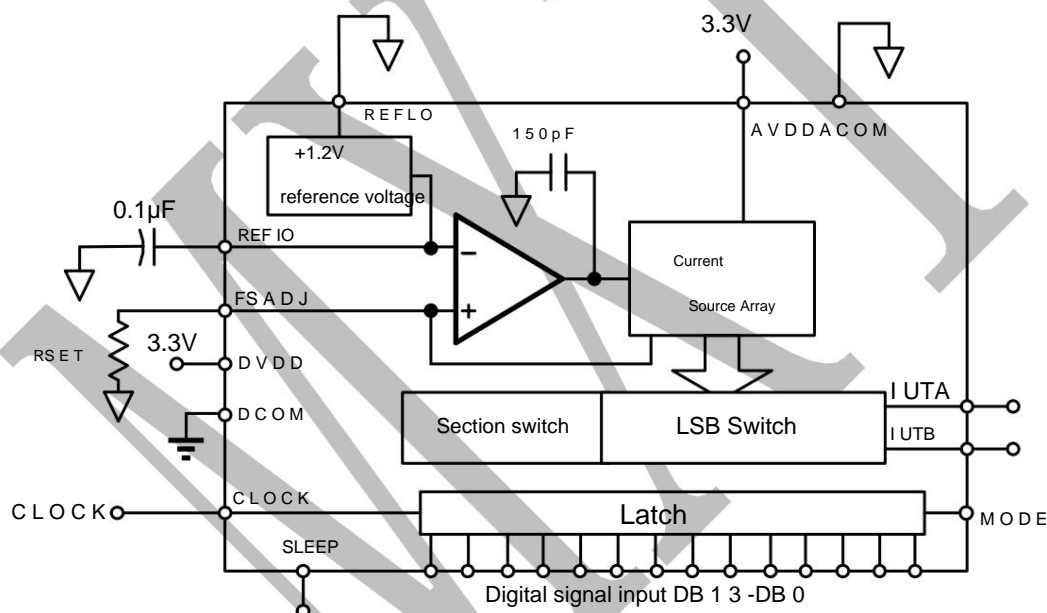
The MXT2144 is a 14-bit precision, high bandwidth, low power DAC chip specifically designed for

The transmit signal path of the communication system is optimized. It provides excellent AC and DC performance and supports the highest 210 MSPS update rate.

The low power consumption of the MXT2144 makes it ideal for portable and low power applications.

The MXT2144 also integrates edge-triggered input latches and a 1.2 V temperature-compensated bandgap reference.

3. Structure diagram





4. TSSOP28 TSSOP28 TSSOP28 TSSOP28 package

