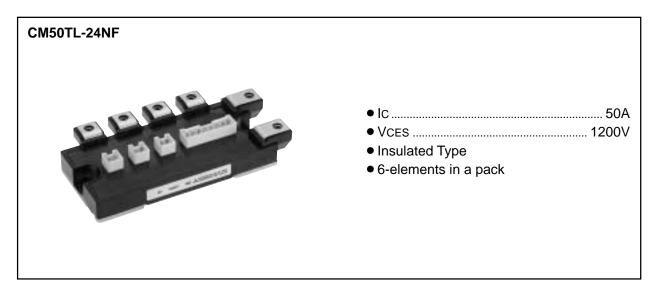
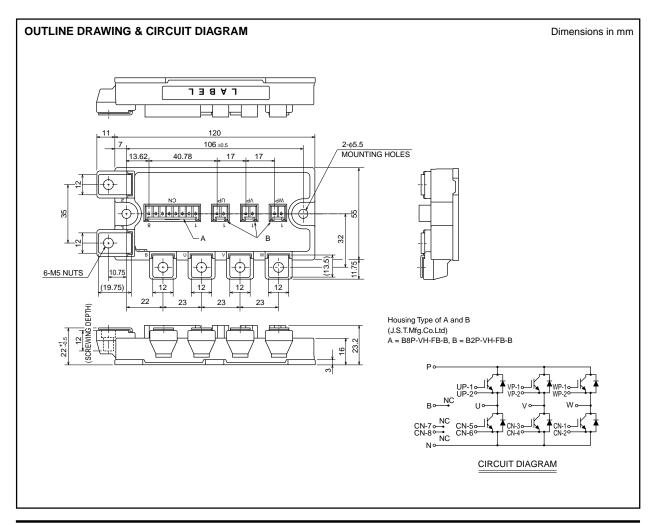
**HIGH POWER SWITCHING USE** 



### **APPLICATION**

AC drive inverters & Servo controls, etc



#### **HIGH POWER SWITCHING USE**

#### ABSOLUTE MAXIMUM RATINGS (Tj = 25°C)

Symbol	Parameter	Conditions	Ratings	Unit	
VCES	Collector-emitter voltage	G-E Short		1200	V
VGES	Gate-emitter voltage	C-E Short		±20	V
Ic	Collector current	DC, $Tc = 94^{\circ}C^{*1}$	50	Α	
Ісм	Collector current	Pulse	(Note 2)	100	Α
IE (Note 1)	F-n-itte v acceptant			50	Α
IEM (Note 1)	Emitter current	Pulse	(Note 2)	100	Α
PC (Note 3)	Maximum collector dissipation	Tc = 25°C		390	W
Tj	Junction temperature			-40 ~ <b>+1</b> 50	°C
Tstg	Storage temperature			-40 ~ <b>+</b> 125	°C
Viso	Isolation voltage	Main Terminal to base plate, AC 1 min.		2500	V
_	T	Main Terminal M5		2.5 ~ 3.5	N•m
_	Torque strength	Mounting holes M5	2.5 ~ 3.5	N•m	
_	Weight	Typical value		350	g

#### **ELECTRICAL CHARACTERISTICS (Tj = 25°C)**

0	Parameter	Test conditions		Limits			1.1
Symbol				Min.	Тур.	Max.	Unit
ICES	Collector cutoff current	VCE = VCES, VGE = 0V		_	_	1	mA
VGE(th)	Gate-emitter threshold voltage	IC = 5.0mA, VCE = 10V		6	7	8	V
IGES	Gate leakage current	VGE = VGES, VCE = 0V		_	_	0.5	μΑ
VCE(sat)	Collector-emitter saturation voltage	IC = 50A, VGE = 15V	Tj = 25°C	_	2.1	3.0	V
			Tj = 125°C	_	2.4	_	
Cies	Input capacitance	VCE = 10V VGE = 0V		_	_	8.5	nF
Coes	Output capacitance			_	_	0.75	nF
Cres	Reverse transfer capacitance				_	0.17	nF
QG	Total gate charge	VCC = 600V, IC = 50A, VGE = 15V		_	250	_	nC
td(on)	Turn-on delay time	Vcc = 600V, Ic = 50A		_	_	100	ns
tr	Turn-on rise time			_	_	50	ns
td(off)	Turn-off delay time	VGE1 = VGE2 = 15V		_	_	300	ns
tf	Turn-off fall time	RG = $6.3\Omega$ , Inductive load switching operation IE = $50A$		_	_	350	ns
trr (Note 1)	Reverse recovery time			_	_	100	ns
Qrr (Note 1)	Reverse recovery charge			_	2		μС
VEC(Note 1)	Emitter-collector voltage	IE = 50A, VGE = 0V		_	_	3.8	V
Rth(j-c)Q	Thermal resistance	IGBT part (1/6 module)*1		_	_	0.32	°C/W
Rth(j-c)R	Thermal resistance	FWDi part (1/6 module)*1		_	_	0.43	°C/W
Rth(c-f)	Contact thermal resistance	Case to fin, Thermal compound Applied (1/6 module)*2		_	0.085	_	°C/W
RG	External gate resistance				_	96	Ω

<sup>\*1 :</sup> Tc measured point is just under the chips.

- Note 1. IE, VEC, trr & Qrr represent characteristics of the anti-parallel, emitter to collector free-wheel diode (FWDi).

  2. Pulse width and repetition rate should be such that the device junction temp. (Tj) does not exceed Tjmax rating.

  - 3. Junction temperature (Tj) should not increase beyond 150°C.

    4. Pulse width and repetition rate should be such as to cause neglible temperature rise.

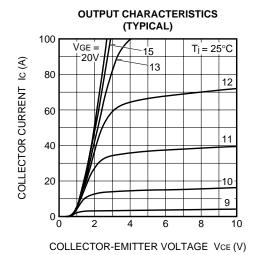


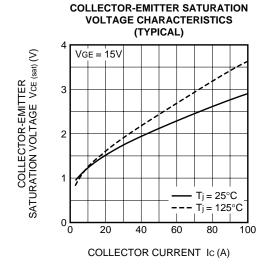
If you use this value, Rth(f-a) should be measured just under the chips.

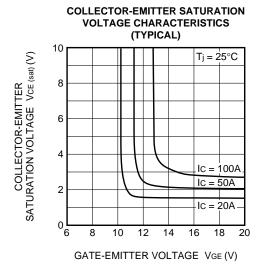
<sup>\*2 :</sup> Typical value is measured by using Shin-etsu Silicone "G-746".

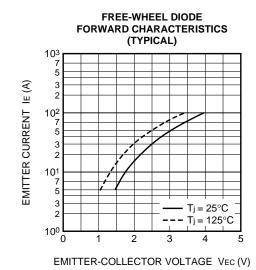
#### **HIGH POWER SWITCHING USE**

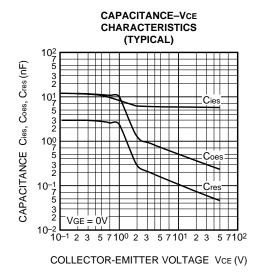
#### **PERFORMANCE CURVES**

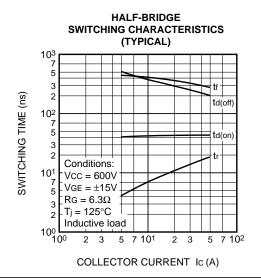






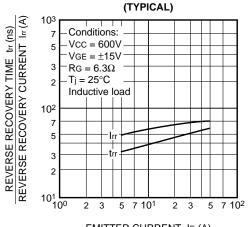




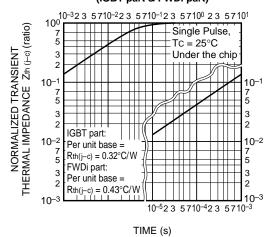


#### **HIGH POWER SWITCHING USE**

### REVERSE RECOVERY CHARACTERISTICS OF FREE-WHEEL DIODE

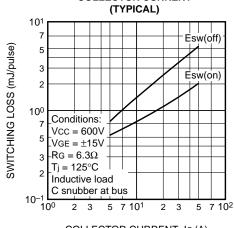


#### TRANSIENT THERMAL **IMPEDANCE CHARACTERISTICS** (IGBT part & FWDi part)

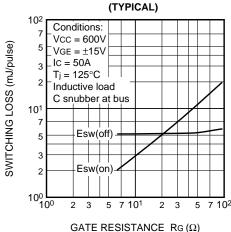


EMITTER CURRENT IE (A)

SWITCHING LOSS vs. **COLLECTOR CURRENT** 

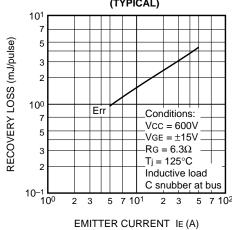


SWITCHING LOSS vs. **GATE RESISTANCE** 

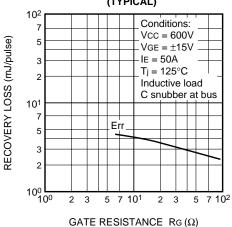


COLLECTOR CURRENT Ic (A)

**RECOVERY LOSS vs. IE** (TYPICAL)



RECOVERY LOSS vs. **GATE RESISTANCE** (TYPICAL)



### **HIGH POWER SWITCHING USE**

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