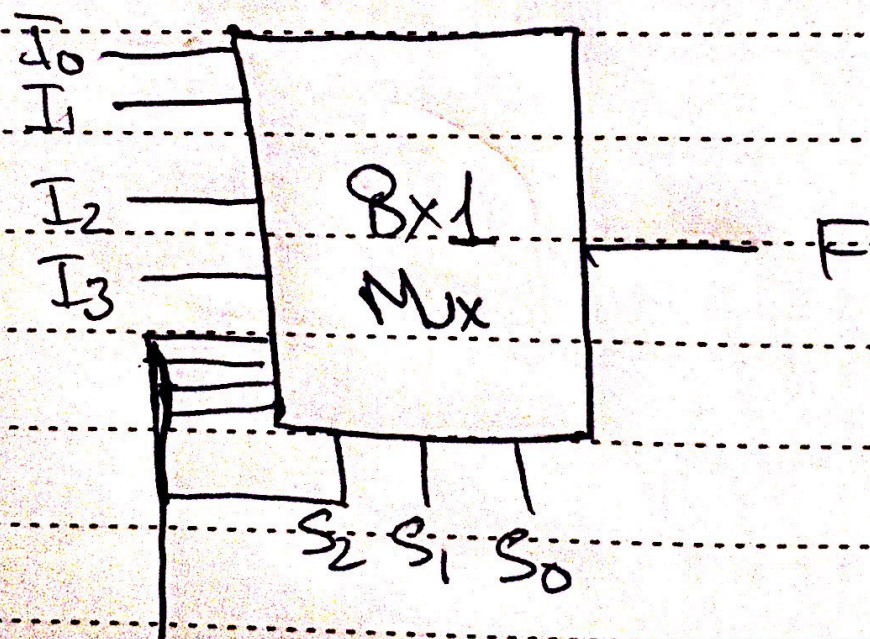


Qs 1a)

S_2	S_1	S_0	F
X	0	0	I_0
X	0	1	I_1
X	1	0	I_2
X	1	1	I_3



Logic 0 (=)