



RISC-V and Open Source Chip Design (Postdoc)

Hochschule München is among the largest German universities of applied sciences, located at the heart of Munich, Germany. The working group "AEMY" focuses on safe, secure and smart systems. Our main working areas include RISC-V processor design, WebAssembly runtime development and open source chip design tools.

We are looking for an experienced researcher to become member of the leadership team of our research group. Your role is to be involved with various running research projects around RISC-V and open source chip design. Along conducting your own research, you are involved in the guidance of PhD students and the acquisition of new research opportunities with funding agencies and industry.

Your experience profile should ideally cover one of the following topics:

- RISC-V processor design
- Safety and/or security of embedded systems
- EDA tool research
- Open source chip design and its tools

Join us now:

- to become a member and leader of a great team
- to be in the epicenter of RISC-V and open source chip design
- to shape the research agenda
- to be actively involved with relevant industry partners in the area

If you are interested, please get in touch:

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Find more on our website:

<https://aemy.cs.hm.edu>

