On-Chip/Off-Chip Interconnect Design (PhD Position)

Hochschule München is among the largest German universities of applied sciences, located at the heart of Munich, Germany. The working group "AEMY" focuses on safe, secure and smart systems. Our main working areas include RISC-V processor design, WebAssembly runtime development and open source chip design tools.

For our research projects we are looking for a qualified and motivated new team member. This position focuses on the evaluation and design of methods and tools for the design of optimized on-chip and off-chip interconnect of complex chiplet-based heterogeneous chip designs. You will be building on existing open source designs and tools flows and explore their extension to an open source hardware/software-codesign tool flow for novel architectures.

Your profile should cover:

- Fundamental understanding of computer architecture
- Basic understanding of digital chip design
- Ideally some experience with interconnect design

Join us now:

- to become a member of a great team in Munich, Germany
- to be in the epicenter of RISC-V and open source chip design
- to pursue a PhD degree at a small and focused research group
- to be actively involved with relevant industry partners in the area

If you are interested, please get in touch:
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Find more on our website:
https://aemy.cs.hm.edu