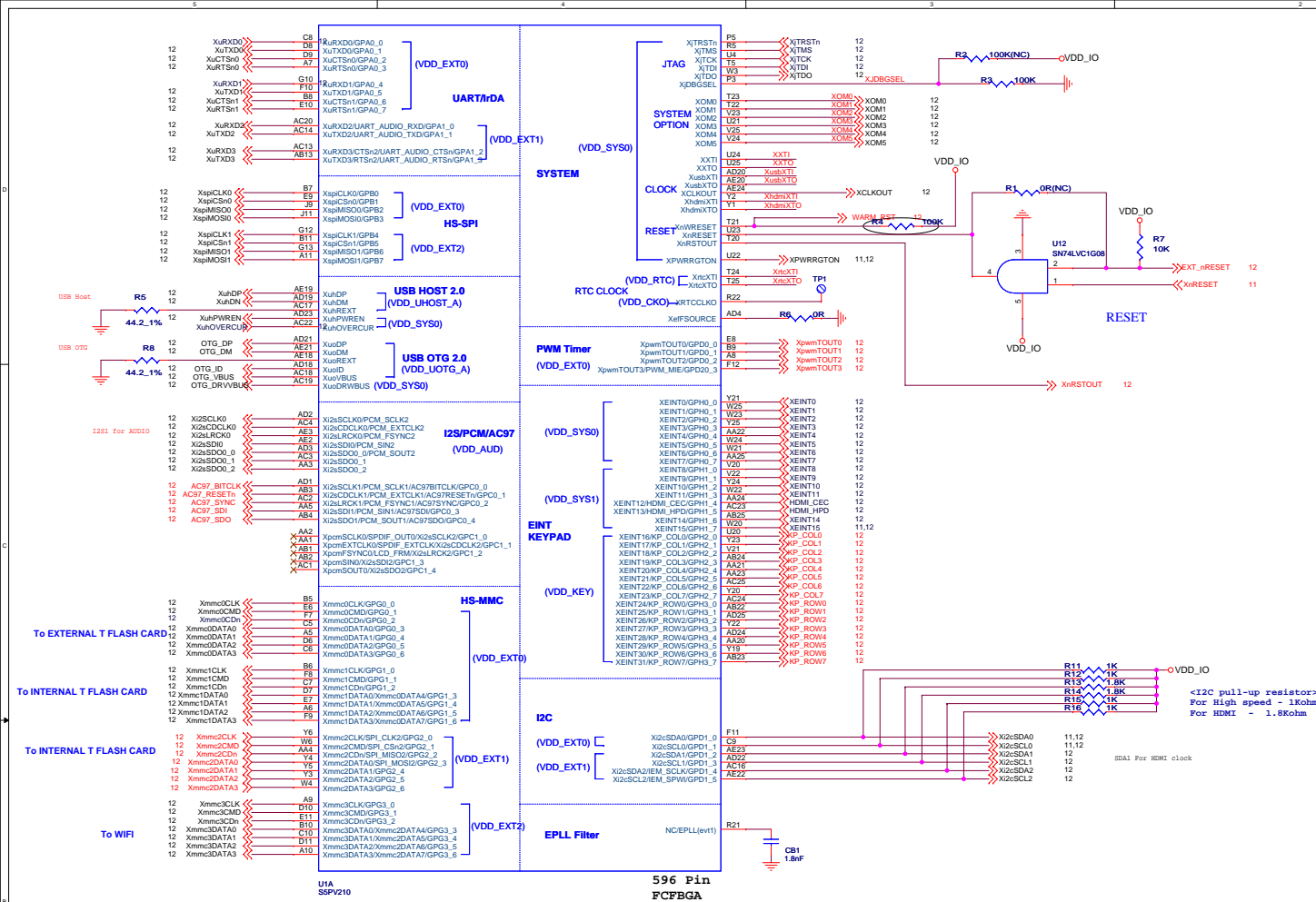


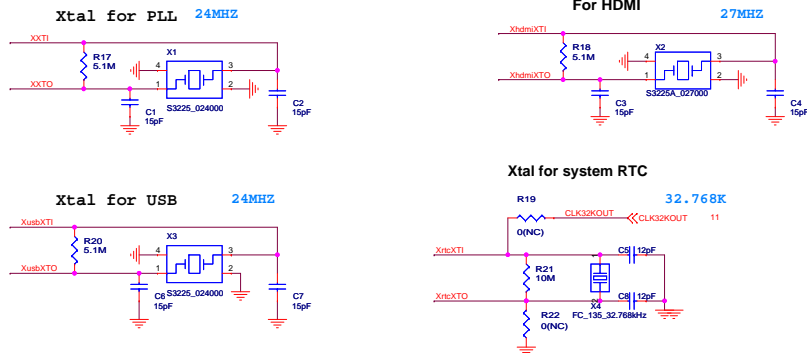
	5	4	3	2	1	
D	<div>Revision History</div> <div>V2.0 Initial Released</div>					D
C						C
B						B
A	<div><div></div><div>Title<Title></div><div><div>Size A</div><div>Document Number<Doc></div><div>Rev A</div></div><div>Date: Monday, August 20, 2012Sheet 1 of 12</div></div>					A
	5	4	3	2	1	

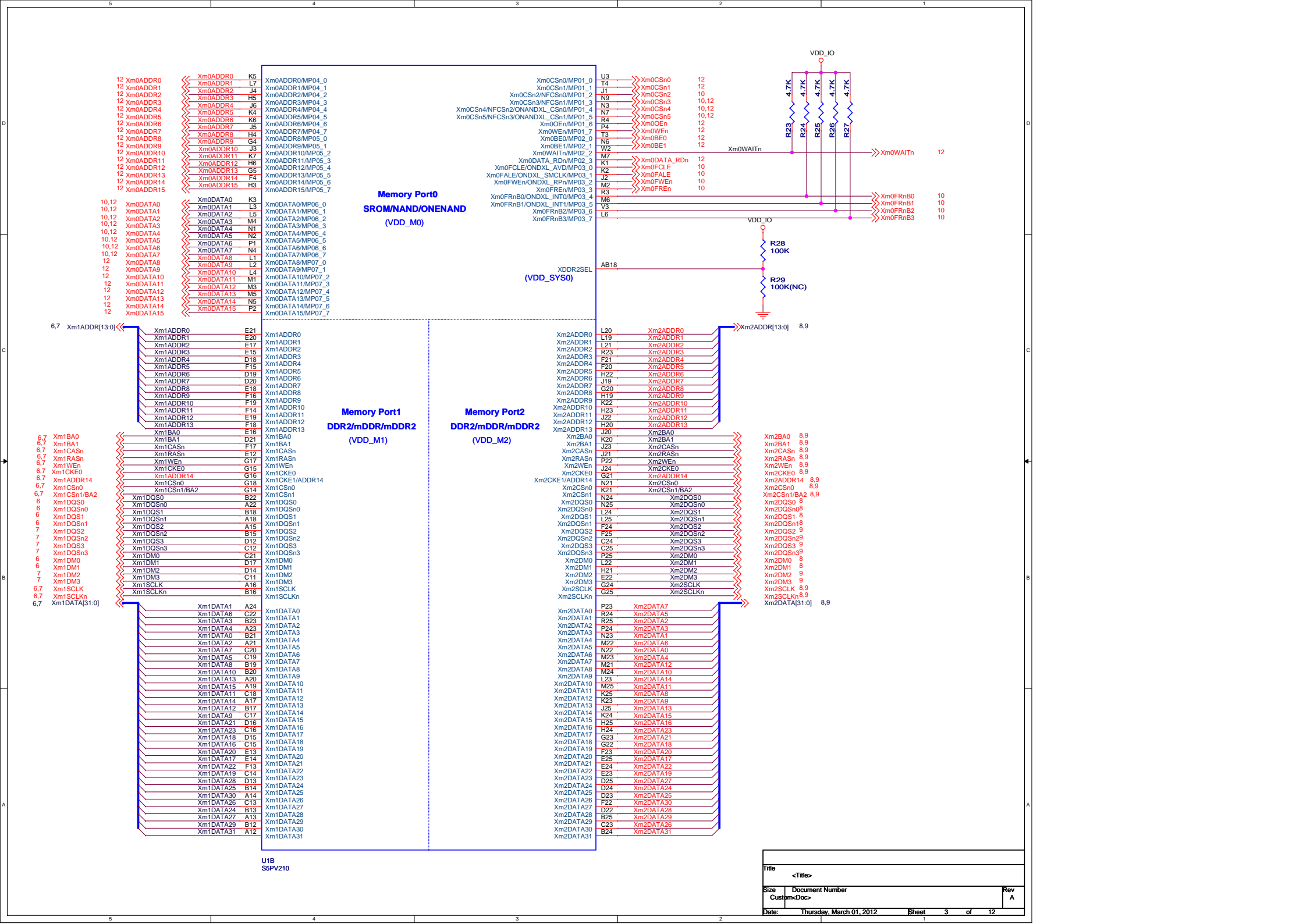


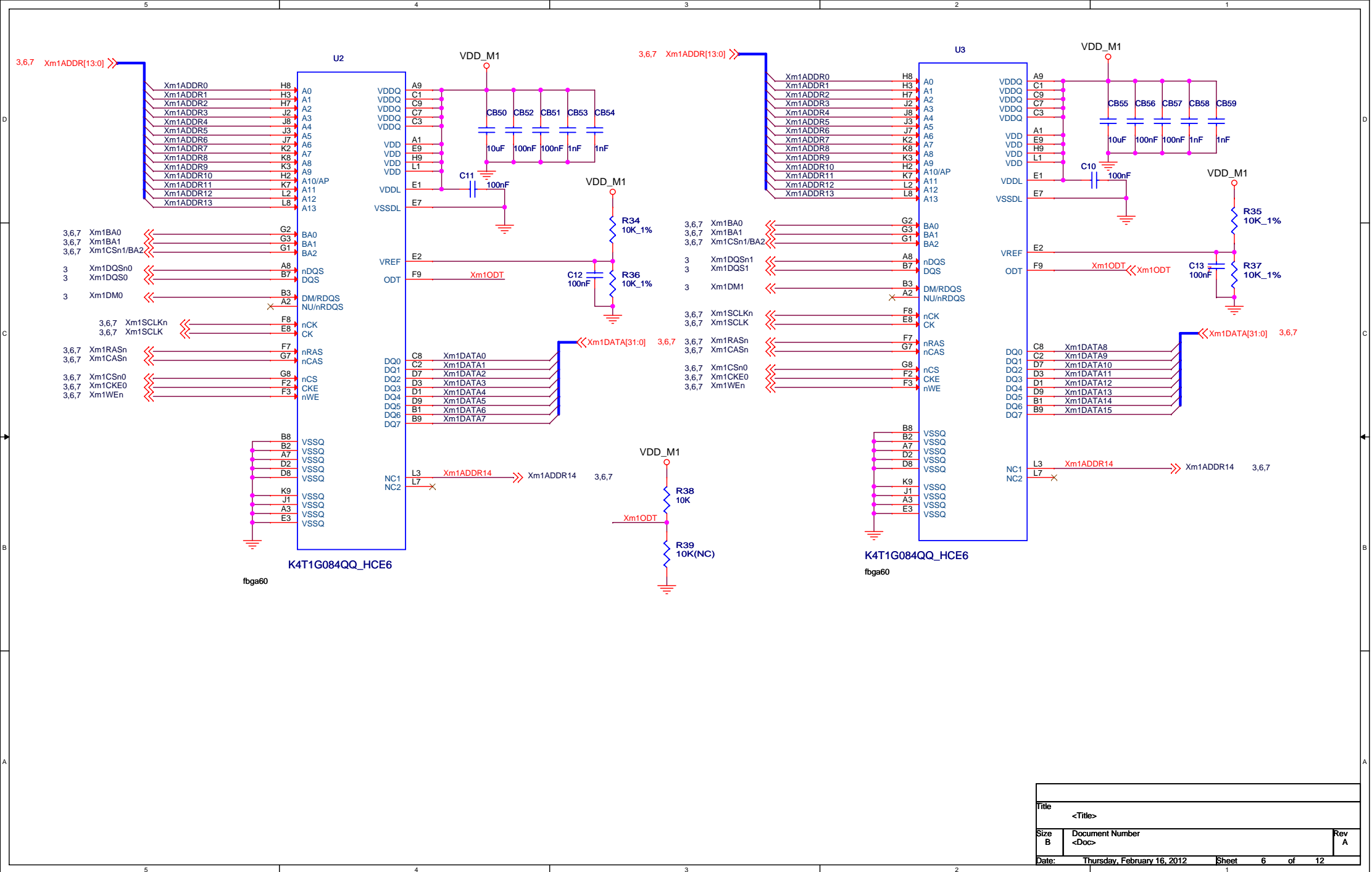
BootingMode Option Selection

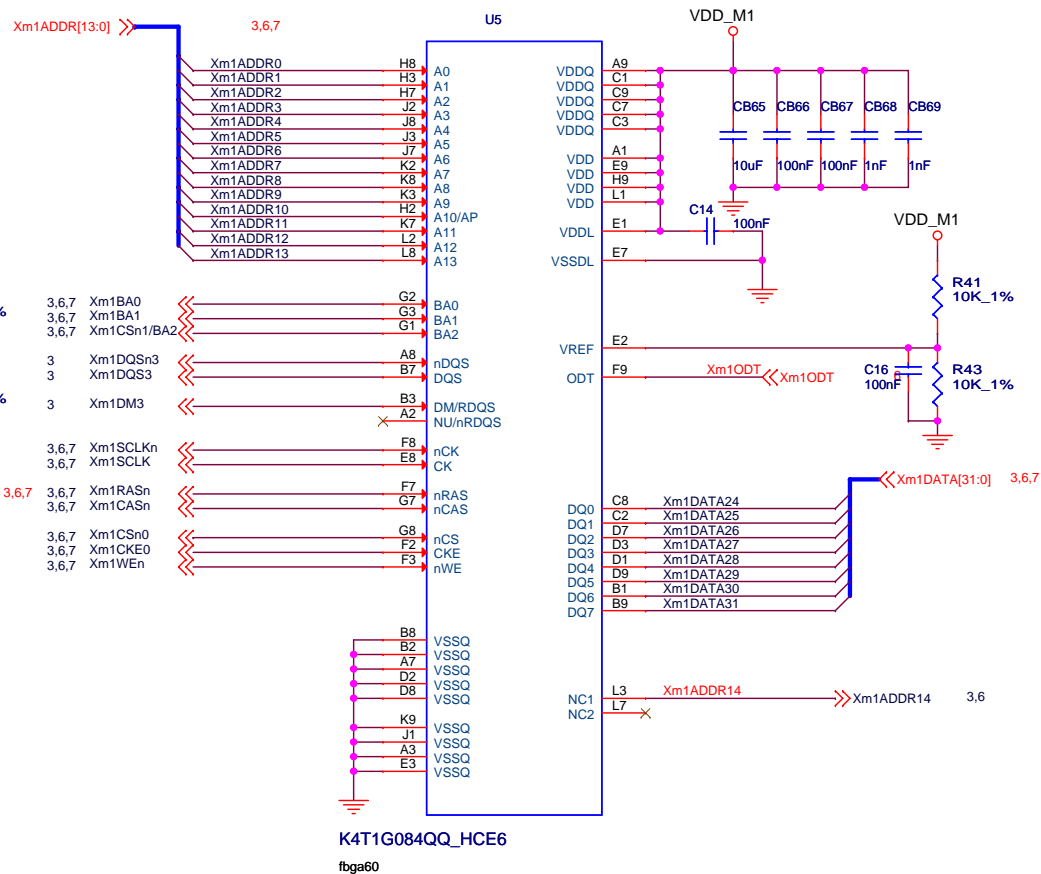
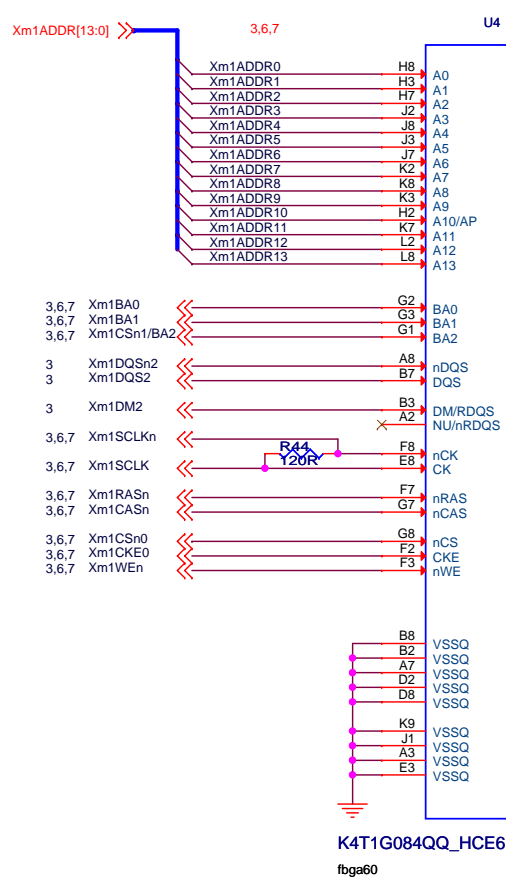
<Silk>				
OM[5]	OM[4:1]	OM[0]	Storage	
Boot Seq.	0 0 0 0		NAND 512B-4cycle	
	0 0 0 1		NAND 2KB-5cycle	
	0 0 1 0		NAND 4KB-5cycle 8-BCC	
0: Storage	0 0 1 1		NAND 4KB-5cycle 16-BCC	
	0 1 0 0	0:	OneNAND Mux(Audi)	
1: USB-> UART-> Storage	0 1 0 1	0:	OneNAND Mux(Audi)	
	0 1 1 0	1:	SD/MMC	
	0 1 1 1	X-TAL (USB)	eMMC (4bit)	
	1 0 0 0		Reserved	
0: Storage	1 0 0 1		NAND 2KByte Page, 4cycle	
	1 0 1 0		1ROM NOR boot	
	1 0 1 1		eMMC (8bit)	

X-Tal Clock Source

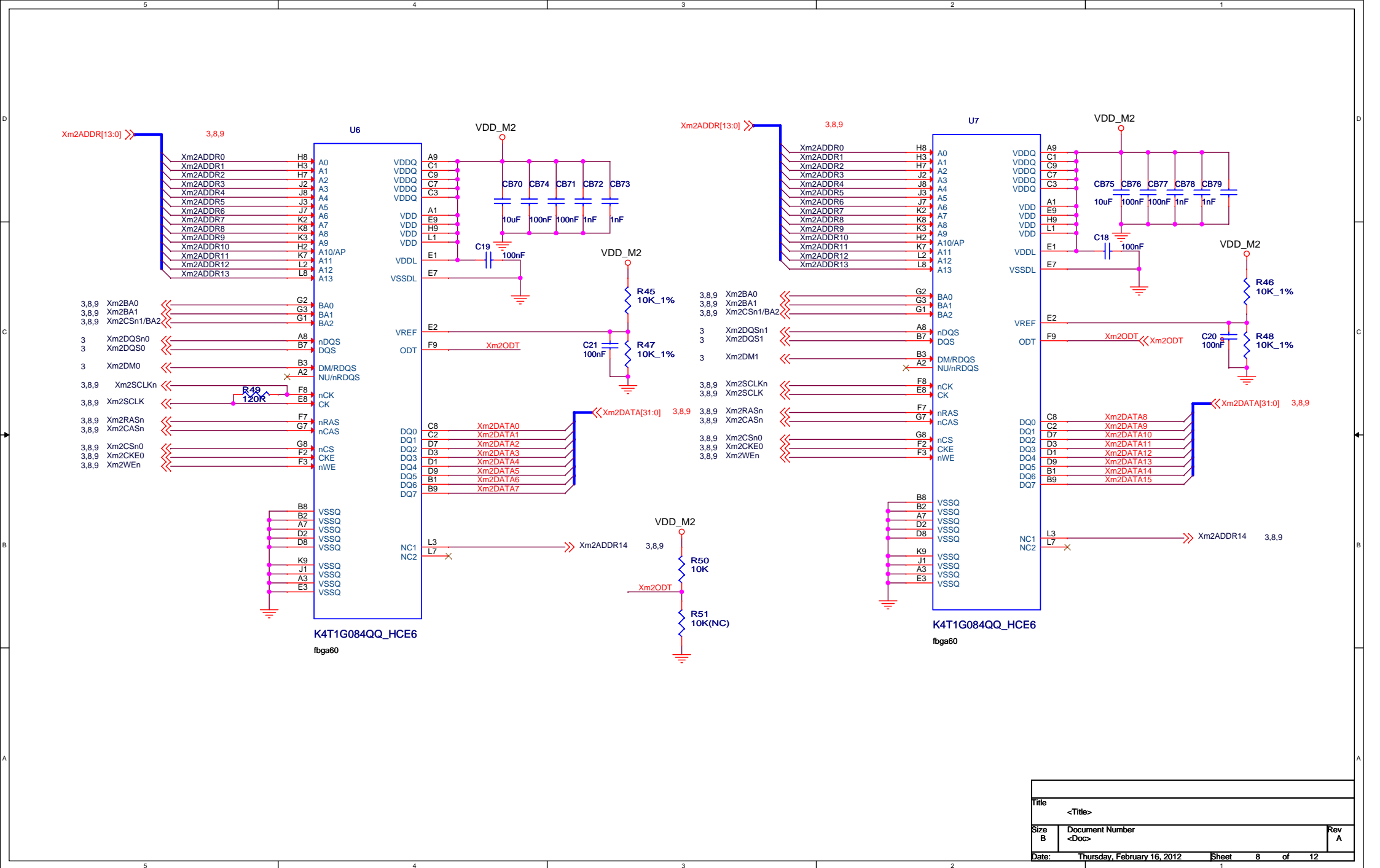


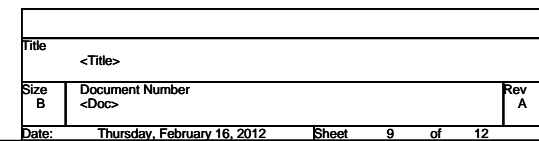


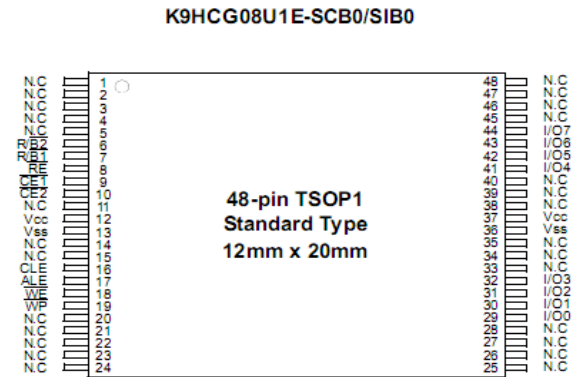




Title		
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Size	Document Number	Rev
B	<Doc>	A
Date:	Thursday, February 16, 2012	Sheet 7 of 12



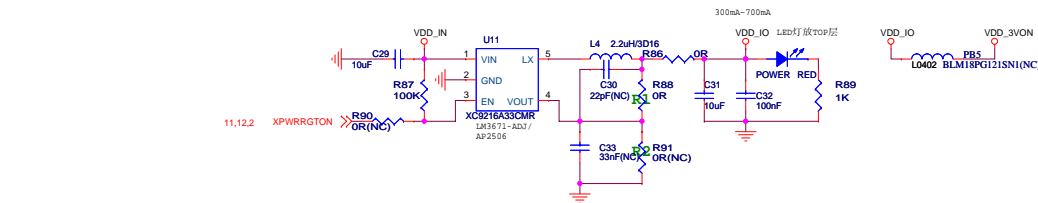
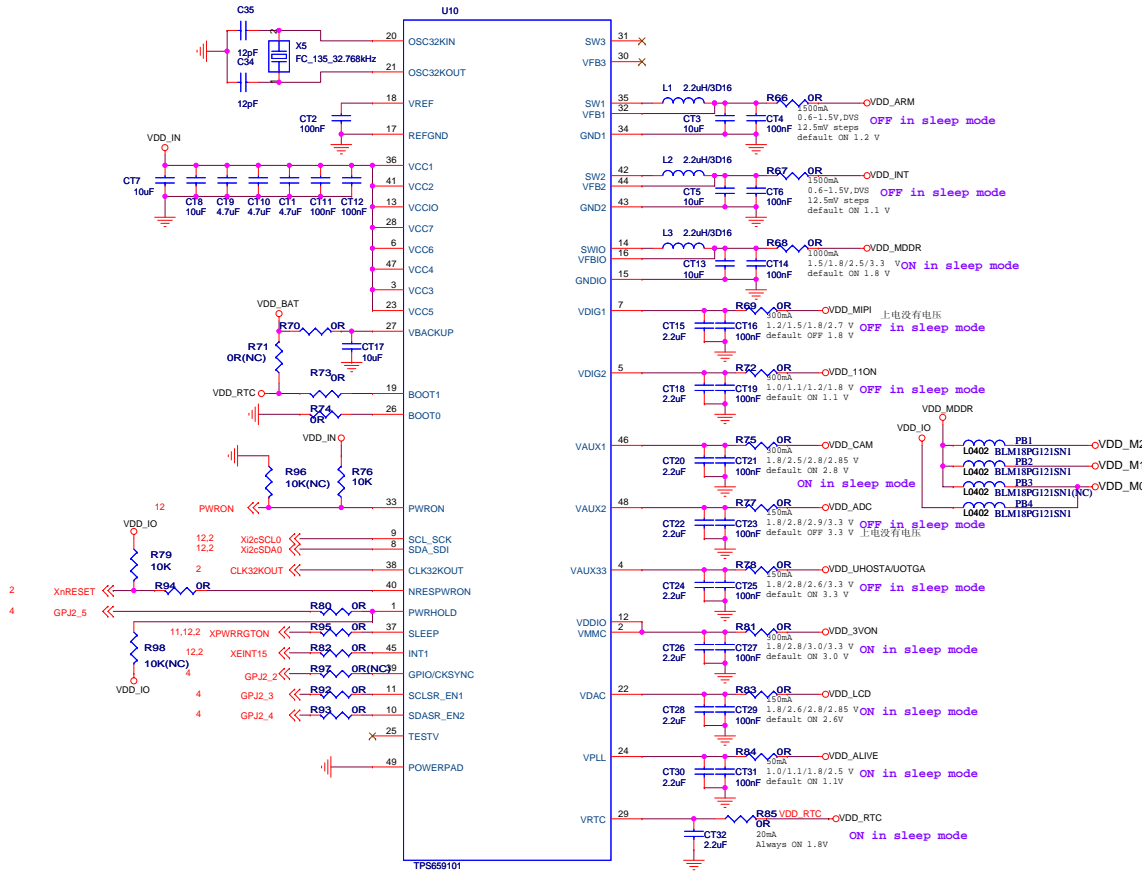




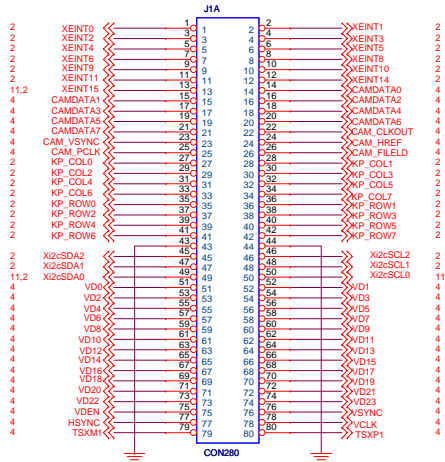
Title <Title>			
Size B	Document Number <Doc>		Rev A
Date:	Thursday, February 16, 2012	Sheet 10 of 12	

CORE POWER ASSIGN LISTS

Symbol	On/Off @ Reset	On/Off @ Sleep	Min	Typ	Max
VDDALIVE	On	On	1.05	1.1	1.35
Symbol	On/Off @ Reset	On/Off @ Sleep	Min	Typ	Max
VDDALIVE	On	On	1.05	1.1	1.35
VDDAPLL VDDMPLL VDDPEPLL VDDVPLL	On	Off	1.0	1.1	1.2
VDDINT	On	Off	1.15	1.2	1.35
			1.15	1.3	1.35
VDDARM	On	Off	1.15	1.2	1.35
			1.15	1.3	1.35
VDD_M0	On	On	1.7	1.8/2.5/3.3	3.6
VDD_M1	On	On	1.15	1.2/1.8	1.9
VDD_M2	On	On	1.15	1.2/1.8	1.9
VDD_SYS0	On	On	1.7	1.8/2.5/3.0	3.6
VDD_SYS1	On	On	1.7	1.8/2.5/3.0	3.6
VDD_EXT0	On	On	1.7	1.8/2.5/3.0	3.6
VDD_EXT1	On	On	1.7	1.8/2.5/3.0	3.6
VDD_EXT2	On	On	1.7	1.8/2.5/3.0	3.6
VDD_CKO	On	On	1.7	2.5/3.0	3.6
VDD_RTC	On	On	1.7	2.5/3.0	3.6
VDD_LCD	On	On	1.7	2.5/3.0	3.6
VDD_CAM	On	On	1.7	2.5/3.0	3.6
VDD_AUD	On	On	1.7	2.5/3.0	3.6
VDD_MIPI_D	On/Off	Off	1.05	1.1	1.15
VDD_MIPI_PLL	On/Off	Off	1.05	1.1	1.15
VDD_UOTG_A	On/Off	Off	3.0	3.3	3.6
VDDI_UOTG_D	On/Off	Off	1.05	1.1	1.15
VDD_UHOST_A	On/Off	Off	3.0	3.3	3.6
VDDI_UHOST_D	On/Off	Off	1.05	1.1	1.15
VDD_MODEM	On	On	1.7	2.5/3.0	3.6
VDD_KEY	On	On	1.7	2.5/3.0	3.6
VDD_ADC	On	On/Off	3.0	3.3	3.6
VDD_DAC_A	On/Off	Off	3.0	3.3	3.6
VDD_DAC	On/Off	Off	3.0	3.3	3.6
VDD_HDMI	On/Off	Off	1.05	1.1	1.15
VDD_HDMI_PLL	On/Off	Off	1.05	1.1	1.15
VDD_HDMI_OSC	On/Off	Off	3.0	3.3	3.6
VDD_MIPI_A	On/Off	Off	1.7	1.8	1.9



LM3671-ADJ: $V_{out}=0.5(R1/R2+1)$
 AP2506: $V_{out}=0.6(R1/R2+1)$



BTB-0.8-808 *2
BTB-0.8-808 *2

