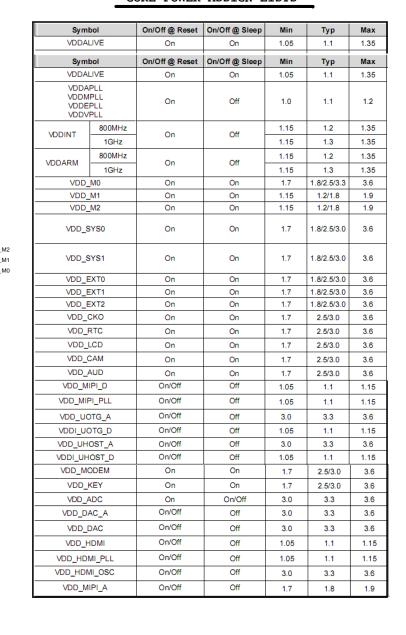


CORE POWER ASSIGN LISTS



C35	U10		
X5 20	OSC32KIN	SW3	31_X
12pF FC_135_32.768kHz		VFB3	30X
	OSC32KOUT		L1 2.2uH/3D16
12pF 18	VREF	SW1 3	35 R66 OR OVDD_ARM
VDD_IN CT2 100nF 17			1500mA 1500mA 1500mA 1500mA 1500mA 1500mA 1500mA 1500mA 1500mB 1500mA 1500mB 15
36			12 22:42006
CT7 + + + + + 41	VCC1	SW2	R67 OR OVDD_INT
10uF C18 C19 C110 C11 C111 C112 13 10uF 4.7uF 4.7uF 4.7uF 100nF 100nF	7.7	VFB2 4	CTS CT6 0.6-1.5V,DVS OFF in sleep mode 12.5mV steps default oN 1.1 V
10uF 4.7uF 4.7uF 4.7uF 100nF 100nF 28	VCC7	GND2	
6		SWIO 1	L3 2.2uH/3D16 R68 OR OVD_MDDR
47	VCC4	VFBIO	CT13
3	VCC3	GNDIO	<u> </u>
VDD_BAT 23	VCC5	VDIG1 7	7 R69 OR OVDD_MIPI 上电没有电压
R70 0R 27	VBACKUP		CT15 CT16 1.2/1.5/1.8/2.7 V OFF in sleep mode 2.2uF 100nF default OFF 1.8 V
0R(NC) < 10uF			₩ ₩ ₩ ₩ ₩ ₩ ₩ ₩ ₩ ₩ ₩ ₩ ₩ ₩ ₩ ₩ ₩ ₩ ₩
R73 _{0R}		VDIG2	5 RZV_QOR_OVDD_110N CT18
VDD_RTC 0 19 26 26	BOOT1 BOOT0		2.2uF 100nF default ON 1.1 V VDD_MDDR
			VDD_IO VDD_IO
4	٧	VAUX1	16 R75 OR OVD_CAM OVD_CAM LQ402 BLMISRGI2ISN1 OVDD_M3
R96 R76 10K(NC) 10K			2 2UF 100nF default ON 2.8 V 10402 RIMIRPGIZISNI VDD_M
} ` ` ` `	PWRON V	4	ON in sleep mode CM2 BLANISFGISINI(NC) CVDD_MC CM2 BLANISFGISINI(NC) CVDD_MC CM2 BLANISFGISINI(NC) CVDD_MC CM2 BLANISFGISINI(NC) CM2 BLANISFGISINI
VDD_IO	PWKON V	VAUX2	S5mA OVDU ADC L0402 BLM18PG121SN1 CT22 CT23 1.8/2.8/2.9/3.3 V OFF in sleep mode
12.2 Xi2cSCL0 — 9 12.2 Xi2cSDA0 — 8	SCL_SCK SDA_SDI		2.2uF 100nF default OFF 3.3 V 上电没有电压
R79 14,2 XIZCSIDA0 (\(\) 10K 2 CLK32KOUT (\(\) 38		AUX33	R78 OR OVDD_UHOSTA/UOTGA
2 XnRESET R94 OR 40	NRESPWRON		CT24
4 GPJ2_5 《 R80 OR 1	PWRHOLD		12
11,12,2 XPWRRGTON (R95 OR 37	SLEEP \	VDDIO 2 VMMC	R81 OR OVDD_3VON
R98 12.2 XEINT15 (R82 OR 45 10K(NC) R97 OP(NC3)	INT1		CT26 1.8/2.8/3.0/3.3 V ON in sleep mode 2.2uF 100nF default ON 3.0 V
4 GPJ2_2 (GPIO/CKSYNC	2	—————————————————————————————————————
DO2 0D 40	_	VDAC -	22 R83 QR OVDD_CD CT28 = CT29 1.8/2.6/2.8/2.85 VON in sleep mode 2.2uF 100nf default on 2.6V
4 GPJ2_4 《	SDASR_EN2 TESTV		2.2uF 100nF default ON 2.6V
1		VPLL 2	
1 49	POWERPAD		CT30 CT31 1.0/1.1/1.8/2.5 V ON in sleep mode 2.20F 100nF default ON 1.1V
			Z.Zui
		VRTC 2	OR JOHN THE
			CT32 20mA ON in sleep mode 2.2uF Always ON 1.8V
	TPS659101		-
			300mA-700mA
	VDD_IN		VDD_IO LED灯放TOP层 VDD_IO VDD_3VON
1 VIN LX 5 08 008 009 BM MSRG12SNIND			
R87 (1) 2 GND C33 R88 C31 POWER RED R89			
100K 3 EN VOUT 4 22PF(NC) R\$1.0R 100F 100F 100F 1 100F			
11,12,2 XPWRRGTON XOR(NC)	XC9216A33CMR LM3671-ADJ/		→
	AP2506	C33	NEC R01 (NEC OR(NC)
South Control of the			
<u> </u>			
LM3671-ADJ: Vout=0.5(R1/R2+1)			
AP2506: Vout=0.6(R1/R2+1)			
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