

Integrantes:

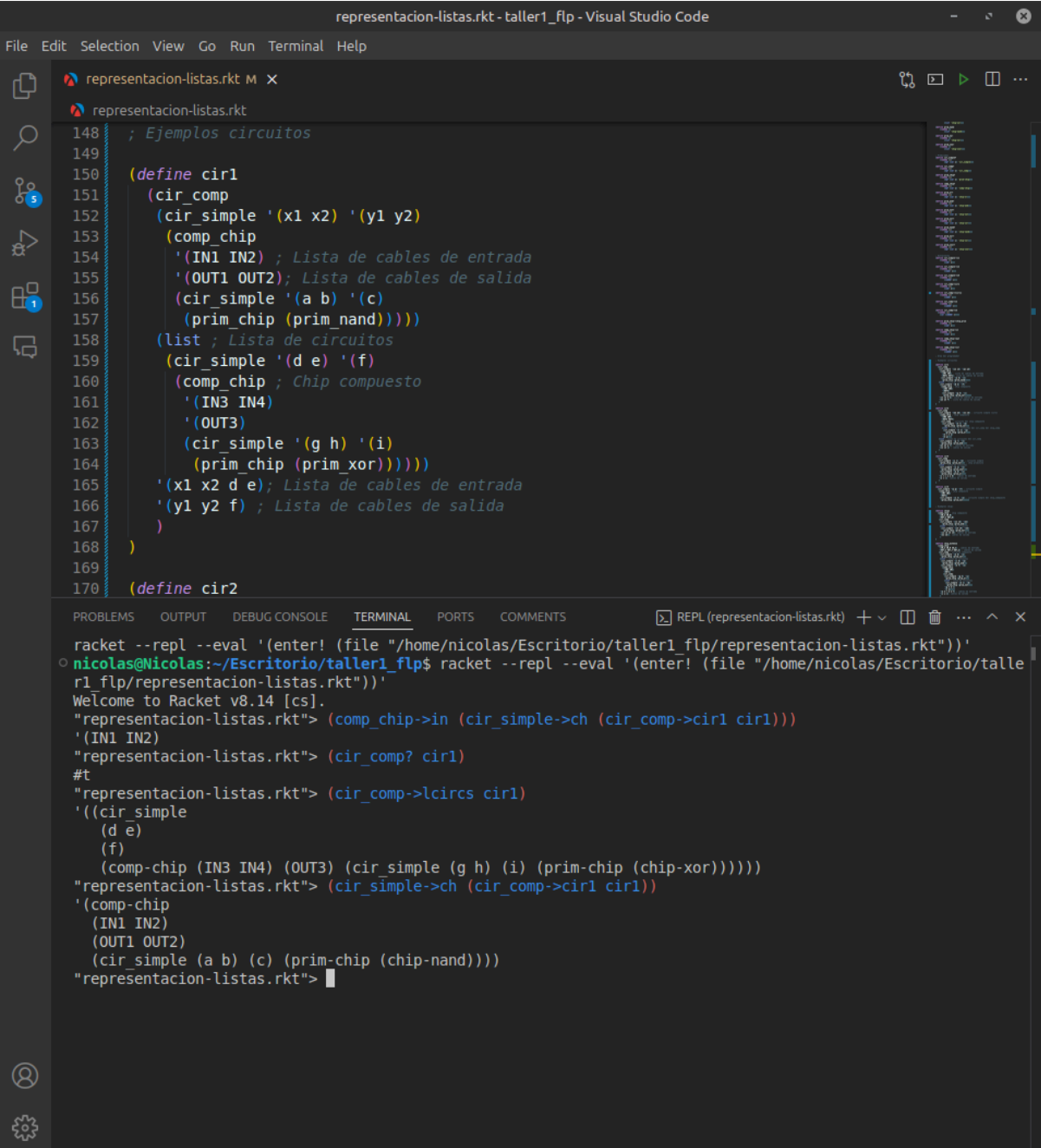
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CAPTURAS REPRESENTACIÓN - LISTAS:



The image shows a Visual Studio Code editor window titled "representacion-listas.rkt - taller1_flp - Visual Studio Code". The editor displays Racket code for circuit representation. The code defines two circuit components, `cir1` and `cir2`, using lists to represent circuit components and connections. The code is as follows:

```
148 ; Ejemplos circuitos
149
150 (define cir1
151   (cir_comp
152     (cir_simple '(x1 x2) '(y1 y2)
153       (comp_chip
154         '(IN1 IN2) ; Lista de cables de entrada
155         '(OUT1 OUT2); Lista de cables de salida
156         (cir_simple '(a b) '(c)
157           (prim_chip (prim_nand))))))
158   (list ; Lista de circuitos
159     (cir_simple '(d e) '(f)
160       (comp_chip ; Chip compuesto
161         '(IN3 IN4)
162         '(OUT3)
163         (cir_simple '(g h) '(i)
164           (prim_chip (prim_xor))))))
165     '(x1 x2 d e); Lista de cables de entrada
166     '(y1 y2 f) ; Lista de cables de salida
167   )
168 )
169
170 (define cir2
```

The terminal window at the bottom shows the execution of the Racket code using the `racket --repl --eval` command. The output displays the internal representation of the circuits as lists, confirming the structure defined in the code.

```
racket --repl --eval '(enter! (file "/home/nicolas/Escritorio/taller1_flp/representacion-listas.rkt"))'
nicolas@Nicolas:~/Escritorio/taller1_flp$ racket --repl --eval '(enter! (file "/home/nicolas/Escritorio/talle
r1_flp/representacion-listas.rkt"))'
Welcome to Racket v8.14 [cs].
"representacion-listas.rkt"> (comp_chip->in (cir_simple->ch (cir_comp->cir1 cir1))
'(IN1 IN2)
"representacion-listas.rkt"> (cir_comp? cir1)
#t
"representacion-listas.rkt"> (cir_comp->lcir1 cir1)
'((cir_simple
  (d e)
  (f)
  (comp-chip (IN3 IN4) (OUT3) (cir_simple (g h) (i) (prim-chip (chip-xor))))))
"representacion-listas.rkt"> (cir_simple->ch (cir_comp->cir1 cir1))
'(comp-chip
  (IN1 IN2)
  (OUT1 OUT2)
  (cir_simple (a b) (c) (prim-chip (chip-nand))))
"representacion-listas.rkt">
```

```
representacion-listas.rkt - Visual Studio Code
File Edit Selection View Go Run Terminal Help

representacion-listas.rkt M X
representacion-listas.rkt
168
169
170 (define cir2
171   (cir_comp
172     (cir_simple '(w1 w2) '(z1 z2) ; circuito simple (cir1)
173       (comp_chip ; chip compuesto
174         '(INX INY)
175         '(OUTX OUTY)
176         (cir_comp ; circuito del chip compuesto
177           (cir_simple '(k l) '(m)
178             (prim_chip (prim_xor)))
179           (list ; lista de circuitos del cir_comp del chip_comp
180             (cir_simple '(n o) '(p)
181               (prim_chip (prim_or)))
182             '(k l n o)
183             '(m p)))
184           (list ; lista de circuitos del cir_comp
185             (cir_simple '(q r) '(s)
186               (prim_chip (prim_and)))
187             '(w1 w2 q r) ; cables de entrada
188             '(z1 z2 s) ; cables de salida
189           )
190         )
191   )

PROBLEMS OUTPUT DEBUG CONSOLE TERMINAL PORTS COMMENTS
REPL (representacion-listas.rkt) + - [ ] [X] ... ^ x

racket --repl --eval '(enter! (file "/home/nicolas/Escritorio/taller1 flp/representacion-listas.rkt"))'
nicolas@Nicolas:~/Escritorio/taller1 flp$ racket --repl --eval '(enter! (file "/home/nicolas/Escritorio/
taller1 flp/representacion-listas.rkt"))'
Welcome to Racket v8.14 [cs].
"representacion-listas.rkt"> (cir_comp? cir2)
#t
"representacion-listas.rkt"> (cir_comp->c1 cir2)
'(w1 w2 q r)
"representacion-listas.rkt"> (cir_comp->c2 cir2)
'(z1 z2 s)
"representacion-listas.rkt"> (cir_comp->lcircs cir2)
'((cir_simple (q r) (s) (prim-chip (chip-and))))
"representacion-listas.rkt"> (cir_simple? (cir_comp->cir1 cir2))
#t
"representacion-listas.rkt"> (comp_chip->in (cir_simple->ch (cir_comp->cir1 cir2)))
'(INX INY)
"representacion-listas.rkt"> (comp_chip->cir (cir_simple->ch (cir_comp->cir1 cir2)))
'(cir_comp
  (cir_simple (k l) (m) (prim-chip (chip-xor)))
  ((cir_simple (n o) (p) (prim-chip (chip-or)))
   (k l n o)
   (m p)))
"representacion-listas.rkt"> |
```

```
representacion-listas.rkt - taller1_flp - Visual Studio Code
File Edit Selection View Go Run Terminal Help

representacion-listas.rkt
214
215 ; Ejemplos chips
216
217 (define chip1
218   (comp_chip ; chip compuesto
219     '(IN_A IN_B)
220     '(OUT_A OUT_B)
221     (cir_comp
222       (cir_simple '(i1 i2) '(o1)
223         (prim_chip (prim_and)))
224       (list ; lista de circuitos
225         (cir_simple '(i3 i4) '(o2)
226           (prim_chip (prim_nor)))
227         '(i1 i2 i3 i4); cables de entrada
228         '(o1 o2)); cables de salida
229       )
230     )
231 )

PROBLEMS OUTPUT DEBUG CONSOLE TERMINAL PORTS COMMENTS REPL (representacion-listas.rkt)
racket --repl --eval '(enter! (file "/home/nicolas/Escritorio/taller1_flp/representacion-listas.rkt"))'
nicolas@Nicolas:~/Escritorio/taller1_flp$ racket --repl --eval '(enter! (file "/home/nicolas/Escritorio/taller1_flp/representacion-listas.rkt"))'
Welcome to Racket v8.14 [cs].
"representacion-listas.rkt"> (comp_chip? chip1)
#t
"representacion-listas.rkt"> (comp_chip->in chip1)
'(IN_A IN_B)
"representacion-listas.rkt"> (comp_chip->out chip1)
'(OUT_A OUT_B)
"representacion-listas.rkt"> (comp_chip->cir chip1)
'(cir_comp
  (cir_simple (i1 i2) (o1) (prim-chip (chip-and)))
  ((cir_simple (i3 i4) (o2) (prim-chip (chip-nor)))
   (i1 i2 i3 i4)
   (o1 o2)))
"representacion-listas.rkt"> (cir_simple? (comp_chip->cir chip1))
#f
"representacion-listas.rkt"> (cir_comp? (comp_chip->cir chip1))
#t
"representacion-listas.rkt"> (cir_comp->cir1 (comp_chip->cir chip1))
'(cir_simple (i1 i2) (o1) (prim-chip (chip-and)))
"representacion-listas.rkt"> (cir_simple->c1 (cir_comp->cir1 (comp_chip->cir chip1)))
'(i1 i2)
"representacion-listas.rkt"> (cir_simple->c2 (cir_comp->cir1 (comp_chip->cir chip1)))
'(o1)
"representacion-listas.rkt"> (cir_simple->ch (cir_comp->cir1 (comp_chip->cir chip1)))
'(prim-chip (chip-and))
"representacion-listas.rkt"> (cir_comp->lcircons (comp_chip->cir chip1))
'(((cir_simple (i3 i4) (o2) (prim-chip (chip-nor)))
  (i1 i2 i3 i4)
  (o1 o2)))
"representacion-listas.rkt">
```

The image shows a Visual Studio Code window titled "representacion-listas.rkt - taller1_flp - Visual Studio Code". The editor displays a Racket script with the following code:

```

230 )
231
232 (define chip2
233   (comp_chip
234     '(IN A IN B IN C) ; cables de entrada
235     '(OUT_X OUT_Y OUT_Z) ; cables de salida
236     (cir_comp ; circuito compuesto
237       (cir_simple '(a b) '(x)
238         (prim_chip (prim_xor)))
239       (list ; lista de circuitos
240         (cir_simple '(c d) '(y)
241           (prim_chip (prim_and)))
242         (cir_simple '(e f) '(z)
243           (comp_chip
244             '(IN1 IN2)
245             '(OUT1)
246             (cir_comp
247               (cir_simple '(g h) '(j)
248                 (prim_chip (prim_or)))
249               (list ; lista de circuitos
250                 (cir_simple '(k l) '(m)

```

The bottom panel shows the REPL (REPL (representacion-listas.rkt)) with the following output:

```

Welcome to Racket v8.14 [cs].
"representacion-listas.rkt"> (comp_chip? chip2)
#t
"representacion-listas.rkt"> (comp_chip->in chip2)
'(IN A IN B IN C)
"representacion-listas.rkt"> (comp_chip->out chip2)
'(OUT_X OUT_Y OUT_Z)
"representacion-listas.rkt"> (comp_chip->cir chip2)
'(cir_comp
  (cir_simple (a b) (x) (prim-chip (chip-xor)))
  ((cir_simple (c d) (y) (prim-chip (chip-and)))
   (cir_simple
    (e f)
    (z)
    (comp-chip
     (IN1 IN2)
     (OUT1)
     (cir_comp
      (cir_simple (g h) (j) (prim-chip (chip-or)))
      ((cir_simple (k l) (m) (prim-chip (chip-not)))
       (g h k l)
       (j m))))))
  (a b c d e f)
  (x y z))
"representacion-listas.rkt"> (cir_simple? (cir_comp->cir1 (comp_chip->cir chip2)))
#t
"representacion-listas.rkt">

```

EJEMPLOS REPRESENTACION PROCEDIMIENTOS:

```

365
366 (define circuito5
367   (cir_comp
368     '(a b c d z)
369     '(aa)
370     (cir_comp
371       '(a b c d)
372       '(w x y)
373       (cir_simple '(a b) '(w)
374         (prim_chip (prim_nor)))
375       (list
376         (cir_simple '(c d) '(x)
377           (prim_chip (prim_or)))
378         (cir_simple '(w x) '(y)
379           (prim_chip (prim_and))))))
380     (list
381       (cir_simple '(y z) '(aa)
382         (prim_chip (prim_xor))))))
383

```

PROBLEMS

OUTPUT

DEBUG CONSOLE

TERMINAL

PORTS

COMMENTS

REP

```

                                circuito5)
"taller1_flp\\representacion-procedimientos.rkt"> (cir_simple?
                                                    circuito5)
#f
"taller1_flp\\representacion-procedimientos.rkt"> (cir_comp?
                                                    circuito5)
#t
"taller1_flp\\representacion-procedimientos.rkt"> (circuito5 1)
'(a b c d z)
"taller1_flp\\representacion-procedimientos.rkt"> (circuito5 2)
'(aa)
"taller1_flp\\representacion-procedimientos.rkt"> (circuito5 3)
#<procedure:...-procedimientos.rkt:16:8>
"taller1_flp\\representacion-procedimientos.rkt"> 

```

```

383
384 (define circuito6
385   (cir_comp
386     '(a b c d ee)
387     '(ff)
388     (cir_comp
389       '(a b c d)
390       '(bb cc dd)
391       (cir_simple '(a b) '(bb)
392         (prim_chip (prim_and)))
393       (list
394         (cir_simple '(c d) '(cc)
395           (prim_chip (prim_xor)))
396         (cir_simple '(bb cc) '(dd)
397           (prim_chip (prim_nor))))))
398   (list
399     (cir_simple '(dd ee) '(ff)
400       (prim_chip (prim_nand))))))

```

PROBLEMS


OUTPUT

DEBUG CONSOLE

TERMINAL

PORTS

COMMENTS

 REPL (representa

```

PS C:\Users\Ander\Desktop\FLP\Flp_taller 1> racket --repl --eval '(enter! (file \
er 1/taller1_flp/representacion-procedimientos.rkt\'))'

```

Welcome to Racket v8.14 [cs].

```
"taller1_flp\\representacion-procedimientos.rkt"> (circuito6 0)
```

```
'(a b c d ee)
```

```
"taller1_flp\\representacion-procedimientos.rkt"> (circuito6 2)
```

```
'(ff)
```

```
"taller1_flp\\representacion-procedimientos.rkt"> (circuito6 3)
```

```
#<procedure:...-procedimientos.rkt:16:8>
```

```

278
279 (define chip6
280   (comp_chip
281     '()
282     '(OUTA)
283     (cir_comp
284       '(a b c d ee)
285       '(ff)
286       (cir_comp
287         '(a b c d)
288         '(bb cc dd)
289         (cir_simple '(a b) '(bb)
290           (prim_chip (prim_and)))
291         (list
292           (cir_simple '(c d) '(cc)
293             (prim_chip (prim_xor)))
294           (cir_simple '(bb cc) '(dd)
295             (prim_chip (prim_nor))))))
296       (list
297         (cir_simple '(dd ee) '(ff)
298           (prim_chip (prim_nand)))))))
299

```

PROBLEMS


OUTPUT

DEBUG CONSOLE

TERMINAL

PORTS

COMMENTS

 REPL (representacion-pr

```

PS C:\Users\Ander\Desktop\FLP\Flp_taller 1> racket --repl --eval '(enter! (file \"c:/Us
er 1/taller1_flp/representacion-procedimientos.rkt\"))'
Welcome to Racket v8.14 [cs].
"taller1_flp\\representacion-procedimientos.rkt"> (chip1 0)
"taller1_flp\\representacion-procedimientos.rkt"> (chip1 1)
'(INA INB INC IND)
"taller1_flp\\representacion-procedimientos.rkt"> (chip1 2)
'(OUTA)
"taller1_flp\\representacion-procedimientos.rkt"> (chip1 3)
#<procedure:...-procedimientos.rkt:16:8>

```

```

216 (define chip3
217   (comp_chip
218     '(INA INB INC)
219     '(OUTA)
220     (cir_comp
221       '(a b c d p)
222       '(q r)
223       (cir_comp
224         '(a b c d)
225         '(m n o)
226         (cir_simple '(a b) '(m)
227           (prim_chip (prim_nand)))
228         (list
229           (cir_simple '(c d) '(n)
230             (prim_chip (prim_nor)))
231           (cir_simple '(m n) '(o)
232             (prim_chip (prim_xor))))))
233       (list
234         (cir_simple '(o p) '(q)
235           (prim_chip (prim_and))))))
236

```

PROBLEMS

OUTPUT

DEBUG CONSOLE

TERMINAL

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COMMENTS

REPL (representa

```
er 1/taller1_flp/representacion-procedimientos.rkt\"))'
```

Welcome to Racket v8.14 [cs].

```
"taller1_flp\\representacion-procedimientos.rkt"> (comp_chip? chip3)
```

```
#t
```

```
"taller1_flp\\representacion-procedimientos.rkt"> (comp_chip->in chip3)
```

```
'(INA INB INC)
```

```
"taller1_flp\\representacion-procedimientos.rkt"> (chip3 1)
```

```
'(INA INB INC)
```

```
"taller1_flp\\representacion-procedimientos.rkt"> (comp_chip->out chip3)
```

```
'(OUTA)
```

```
"taller1_flp\\representacion-procedimientos.rkt"> (chip3 2)
```

```
'(OUTA)
```

```
"taller1_flp\\representacion-procedimientos.rkt"> (chip3 3)
```

```
#<procedure:...-procedimientos.rkt:16:8>
```



```

195 ✓ (define chip2
196   (comp_chip
197     '(INA INB)
198     '(OUTA OUTB)
199   (cir_comp
200     '(a b c d k)
201     '(l m)
202   (cir_comp
203     '(a b c d)
204     '(h i j)
205   (cir_simple '(a b) '(h)
206     (prim_chip (prim_or)))
207   (list
208     (cir_simple '(c d) '(i)
209       (prim_chip (prim_xor)))
210     (cir_simple '(h i) '(j)
211       (prim_chip (prim_and))))))
212   (list
213     (cir_simple '(j k) '(l)
214       (prim_chip (prim_nand))))))
215

```

PROBLEMS OUTPUT DEBUG CONSOLE TERMINAL PORTS COMMENTS

REPL (rep

```

"taller1_flp\\representacion-procedimientos.rkt"> (chip2 0)
'comp_chip
"taller1_flp\\representacion-procedimientos.rkt"> (chip2 1)
'(INA INB)
"taller1_flp\\representacion-procedimientos.rkt"> (chip2 2)
'(OUTA OUTB)
"taller1_flp\\representacion-procedimientos.rkt"> (chip2 3)
#<procedure:...-procedimientos.rkt:16:8>

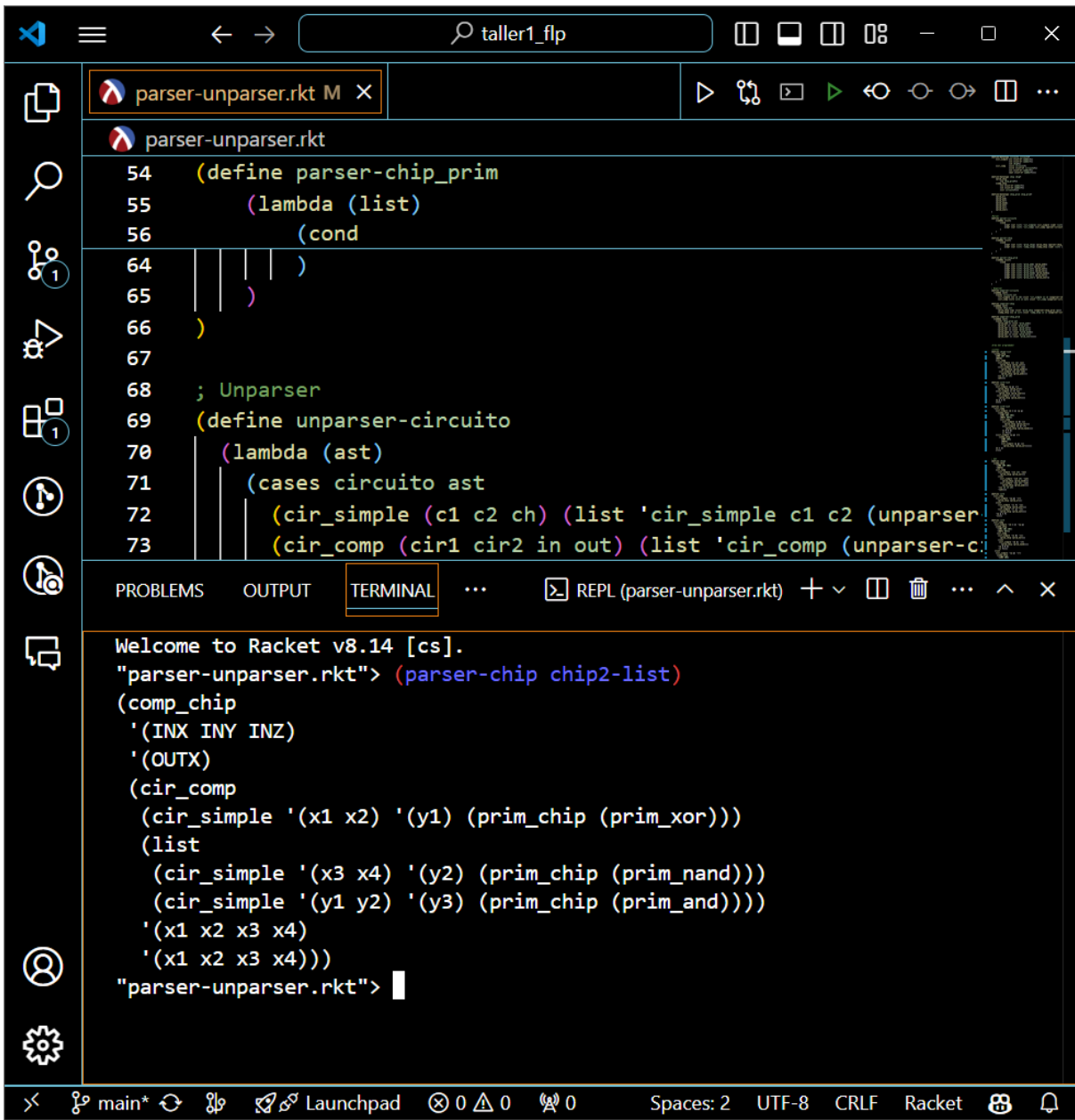
```

CAPTURAS DATATYPE

```
representation-datatype.rkt M x
representation-datatype.rkt
62
63 (define cir3
64   (cir_comp
65     (cir_simple '(A B) '(S1)
66       (prim_chip (prim_xor)))
67     (list
68       (cir_simple '(S1 C) '(OUT1)
69         (prim_chip (prim_and)))
70       (cir_simple '(C D) '(OUT2)
71         (prim_chip (prim_or)))
72     )
73     '(A B C D)
74     '(OUT1 OUT2)
75   ))
76
77 (define cir_comp1
78   (cir_comp
```

```
representation-datatype.rkt M X
representation-datatype.rkt
49 (define cir2
50   (cir_comp
51     (cir_simple '(A B) 'X)
52     (list
53       (cir_simple '(C D) 'Y)
54       (prim_chip (prim_or))
55       (cir_simple '(X Y) '(OUT1))
56       (prim_chip (prim_nand))
57     )
58     '(A B C D)
59     '(OUT1)
60   ))
61
62
63 (define cir3
64   (cir_comp
65     (cir_simple '(A B) 'S1)
66     (prim_chip (prim_xor))
67     (list
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parser-unparser.rkt M

parser-unparser.rkt

97 (define chip2-list

98 '(comp_chip

101 (cir_comp

110

111 (define cir3-list

112 '(cir_comp

113 (cir_simple (p q) (r)

114 | (prim_chip (prim_or)))

115 ((cir_simple (s t) (u)

116 | (prim_chip (prim_xnor)))

117 (cir_simple (r u) (v)

118 | (prim_chip (prim_not)))

119 (p q s t)

120 (p q s t))

PROBLEMS

OUTPUT

DEBUG CONSOLE

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REPL (parser-unparser.rkt)

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PS C:\Users\maico\Desktop\Mai\UNIVERSIDAD\SEMESTRE 5\FLP\taller1_flp> rack

et --repl --eval '(enter! (file \"c:/Users/maico/Desktop/Mai/UNIVERSIDAD/S

EMESTRE 5/FLP/taller1_flp/parser-unparser.rkt\"))'

Welcome to Racket v8.14 [cs].

"parser-unparser.rkt"> (parser-circuito cir3-list)

(cir_comp

(cir_simple '(p q) '(r) (prim_chip (prim_or)))

(list

(cir_simple '(s t) '(u) (prim_chip (prim_xnor)))

(cir_simple '(r u) '(v) (prim_chip (prim_not)))

'(p q s t)

'(p q s t))

"parser-unparser.rkt">

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main*

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parser-unparser.rkt M

parser-unparser.rkt

162 (define cir3

163 |

173

174 (define cir4

175 | (cir_comp

176 | | (cir_simple '(m n o) '(p q)

177 | | (comp_chip

178 | | | '(IN1 IN2 IN3)

179 | | | '(OUT1 OUT2)

180 | | (cir_comp

181 | | | (cir_simple '(a b) '(c)

182 | | | | (prim_chip (prim_nor)))

183 | | (list

184 | | | (cir_simple '(d e) '(f))

PROBLEMS

OUTPUT

DEBUG CONSOLE

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REPL (parser-unparser.rkt)

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PS C:\Users\maico\Desktop\Mai\UNIVERSIDAD\SEMESTRE 5\FLP\taller1_flp> rack

et --repl --eval '(enter! (file \"c:/Users/maico/Desktop/Mai/UNIVERSIDAD/S

EMESTRE 5/FLP/taller1_flp/parser-unparser.rkt\"))'

Welcome to Racket v8.14 [cs].

"parser-unparser.rkt"> (unparser-circuito cir3)

'(cir_comp

(cir_simple (p q) (r) (prim_chip (prim_or)))

((cir_simple (s t) (u) (prim_chip (prim_xnor)))

(cir_simple (r u) (v) (prim_chip (prim_not))))

(p q s t)

(v))

"parser-unparser.rkt">

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main*

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Racket

parser-unparser.rkt M

parser-unparser.rkt

```
122 (define cir4-list
123   '(cir_comp
135     ((cir_simple (p q) (r)
141       (m n o)
142       (r)))
143
144
145
146 ; AST
147 (define chip2
148   (comp_chip
149     '(INX INY INZ)
150     '(OUTX)
```

PROBLEMS

OUTPUT

DEBUG CONSOLE

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REPL (parser-unparser.rkt)

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PS C:\Users\maico\Desktop\Mai\UNIVERSIDAD\SEMESTRE 5\FLP\taller1_flp> rack
et --repl --eval '(enter! (file \"c:/Users/maico/Desktop/Mai/UNIVERSIDAD/S
EMESTRE 5/FLP/taller1_flp/parser-unparser.rkt\"))'
Welcome to Racket v8.14 [cs].
"parser-unparser.rkt"> (unparser-chip chip2)
'(comp_chip
 (INX INY INZ)
 (OUTX)
 (cir_comp
 (cir_simple (x1 x2) (y1) (prim_chip (prim_xor)))
 ((cir_simple (x3 x4) (y2) (prim_chip (prim_nand)))
 (cir_simple (y1 y2) (y3) (prim_chip (prim_and))))
 (x1 x2 x3 x4)
 (y3)))
"parser-unparser.rkt">

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main*

Launchpad

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Spaces: 2

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