



**Date:** July 26, 2016

**Working Plan:** #8

**WP Period:** July 26, 2016 - September 04, 2016

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## Working Plan Document

From the last Working Plan(WP #9), the summary is presented at table 1.

Table 1: Working Plan #7 results

Task	Status
Study: statistical methods	done
Fix: revise Database	done
Fix: revise SW Design Pattern	done
Prototype: parametrization methods in matlab	done
Implementation: File/Session times classificator	to do
Implementation: parametrization methods in C++	done
Implementation: Flow Emission Algorithm	to do
Implementation: low priority features	to do
Presentation	done

Now, the main goals of this working plan will be:

- Finish the high priority tasks of the implementation. They are File/Session times classificator, Flow Emission Algorithm, XML interface for the compact trace descriptor, Ostinato API support, and sniffer in C++;
- Complete the first version of the non-result's part of the dissertation: Ch1 Introduction, Ch2 Biographic revision, Ch3 System architecture and methods;
- Start paper writing

Doing so, the most critical part of the project will be concluded. For reference, all the remain main tasks (which one of them require an close amount of work) are listed below:

1. Implementation: File/Session times classificator
2. Implementation: parametrization methods in C++
3. Implementation: Flow Emission Algorithm

4. Implementation: sniffer in C++ (Libtins)
5. Implementation: XML trace recording/reading
6. Implementation: Ostinato API support
7. Implementation: TCP/IP socket API support
8. Implementation: DPDK pktgen (NFPA)
9. Usage case: P4 switch benchmark
10. Usage case: NFV benchmark
11. Writing: (1) Introduction
12. Writing: (2) Biographic revision
13. Writing: (3) System architecture and models
14. Writing: (4) Modeling evaluation
15. Writing: (5) Proof of concepts
16. Writing: (6) Usage cases
17. Writing: (7) Conclusion
18. Writing: Paper Writing

Currently WP tasks represents more than half of remain tasks. For the next Working plans, in terms of practical activities will remain the DPDK support, including extension to DPDK pktgen API and packet acceleration through KNI interfaces; and the proposed usage cases, P4 switch and NFV benchmark. Also will remain the compilation of results and writing the missing chapters and writing a paper.

Just as a preview, is presented below a preview of the next working plans, including all remain activities:

## References

	22/01	29/01	05/02	12/02	19/02	26/02
Implementation: DPDK pktgen (NFPA)						
Usage case: P4 switch benchmark						
Usage case: NFV benchmark						
Writing: (4) Modeling evaluation						
Writing: (5) Proof of concepts						
Writing: (6) Usage cases						
Writing: (7) Conclusion						
Writing: Paper Writing						

Figure 1: Next WP: January/February

	5/3	12/3	19/3	26/3	2/4	9/4
Usage case: P4 switch benchmark						
Usage case: NFV benchmark						
Writing: (6) Usage cases						
Writing: (7) Conclusion						
Writing: Paper Writing						

Figure 2: Next WP: March/April

## Week Work Plan

			Week					
Tasks		Sts	27/11	4/12	11/12	18/12	8/01	15/01
<b>Practical tasks</b>								
t 1	Implementation: File/Session classifier	times to do						
t 2	Implementation: parametrization methods in C++	doing						
t 3	Implementation: Flow Emission Algorithm	to do						
t 4	Implementation: sniffer in C++ (Libtins)	to do						
t 5	Implementation: XML trace recording/reading	to do						
t 6	Implementation: Ostinato API support	to do						
t 7	Implementation: TCP/IP socket API support	to do						
t 8	Writing: (1) Introduction	to do						
t 9	Writing: (2) Biographic revision	to do						
t 10	Writing: (3) System architecture and methods	to do						
t 11	Writing: Paper Writing	to do						