

君正® T31

君正君正

君正: 1.0 版

版: 2019 年 8 月



北京君正集成电路股份有限公司
Ingenic Semiconductor Co., Ltd.

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1. yyyyyyyyyy 1

GDR 1

yy..... 1 y

y..... 2 JTAG

Debug yy 2 DVP/MIPI y

y 2

USB OTG..... 3

MAC yy..... 3 SFC y

y..... 3 MSC y

y..... 3 Audio

yy..... 3 ADC y

y 4 EFUSEy

y..... 4 LCD y

y..... 4 2. PCB

yyyyyy..... 4 PCB y

y..... 4 yyyyy

y..... 4 yyy

y..... 4 GDR yyyyyy

y..... 4 yyy

y..... 5 yy

y..... 5yyyyyy

y..... 6 yy

y..... 6 USB y

y..... 6 DVP y

y..... 6 MIPI y

y..... 7 yy

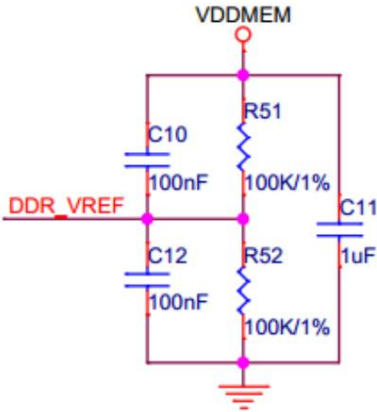
y..... 7 MAC y

y..... 7

1. The first step in the development of the disease

GDR

- 1) ZQ: 240Ω 1%.
- 2) VREF → VDDMEM → 100K 1% → 100nF



→

- 1).
-
- 2) → PIN →
- 3) T31 →

symbol	Description	Min	Typical	Max	Unit	
VDDMEM	VDDQ voltage for DDR PHY in T31L/N/X 1.7		1.8	1.9	V	
DDRVDD	VDDQ and VDD voltage for DDR2 in T31 L/N/X	1.7	1.8	1.9	V	
VDDMEM	VDDQ voltage for DDR PHY in T31Z	1,425	1,5	1,575	V	
DDRVDD	VDDQ and VDD voltage for DDR3 in T31Z	1,425	1,5	1,575	V	
VDDMEM	VDDQ voltage for DDR PHY in T31A	1.283	1.35	1.45	V	
DDRVDD	VDDQ and VDD voltage for DDR3 in T31A	1.283	1.35	1.45	V	
DDRPLL_VCCA	DDR PHY PLL power supplies voltage	1.62	1.8	1.98	V	
DDRPLL_VCCD	DDR PHY PLL power supplies voltage	0.72	0.8	0.88	V	
VDDIO0	IO digital power for GPIO power Domain 0	1.62	1.8	1.98	V	
VDDIO1	IO digital power for GPIO power Domain 1	1.5	3.3	3.63	V	
VDDIO2	IO digital power for GPIO power Domain 2	1.5	3.3	3.63	V	

VDD	VDD core voltage	0.72 0.8	0.96 V
PLL_VDDHV	APLL, MPLL and VPLL analog voltage	1.62 1.8	1.98 V
PLL_VDD	APLL, MPLL and VPLL digital voltage	0.72 0.8	0.88 V
EFUSE_AVDD	EFUSE program supplies voltage	1.62 1.8	1.98 V
VDDIO_OSC	Oscillator supplies voltage	1.62 1.8	1.98 V
USB_AVDD33	USB PHY VCCA3P3 analog voltage	3.0 3.3	3.6 V
USB_AVDD18	USB PHY VCC18 analog voltage	1.62 1.8	1.98 V
USB_AVDD08	USB PHY core voltage	0.72 0.8	0.88 V
ADC_AVDD	SAR-ADC analog voltage	1.62 1.8	1.98 V
CODEC_AVDD	CODEC analog voltage	1.62 1.8	1.98 V
MIPI_AVDD08	MIPI PHY 0.8V analog voltage	0.72 0.8	0.88 V
MIPI_AVDD18	MIPI PHY 1.8 analog voltage	1.62 1.8	1.98 V

VDDCORE 1A VDDMEM 1A DDRPLL_VCCA

DDRPLL_VCCD PLL_VDDHV PLL_VDD ADC_AVDD CODEC_AVDD MIPI_AVDD08

MIPI_AVDD18 1k @100MHz

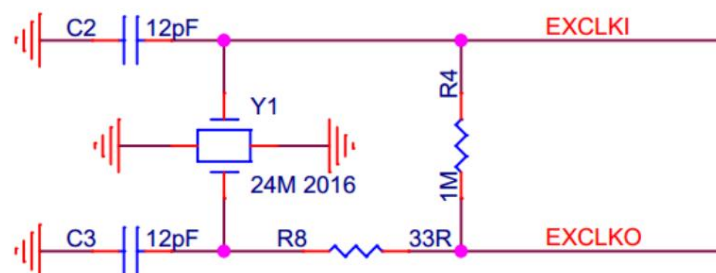
T31 for BOOT

BOOT_SEL1	BOOT_SEL0	Boot From
0	0	MMC/SD boot @ MSC0 (MMC/SD use GPIO Port B. MSC1 use GPIO Port C)
0	1	SFC boot @ CS4 (SPI boot @ SSI0)

Select SFC boot and SD boot.

Y

T31 24MHz 30ppm



JTAG debugging

T31 JTAG IEEE1149.1 PC ICE TCK TRST

JTAG TDI TMS TDO

10K JTAG

GPIO

DVP/MIPI report

T31 12 DVP GPIO PA PA00~PA22

sensor:

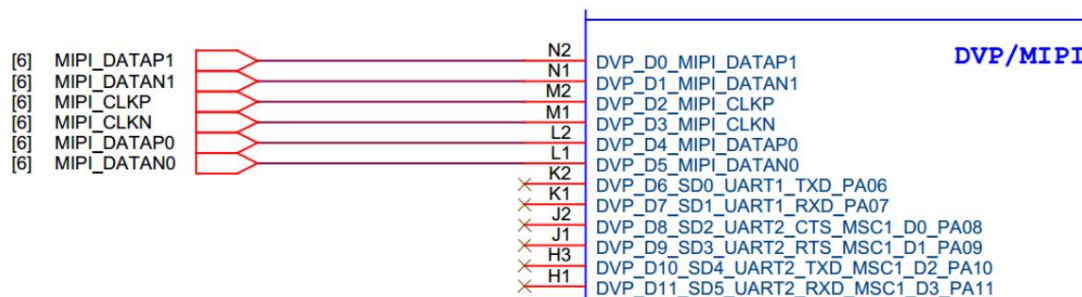
1) sensor 10 DATA DVP00 DVP09

2) 3 sensor 12 DATA DVP00 DVP11 3) 3 sensor 12 DATA

T31 DVP VDDIO0 IO 1.8V PA IO VDDIO1 DVP
GPIO T31 GPIO
CMOS Sensor IO DVP

The only thing that matters is that the food is not enough.

MIPI DVP DVP00 DVP05 2lane MIPI-CSI



USB OTG

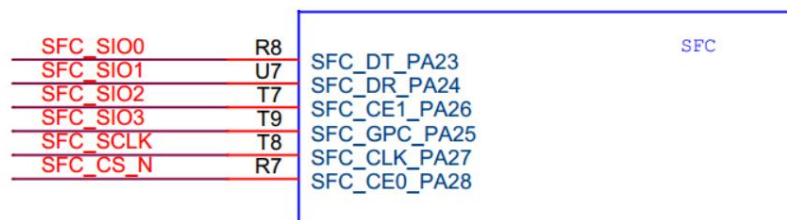
T31 USB OTG OTG USB_ID pin HOST DEVICE
ID pin VBUS pin

MAC operating system

T31 MAC 10/100M RMII 50Mhz T31 A15 MDIO
MDCK TXCK TXD0 TXD1 RXCK RXD0 RXD1 33

SFC software

T31 SFC PA23~PA28 SPI NOR FLASH



MSC School

T31 MSC TF PB MSC0 SDIO WIFI
PC MSC1 MSC1.

audio recording

T31 CODEC 1 1 T31 I2S GPIO PB PC
I2S audio codec.

ADC report

T31 3 10 ADC ADC_VREF 0-1.8V

EFUSE Plugin

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                yyyyyyyyyy 1.8V+/-10%yyy EFUSE_AVD yyyyyyyyyy 1s
yyyyEFUSE_AVD yyy 0V yyy 0 yyyyyyyyyyyy EFUSE_AVD yy
yyy.

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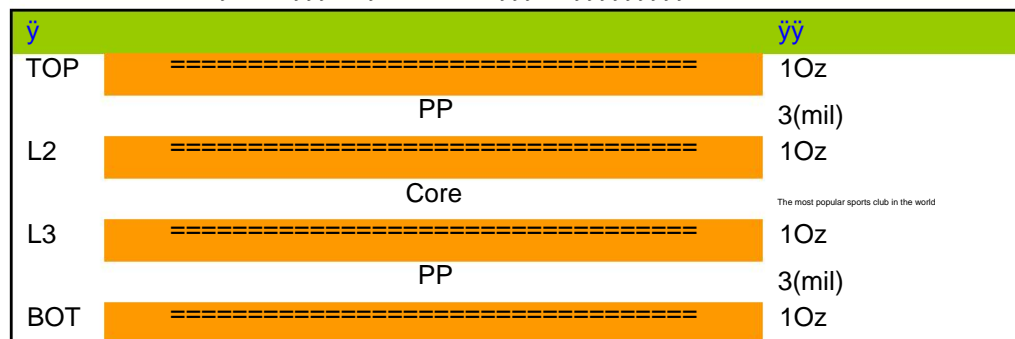
LCD monitor

T31 yy 8bit SLCD yyyyyyyyyy IO yy PB y GPIO yy

2.PCB test bench test bench

PCB board

T31 ý 0.65 pitch ý 223 ýýý BGA ýýýPCB ýýýýýýýýýý 4ýýýýýýýýýýýý
ýýýý GND ýýýýýýýýýýýýýýýýýýýýýýýýýýýýTop ýýýýýýýýýý
TOP-GND-VCC-BOTTOMýTOP ýýý L2ýBOTTOM ýýý L3ýýýýýýýýýýýý



Chinese New Year celebration

- 1) SMT is a professional computer program.
- 2) CPU and CMOS sensor.
- 3) yyyyyyyyyy

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yyyyyyyyyy y yyyyyyyyy

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- 1) yyyyyy 8mil yyy16mil yyy
- 2) yyyyyyyy 12mil yyy24mil yyy
- 3) CPU and DDR yyyyyyyyyyyyyyyyyyyy 30milv

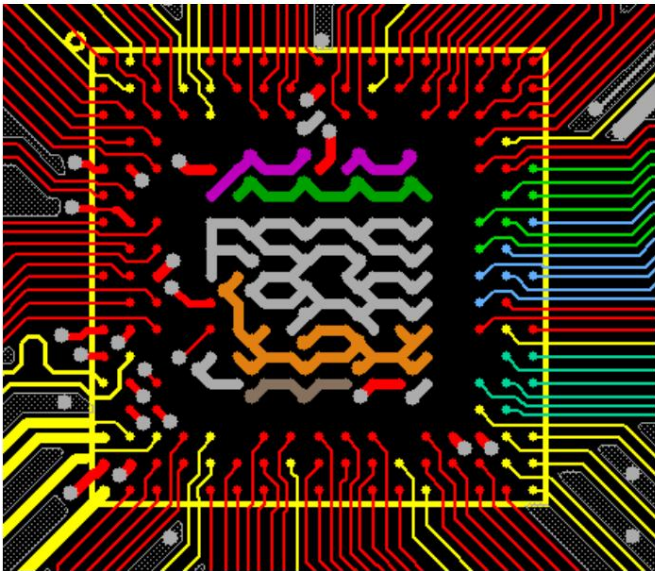
DDR and German Empire

[illegible]

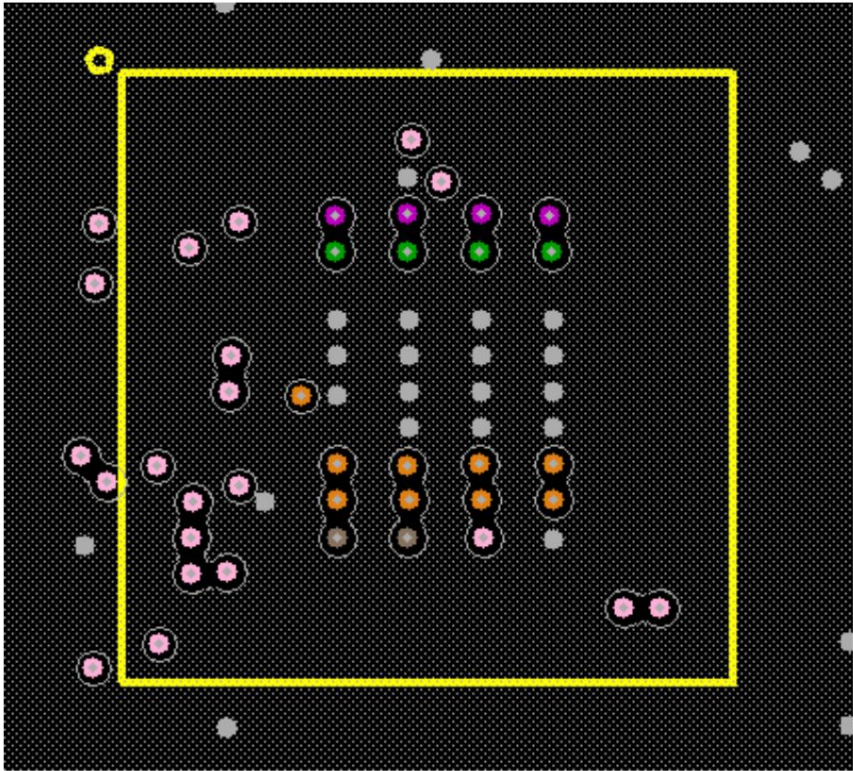
yyyyyyyyyyyyyyyy
VREFyDDRybufferyVREFyyyyyyyyyyyyyyyy20mil'y

yyyyy
1) yyyyyyyyyy 2mil'y
4mil'y

yyyy
1) yyyyyyy 10Zy
2) CPU:



3) yyyyyyyCPU yyyyyyy CPU yyy



4).

yyyyyyyyyyyyyyyyyyyy

5)PCByyyyyCPU yyyyyyyyyyyyyyyyyy 20mmyyyyyyyyyyyy yyyyyyyyyyyyyyyyyyyyyyyyyyyyyy.yyyyy
yCPU yyyyyyyyyyyyyyyyyyyyyyyyyyyyyyyyy

yyyyyy

1) yyyyyyy CPU yy 2) yyyyyy

yyyyy DC/DCyyyyyyyyyyyyyyyyyyyy

DC/DC y PIN yyyyyyyyy CPU yyyyyyy

3) CPU VDDyVDDMEMyDDRVDdyDDRPLL_VCCAyDDRPLL_VCCD yyyyyyy

yyyy CPU PIN yyy

4) VDDyVDDMEMyDDRVDd yyyyyyyyyyyyyy 5)yyyyyyyyyy

yyyyyyyyyyyyyy

6) CPU version: 1 yy:

PLL_VDDHV, PLL_VDD, DDRPLL_VCCA, DDRPLL_VCCD, VDD, VDDMEM, DDRVDD, VREF.

y 2 yyMIPI_AVDD08yMIPI_AVDD18yCODEC_AVDDyVDDIO_OSCyUSB_AVDD33y

USB_AVDD18y USB_AVDD10y

Number 3: VDDIO0, VDDIO1, VDDIO2, ADC_AVDD.

7) CPU yy 8) yyyyyy

yyyyyyyyyy

Japanese food

yyyyyyyyyyyyyyyyyyyy 3Wyyyyyyyyyyyyyyyyyy

yy

USB flash drive

yyyyyyyyyyUSB2.0 yyyyyyyyyyyyyyyyyy USB 2.0 y

y 480MHz yyyyyy PCB yyyyyyy 1)yyyyyyyyyyyyyy

yyyyyyyyyyyyyyyyyy±

5mil yy

2) yyyyyy 90y±10%yyyyyy 3)yyyyyyyyyyyyyy

yyyyyyyyyy 4) yyyyyyyyyyyyyyyyyyyyyyyyyy 5)yyyyyyyyyy

yyyyyyyyyyyyyy 135 yyyyyy

yyyyyyyyyy

6) yyyyyyyyyyyyyyyyyy50milyyyyyyy

yyyyyyyyyyyyyy 20mil yy

DVP report

yyyyyyyyyyyyyy±300mil yyDVP_MCLKyDVP_PCLKyDVP_HSYNCy

DVP_VSYNC $\pm 5\text{mil}$ $\pm 100\text{mil}$ $100 \pm 10\%$ "3W"

MIPI software

$\pm 5\text{mil}$ $\pm 100\text{mil}$ $100 \pm 10\%$ "3W"

MIPI

MIPI/MICN

MAC OS

GMAC PCB 1) 2) $\pm 200\text{mil}$ 3) 4) "3W" 5) 33

PHY MDI_TP MDI_TN MDI_RP MDI_RN $\pm 5\text{mil}$ $100 \pm 10\%$