



low current, low VGS D-NMOS-FET  
VGS=0.6-2.5V, I<sub>max</sub>=230mA, VDS<sub>max</sub>=60V, C<sub>in</sub>=60pF

by Andi

**LENS**

Sheet: /

File: FPGA\_cable\_driver.sch

**Title: FPGA cable driver**

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