CircuiTikZ

version 0.4

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1 Introduction

After two years of little exposure only on my personal website 1 , I did a major rehauling of the code of CircuiTikZ, fixing several problems and converting everything to TikZ version 2.0.

I'm not too sure about the result, because my (La) T_EX skills are much to be improved, but it seems it's time for more user feedback. So, here it is...

I know the documentation is somewhat scant. Hope to have time to improve it a bit.

¹Now the package is moved to its own git repository: https://github.com/mredaelli/circuitikz.Contributions are welcome.

1.1 About

This package provides a set of macros for naturally typesetting electrical and (somewhat less naturally, perhaps) electronical networks.

It was born mainly for writing my own exercise book and exams sheets for the Elettrotecnica courses at Politecnico di Milano, Italy. I wanted a tool that was easy to use, with a lean syntax, native to ETEX, and supporting directly PDF output format.

So I based everything with the very impressive (if somewhat verbose at times) TikZ package.

1.2 Loading the package

\usepackage{circuitikz}
TikZ will be automatically loaded.

1.3 License

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1.4 Feedback

Much appreciated: 록 mailto:m.redaelli@gmail.com. Although I don't guarantee quick answers.

1.5 Requirements

- tikz, version ≥ 2 ;
- xstring, not older than 2009/03/13;
- siunitx, if using siunitx option.

1.6 Incompatible packages

TikZ's own circuit library, which is based on CircuiTikZ, (re?)defines several styles used by this library. In order to have them work together I added the compatibility package option, which basically prefixes the names of all CircuiTikZ to[] styles with an asterisk.

So, if loaded with said option, one must write (0,0) to [*R] (2,0) and, for transistors on a path, (0,0) to [*Tnmos] (2,0), and so on (but (0,0) node [nmos] {}). See example at page ??.

1.7 Introduction to version 0.3.0

Probably nobody is hoping or caring for a new version of the package at this point, seeing how long it took me for this next release. But here it is, fixing a big problem (voltage labels in the wrong place, in some cases) and adding several components.

Thanks for bug reporting and suggesting improvements.

1.8 Introduction to version 0.2.3

Having waited a long time before updating the package, many feature requests piled on my desk. They should all be implemented now.

There are a number of backward incompatibilities—I'm sorry, but I had to make a choice in order not to have a schizophrenic interface. They are mostly, in my opinion, minor problems that can be dealt with with appropriate package options:

- potentiometer is now the standard resistor-with-arrow-in-the-middle; the old potentiometer is now known as variable resistor (or vR), similarly to variable inductor and variable capacitor;
- american inductor was not really the standard american inductor. The old american inductor has been renamed cute inductor;
- transformer, transformer core and variable inductor are now linked with the chosen type of inductor;
- styles for selecting shape variants (like [american resistors]) are now in the plural to avoid conflict with paths (like to [american resistor]).

1.9 ConT_FXt compatibility

As requested by some users, I fixed the package for it to be compatible with ConT_EXt. Just use \usemodule[circuitikz] in your preamble and include the code between \startcircuitikz and \endcircuitikz. Please notice that the package siunitx in not available for ConT_EXt: the option siunitx simply defines a few measurement units typical in electric sciences.

In actually using CircuiTikZ with TikZ version 2 in $ConT_EXt$ an error comes up, saying something like

```
! Undefined control sequence.
\tikz@cc@mid@checks -> \pgfutil@ifnextchar!
```

The solution has been suggested to me by Aditya Mahajan, and involves modifying a file in TikZ:

```
Here is the fix. In tikzlibrarycalc.code.tex change
```

```
\def\tikz@cc@mid@checks{
  \pgfutil@ifnextchar !{%AM: Added space
  \tikz@cc@mid%
  }{%
  \advance\pgf@xa by\tikz@cc@factor\pgf@xb%
  \advance\pgf@ya by\tikz@cc@factor\pgf@yb%
```

```
\tikz@cc@parse% continue
}%
}

\def\tikz@cc@mid !{%AM Added space
  \pgfutil@ifnextchar({%
    \tikz@scan@one@point\tikz@cc@project%
}{%
    \tikz@cc@mid@num%
}%
}
```

As far as I know, this is a bug in TikZ, and I notified the author, but until he fixes it, you know the workaround.

2 Options

- europeanvoltages: uses arrows to define voltages, and uses european-style voltage sources;
- americanvoltages: uses and + to define voltages, and uses american-style voltage sources;
- europeancurrents: uses european-style current sources;
- americancurrents: uses american-style current sources;
- europeanresistors: uses rectangular empty shape for resistors, as per european standards;
- americanresistors: uses zig-zag shape for resistors, as per american standards:
- europeaninductors: uses rectangular filled shape for inductors, as per european standards;
- americaninductors: uses "4-bumps" shape for inductors, as per american standards;
- cuteinductors: uses my personal favorite, "pig-tailed" shape for inductors;
- americanports: uses triangular logic ports, as per american standards;
- europeanports: uses rectangular logic ports, as per european standards;
- americangfsurgearrester: uses round gas filled surge arresters, as per american standards;
- europeangfsurgearrester: uses rectangular gas filled surge arresters, as per european standards;
- european: equivalent to european currents, european voltages, european resistors, european inductors, european ports, european gfsurgearrester;

- american: equivalent to american currents, american voltages, american resistors, american inductors, american ports, americangf surgearrester;
- siunitx: integrates with SIunitx package. If labels, currents or voltages are of the form #1<#2> then what is shown is actually \SI{#1}{#2};
- nosiunitx: labels are not interpreted as above;
- fulldiodes: the various diodes are drawn and filled by default, i.e. when using styles such as diode, D, sD, ...Un-filled diode can always be forced with Do, sDo, ...
- emptydiodes: the various diodes are drawn but not filled by default, i.e. when
 using styles such as diode, D, sD, ...Filled diode can always be forced with D*,
 sD*, ...
- arrowmos: pmos and nmos have arrows analogous to those of pnp and npn transistors;
- noarrowmos: pmos and nmos do not have arrows analogous to those of pnp and npn transistors;
- straightlabels: labels on bipoles are always printed straight up, i.e. with horizontal baseline;
- rotatelabels: labels on bipoles are always printed aligned along the bipole;
- smartlabels: labels on bipoles are rotated along the bipoles, unless the rotation is very close to multiples of 90°;
- compatibility: makes it possibile to load CircuiTikZ and TikZ circuit library together.

The old options in the singular (like american voltage) are still available for compatibility, but are discouraged.

Loading the package with no options is equivalent to my own personal liking, that is to the following options:

[europeancurrents, europeanvoltages, americanresistors, cuteinductors, americanports, nosiunitx, noarrowmos, smartlabels].

In ConTeXt the options are similarly specified: current=european|american, voltage=european|american,resistor=american|european,inductor=cute|american|european,logic=american|european,siunitx=true|false,arrowmos=false|true.

3 The components

Here follows the list of all the shapes defined by CircuiTikZ. These are all pgf nodes, so they are usable in both pgf and TikZ.

Each bipole (plus triac and thyristors) are shown using the following command, where #1 is the name of the component²:

 $^{^2\}mbox{If}$ #1 is the name of the bipole/the style, then the actual name of the shape is #1shape.

\begin{center}\begin{circuitikz} \draw
(0,0) to[#1] (2,0)

; \end{circuitikz} \end{center}

The other shapes are shown with:

\begin{center}\begin{circuitikz} \draw
(0,0) node[#1] {}

; \end{circuitikz} \end{center}

Please notice that for user convenience transistors can also be inputted using the syntax for bipoles. See section 5.3.

If using the \tikzexternalize feature, as of Tikz 2.1 all pictures must end with \end{tikzpicture}. Thus you cannot use the circuitikz environment. Which is ok: just use tikzpicture: everything will work there just fine.

3.1 Monopoles

• Ground (ground)



• Reference ground (rground)



• Signal ground (sground)



Noiseless ground (nground)



• Protective ground (pground)



• Chassis ground³ (cground)

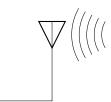


³These last three were contributed by Luigi «Liverpool»)

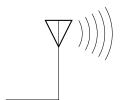
• Antenna (antenna)



• Receiving antenna (rxantenna)



• Transmitting antenna (txantenna)



• Transmission line stub (tlinestub)



• VCC (vcc)



• match (match)



• LO4 (oscillator)



 $^{^4}$ These last three come from Stefan Erhardt's contribution of HF components

3.2 Bipoles

3.2.1 Instruments

• Ammeter (ammeter)



• Voltmeter (voltmeter)



3.2.2 Basic resistive bipoles

• Short circuit (short)

• Open circuit (open)

• Lamp (lamp)



• Generic (symmetric) bipole (generic)



• Tunable generic bipole (tgeneric)



• Generic asymmetric bipole (ageneric)



• Generic asymmetric bipole (full) (fullgeneric)



• Tunable generic bipole (full) (tfullgeneric)



• Memristor (memristor, or Mr)



3.2.3 Resistors and the like

If (default behaviour) americanresistors option is active (or the style [american resistors] is used), the resistor is displayed as follows:

• Resistor (R, or american resistor)

• Variable resistor (vR, or variable american resistor)

• Potentiometer (pR, or american potentiometer)

If instead europeanresistors option is active (or the style [european resistors] is used), the resistors, variable resistors and potentiometers are displayed as follows:

• Resistor (R, or european resistor)



• Variable resistor (vR, or european variable resistor)



• Potentiometer (pR, or european potentiometer)



Other miscellaneous resistor-like devices:

• Varistor (varistor)



• Photoresistor (phR, or photoresistor)



• Thermocouple (thermocouple)



• Thermistor (thR, or thermistor)



• PTC thermistor (thRp, or thermistor ptc)



• NTC thermistor (thRn, or thermistor ntc)



• Fuse (fuse)



• Asymmetric fuse (afuse, or asymmetric fuse)



3.2.4 Stationary sources

• Battery (battery)



• Single battery cell (battery1)



• Voltage source (european style) (european voltage source)



• Voltage source (american style) (american voltage source)



• Current source (european style) (european current source)



• Current source (american style) (american current source)



If (default behaviour) europeancurrents option is active (or the style [european currents] is used), the shorthands current source, isource, and I are equivalent to european current source. Otherwise, if americancurrents option is active (or the style [american currents] is used) they are equivalent to american current source.

Similarly, if (default behaviour) europeanvoltages option is active (or the style [european voltages] is used), the shorthands voltage source, vsource, and V are equivalent to european voltage source. Otherwise, if americanvoltages option is active (or the style [american voltages] is used) they are equivalent to american voltage source.

3.2.5 Diodes and such

• Empty diode (empty diode, or Do)



• Empty Schottky diode (empty Schottky diode, or sDo)



• Empty Zener diode (empty Zener diode, or zDo)



• Empty tunnel diode (empty tunnel diode, or tDo)



• Empty photodiode (empty photodiode, or pDo)



• Empty led (empty led, or leDo)



• Empty varcap (empty varcap, or VCo)



• Full diode (full diode, or D*)



• Full Schottky diode (full Schottky diode, or sD*)



• Full Zener diode (full Zener diode, or zD*)



• Full tunnel diode (full tunnel diode, or tD*)



• Full photodiode (full photodiode, or pD*)



• Full led (full led, or leD*)



• Full varcap (full varcap, or VC*)



The options fulldiodes and emptydiodes (and the styles [full diodes] and [empty diodes]) define which shape will be used by abbreviated commands such that D, sD, zD, tD, pD, leD, and VC.

• Squid (squid)



• Barrier (barrier)



- European gas filled surge arrester (european gas filled surge arrester)
- American gas filled surge arrester (american gas filled surge arrester)

If (default behaviour) europeangf surgearrester option is active (or the style [european gas filled surge arrester] is used), the shorthands gas filled surge arrester and gf surge arrester are equivalent to the european version of the component.

If otherwise americangfsurgearrester option is active (or the style [american gas filled surge arrester] is used), the shorthands the shorthands gas filled surge arrester and gf surge arrester are equivalent to the american version of the component.

3.2.6 Basic dynamical bipoles

• Capacitor (capacitor, or C)



• Polar capacitor (polar capacitor, or pC)



• Variable capacitor (variable capacitor, or vC)



If (default behaviour) cuteinductors option is active (or the style [cute inductors] is used), the inductors are displayed as follows:

• Inductor (L, or cute inductor)



• Variable inductor (vL, or variable cute inductor)



If americaninductors option is active (or the style [american inductors] is used), the inductors are displayed as follows:

• Inductor (L, or american inductor)



• Variable inductor (vL, or variable american inductor)



Finally, if europeaninductors option is active (or the style [european inductors] is used), the inductors are displayed as follows:

• Inductor (L, or european inductor)



• Variable inductor (vL, or variable european inductor)



There is also a transmission line:

• Transmission line (TL, or transmission line, tline)



3.2.7 Sinusoidal sources

Here because I was asked for them. But how do you distinguish one from the other?!

• Sinusoidal voltage source (sinusoidal voltage source, or vsourcesin, sV)



- Sinusoidal current source (sinusoidal current source, or isourcesin, ${\tt sI}$)



3.2.8 Square sources

• Square voltage source (square voltage source, or vsourcesquare, sqV)



3.2.9 Switch

• Closing switch (closing switch, or cspst)



• Opening switch (opening switch, or ospst)



• Push button (push button)

3.2.10 HF components

Contributed by Stefan Erhardt.

• generic two port⁵ (twoport)



 $^{^5}$ To specify text to be put in the component: twoport[t=text]): $\xrightarrow{\text{text}}$

• vco (vco)



• bandpass (bandpass)



• highpass (highpass)



• lowpass (lowpass)



• A/D converter (adc)



• D/A converter (dac)



• DSP (dsp)



• FFT (fft)



• amplifier (amp)



• VGA (vamp)



• π attenuator (piattenuator)



• var. π attenuator (vpiattenuator)



• Tattenuator (tattenuator)



• var. T attenuator (vtattenuator)



• phase shifter (phaseshifter)



• var. phase shifter (vphaseshifter)



• detector (detector)



3.3 Tripoles

3.3.1 Controlled sources

Admittedly, graphically they are bipoles. But I couldn't...

 Controlled voltage source (european style) (european controlled voltage source)



 Controlled voltage source (american style) (american controlled voltage source)



 Controlled current source (european style) (european controlled current source)



• Controlled current source (american style) (american controlled current source)



If (default behaviour) europeancurrents option is active (or the style [european currents] is used), the shorthands controlled current source, cisource, and cI are equivalent to european controlled current source. Otherwise, if americancurrents option is active (or the style [american currents] is used) they are equivalent to american controlled current source.

Similarly, if (default behaviour) europeanvoltages option is active (or the style [european voltages] is used), the shorthands controlled voltage source, cvsource, and cV are equivalent to european controlled voltage source. Otherwise, if americanvoltages option is active (or the style [american voltages] is used) they are equivalent to american controlled voltage source.

 Controlled sinusoidal voltage source (controlled sinusoidal voltage source, or controlled vsourcesin, cvsourcesin, csV)



• Controlled sinusoidal current source (controlled sinusoidal current source, or controlled isourcesin, cisourcesin, csI)



3.3.2 Transistors

• nmos (nmos)



• pmos (pmos)



• npn (npn)



• pnp (pnp)



• npigbt (nigbt)



• pigbt (pigbt)



If the option arrowmos is used (or after the commant $\texttt{ctikzset}\{\texttt{tripoles/mos style/arrows}\}\$ is given), this is the output:

• nmos (nmos)



• pmos (pmos)



nfets and pfets have been incorporated based on code provided by Clemens Helfmeier and Theodor Borsche:

• nfet (nfet)



• nigfete (nigfete)



• nigfetd (nigfetd)



• pfet (pfet)



• pigfete (pigfete)



• pigfetd (pigfetd)



• njfet (njfet)



• pjfet (pjfet)



isfet

• isfet(isfet)



3.3.3 HF

These come from Stefan Erhardt's contribution of HF components.

• circulator (circulator)



• wilkinson divider (wilkinson)



3.3.4 Switch

• spdt (spdt)



• Toggle switch (toggle switch)



3.3.5 Other bipole-like tripoles

The following tripoles are entered with the usual command of the form

• triac (triac, or Tr)



• thyristor (thyristor, or Ty)



3.3.6 Misc

• Mixer (mixer)



3.4 Double bipoles

Transformers automatically use the inductor shape currently selected. These are the three possibilities:

• Transformer (cute inductor) (transformer)



• Transformer (american inductor) (transformer)



• Transformer (european inductor) (transformer)



Transformers with core are also available:

• Transformer core (cute inductor) (transformer core)



• Transformer core (american inductor) (transformer core)



• Transformer core (european inductor) (transformer core)



• Gyrator (gyrator)



• Coupler (coupler)



• Coupler, 2 (coupler2)



3.5 Logic gates

• American and port (american and port)



• American or port (american or port)



• American not port (american not port)



• American nand port (american nand port)



• American nor port (american nor port)



• American xor port (american xor port)



• American xnor port (american xnor port)



• European and port (european and port)



• European or port (european or port)



• European not port (european not port)



• European nand port (european nand port)



• European nor port (european nor port)



• European xor port (european xor port)

• European xnor port (european xnor port)

If (default behaviour) americanports option is active (or the style [american ports] is used), the shorthands and port, or port, not port, nand port, not port, xor port, and xnor port are equivalent to the american version of the respective logic port.

If otherwise europeanports option is active (or the style [european ports] is used), the shorthands and port, or port, not port, nand port, not port, xor port, and xnor port are equivalent to the european version of the respective logic port.

3.6 Amplifiers

• Operational amplifier (op amp)



• Fully differential operational amplifier⁶ (fd op amp)



• Plain amplifier (plain amp)



• Buffer (buffer)



 $^{^6}$ Contributed by Kristofer M. Monisit.

3.7 Support shapes

• Arrows (current and voltage) (currarrow)

▶

• Connected terminal (circ)

•

• Unconnected terminal (ocirc)

0

4 Usage

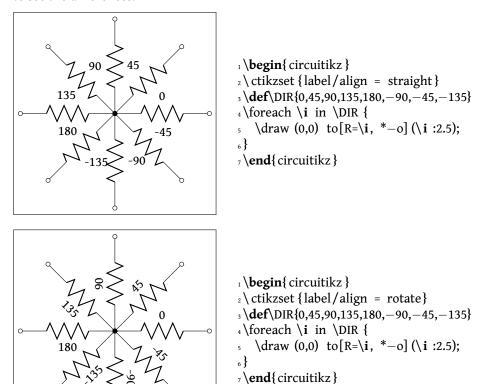
```
1\begin{circuitikz}
R_1
                \draw (0,0) to[R, l=$R_1$] (2,0);
            3 \end{circuitikz}
            1\begin{circuitikz}
R_1
                \draw (0,0) to [R=$R_1$] (2,0);
            3\end{circuitikz}
            1 \begin{ circuitikz }
                \draw (0,0) to[R, i=$i_1$] (2,0);
            3 \end{circuitikz}
            1\begin{circuitikz}
                \draw (0,0) to [R, v=$v_1$] (2,0);
            3 \end{circuitikz}
            1 \begin{circuitikz}
                \draw (0,0) to[R=$R_1$, i=$i_1$, v=$v_1$] (2,0);
            3 \end{circuitikz}
            1 \begin{ circuitikz }
R_1
                \draw (0,0) to [R=R_1, i=i_1, v=v_1] (2,0);
            3 \end{circuitikz}
```

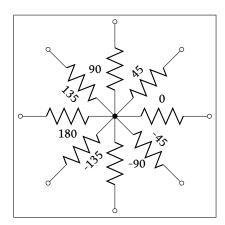
Long names/styles for the bipoles can be used:

4.1 Labels

```
R_{1} = \frac{1 \text{ begin}\{\text{circuitikz}\}}{2 \text{ draw } (0,0) \text{ to}[R, l^=\$R_1\$] (2,0);}
\frac{1}{3} \text{ end}\{\text{circuitikz}\}
\frac{1}{2} \text{ draw } (0,0) \text{ to}[R, l_=\$R_1\$] (2,0);}{3 \text{ end}\{\text{circuitikz}\}}
```

The default orientation of labels is controlled by the options smartlabels, rotatelabels and straightlabels (or the corresponding label/align keys). Here are examples to see the differences:





```
1 \begin{ circuitikz }
2 \ ctikzset { label / align = smart}
3 \ def\DIR{0,45,90,135,180,-90,-45,-135}
4 \ foreach \ i in \DIR {
5 \ \draw (0,0) \ to [R=\i, *-o] (\i :2.5);
6 }
7 \ end{ circuitikz }
```

4.2 Currents

- - $\downarrow \downarrow i_1$
- 1\begin{circuitikz}
- \draw (0,0) to [R, i^>=\\$i_1\\$] (2,0); \end{circuitikz}
- - \bigvee_{i_1}
- 1\begin{circuitikz}
- ² \draw (0,0) to[R, i_>=\$i_1\$] (2,0);
- 3 \end{circuitikz}
- - $\sqrt{\frac{i_1}{4}}$
- 1 \begin{ circuitikz }
- \draw (0,0) to [R, i^<=\\$i_1\\$] (2,0);
- 3 \end{circuitikz}



- 1\begin{circuitikz}
- \draw (0,0) to[R, i_<=\$i_1\$] (2,0);
- 3 \end{circuitikz}



- 1 \begin{circuitikz}
- \draw (0,0) to [R, $i > ^= i_1$] (2,0);
- $_{^{3}}\end\{circuitikz\}$



- 1\begin{circuitikz}
- $draw (0,0) to[R, i>=$i_1$] (2,0);$
- 3 \end{circuitikz}



- 1 \begin{circuitikz}
- \draw (0,0) to [R, i<^= i_1] (2,0); \\end{circuitikz}
- $\overline{i_1}$
- 1 \begin{ circuitikz }
- ² \draw (0,0) to[R, i<_=\$i_1\$] (2,0);
- 3 \end{circuitikz}

Also

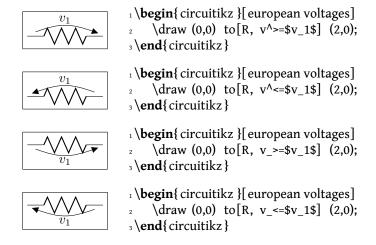
- - $\sqrt{i_1}$
- 1\begin{circuitikz}
- ² \draw (0,0) to[R, i<=\$i_1\$] (2,0);
- 3\end{circuitikz}

```
| \begin{circuitikz}
| 2 \draw (0,0) to[R, i>=$i_1$] (2,0);
| 3 \end{circuitikz}
| 1 \begin{circuitikz}
| 2 \draw (0,0) to[R, i^=$i_1$] (2,0);
| 3 \end{circuitikz}
| 1 \begin{circuitikz}
| 2 \draw (0,0) to[R, i^=$i_1$] (2,0);
| 3 \end{circuitikz}
| 3 \end{circuitikz}
```

4.3 Voltages

4.3.1 European style

The default, with arrows. Use option european voltage or style [european voltages].



4.3.2 American style

For those who like it (not me). Use option american voltage or set [american voltages].

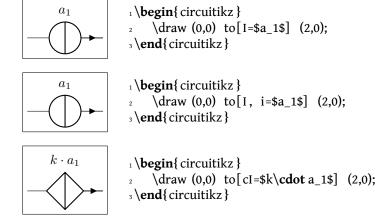
```
| \begin{circuitikz} [american voltages] | \draw (0,0) to [R, v^>=\sv_1\stacks] (2,0); | \draw (0,0) to [R, v^>=\sv_1\stacks] (2,0); | \draw (0,0) to [R, v^<=\sv_1\stacks] (2,0); | \draw (0,0) to [R, v^<=\sv_1\stacks] (2,0); | \draw (0,0) to [R, v_>=\sv_1\stacks] (2,0); | \draw (0,0) to [R, v_>=\sv_1\stacks] (2,0); | \draw (0,0) to [R, v_>=\sv_1\stacks] (2,0); | \draw (0,0) to [R, v_<=\sv_1\stacks] (2,0); | \draw (0,0) to [R, v_<=\
```

4.4 Nodes

```
1\begin{circuitikz}
    draw (0,0) to[R, o-o] (2,0);
3\end{circuitikz}
1\begin{circuitikz}
   draw (0,0) to[R, -0] (2,0);
3 \end{circuitikz}
1\begin{circuitikz}
   draw (0,0) to[R, o-] (2,0);
3\end{circuitikz}
1\begin{circuitikz}
    \draw (0,0) to [R, *-*] (2,0);
3\end{circuitikz}
1\begin{circuitikz}
   draw (0,0) to[R, -*] (2,0);
3 \end{circuitikz}
1\begin{circuitikz}
   \draw (0,0) to[R, *-] (2,0);
3 \end{circuitikz}
1\begin{circuitikz}
    draw (0,0) to[R, o-*] (2,0);
3 \end{circuitikz}
1\begin{circuitikz}
    \draw (0,0) to[R, *-o] (2,0);
3 \end{circuitikz}
```

4.5 Special components

For some components label, current and voltage behave as one would expect:





```
1 \begin{circuitikz}
2 \draw (0,0) to[sI=$a_1$] (2,0);
3 \end{circuitikz}
```

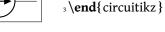


- 1 \begin{ circuitikz }
- \draw (0,0) to [csI = $k \cdot dot a_1$] (2,0);
- 3 \end{circuitikz}

The following results from using the option american current or using the style [american currents].



1\begin{circuitikz }[american currents]
2 \draw (0,0) to[I=\$a_1\$] (2,0);

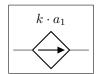




1\begin{circuitikz }[american currents]

 $draw (0,0) to[I, i=$a_1$] (2,0);$

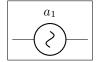
3 \end{circuitikz}



1\begin{circuitikz }[american currents]

 $draw (0,0) to[cI=$k\cdot a_1$] (2,0);$

3 \end{circuitikz}



1\begin{circuitikz }[american currents]

² \draw (0,0) to [sI=\$a_1\$] (2,0);

3 \end{circuitikz}



1 \begin{ circuitikz }[american currents]

 $draw (0,0) to [csI=$k\cdot a_1$] (2,0);$

3\end{circuitikz}

The same holds for voltage sources:



1**begin**{circuitikz}

² \draw (0,0) to[V=\$a_1\$] (2,0);

3\end{circuitikz}



1 \begin{ circuitikz }

² \draw (0,0) to[V, v=\$a_1\$] (2,0);

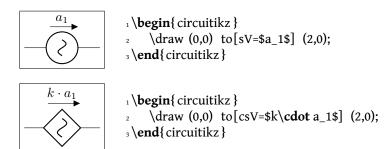
3\end{circuitikz}



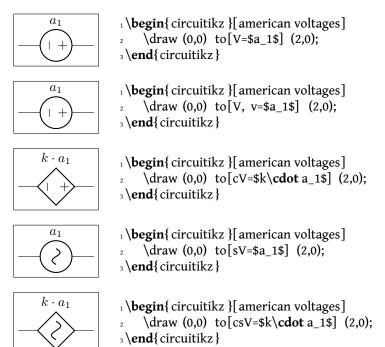
1\begin{circuitikz}

 $draw (0,0) to [cV=$k\cdot a_1$] (2,0);$

3\end{circuitikz}



The following results from using the option americanvoltage or the style $[american\ voltages]$.

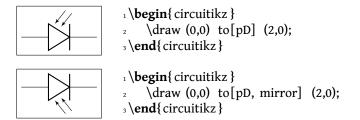


4.6 Integration with siunitx

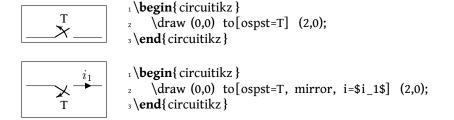
If the option siunitx is active (and not in ConT_FXt), then the following are equivalent:

```
| \begin{circuitikz}
| \draw (0,0) to[R, i=$\SI\{1\}\ milli\ampere\$] (2,0);
| \end{circuitikz}
| \begin{circuitikz}
| \draw (0,0) to[R, v=1<\volt>] (2,0);
| \draw (0,0) to[R, v=1<\volt>] (2,0);
| \draw (0,0) to[R, v=\SI\{1\}\ volt\$] (2,0);
```

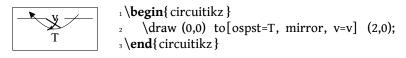
4.7 Mirroring



At the moment, placing labels and currents on mirrored bipoles works:

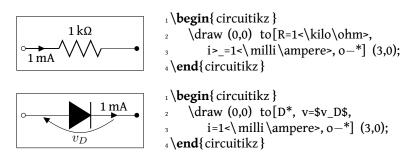


But voltages don't:



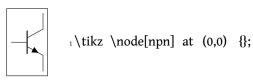
Sorry about that.

4.8 Putting them together



5 Not only bipoles

Since only bipoles (but see section 5.3) can be placed "along a line", components with more than two terminals are placed as nodes:

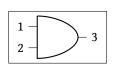


5.1 Anchors

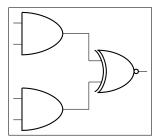
In order to allow connections with other components, all components define anchors.

5.1.1 Logical ports

All logical ports, except not, have two inputs and one output. They are called respectively in 1, in 2, out:

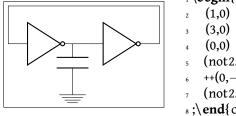


```
1 \begin{circuitikz } \draw
2  (0,0) node[and port] (myand) {}
3  (myand.in 1) node[anchor=east] {1}
4  (myand.in 2) node[anchor=east] {2}
5  (myand.out) node[anchor=west] {3}
6 ;\end{circuitikz}
```



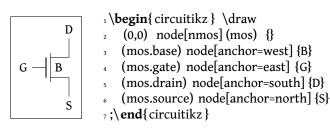
```
1\begin{circuitikz} \draw
2 (0,2) node[and port] (myand1) {}
3 (0,0) node[and port] (myand2) {}
4 (2,1) node[xnor port] (myxnor) {}
5 (myand1.out) -| (myxnor.in 1)
6 (myand2.out) -| (myxnor.in 2)
7;\end{circuitikz}
```

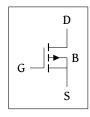
In the case of not, there are only in and out (although for compatibility reasons in 1 is still defined and equal to in):



5.1.2 Transistors

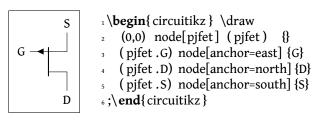
For nmos, pmos, nfet, nigfete, nigfetd, pfet, pigfete, and pigfetd transistors one has base, gate, source and drain anchors (which can be abbreviated with B, G, S and D):



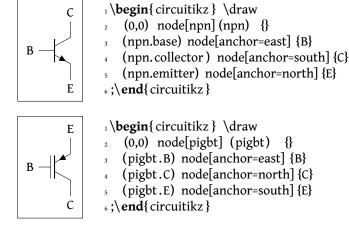


```
1\begin{circuitikz} \draw
2  (0,0) node[pigfete] (pigfete) {}
5  (pigfete .B) node[anchor=west] {B}
4  (pigfete .G) node[anchor=east] {G}
5  (pigfete .D) node[anchor=south] {D}
6  (pigfete .S) node[anchor=north] {S}
7 ;\end{circuitikz}
```

Similarly nifet and pifet have gate, source and drain anchors (which can be abbreviated with G, S and D):



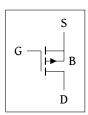
For npn, pnp, nigbt, and pigbt transistors the anchors are base, emitter and collector anchors (which can be abbreviated with B, E and C):



Here is one composite example (please notice that the xscale=-1 style would also reflect the label of the transistors, so here a new node is added and its text is used, instead of that of pnp1):

```
\begin{circuitikz} \draw
\( \) (0,0) node[pnp] (pnp2) \{2\}
\( \) (pnp2.B) node[pnp, xscale=-1, anchor=B] (pnp1) \{\}
\( \) (pnp1) node \{1\}
\( \) (pnp1.C) node[npn, anchor=C] (npn1) \{\}
\( \) (pnp2.C) node[npn, xscale=-1, anchor=C] (npn2) \{\}
\( \) (pnp1.E) -- (pnp2.E) (npn1.E) -- (npn2.E)
\( \) (pnp1.B) node[circ] \{\} \| - (pnp2.C) node[circ] \{\}
\( \) ;\end{circuitikz}
```

Similarly, transistors and other components can be reflected vertically:





```
begin{circuitikz}

draw (0,2)

node[rground, yscale=-1] {}

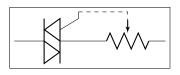
to[R=$R_1$] (0,0)

node[sground] {};

end{circuitikz}
```

5.1.3 Other tripoles

When inserting a thrystor, a triac or a potentiometer, one needs to refer to the third node—gate (gate or G) for the former two; wiper (wiper or W) for the latter one. This is done by giving a name to the bipole:



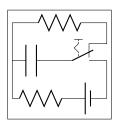
```
1 \begin{circuitikz} \draw
2 (0,0) to[Tr, n=TRI] (2,0)
3 to[pR, n=POT] (4,0);
4 \draw[dashed] (TRI.G) -| (POT.wiper)
5 ;\end{circuitikz}
```

As for the switches:

```
in — out 1

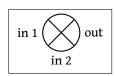
o out 2
```

```
1\begin{circuitikz} \draw
2 (0,0) node[spdt] (Sw) {}
3 (Sw.in) node[left] {in}
4 (Sw.out 1) node[right] {out 1}
5 (Sw.out 2) node[right] {out 2}
6 ;\end{circuitikz}
```



```
 \begin{circuitikz} $ \det $ & (0,0) $ to[C] (1,0) $ to[toggle switch , n=Sw] (2.5,0) $ & $--(2.5,-1) $ to[battery1] (1.5,-1) $ to[R] (0,-1) $ $-| (0,0) $ $ $$ $$ circuitikz $ $ \end{circuitikz}
```

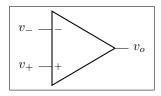
And the mixer:



1 \begin{circuitikz} \draw
2 (0,0) node[mixer] (mix) {}
3 (mix.in 1) node[left] {in 1}
4 (mix.in 2) node[below] {in 2}
5 (mix.out) node[right] {out}
6 ;\end{circuitikz}

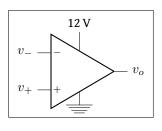
5.1.4 Operational amplifier

The op amp defines the inverting input (-), the non-inverting input (+) and the output (out) anchors:

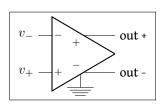


```
1\begin{ circuitikz } \draw
2 (0,0) node[op amp] (opamp) {}
3 (opamp.+) node[left] {$v_+$}
4 (opamp.-) node[left] {$v__$}
5 (opamp.out) node[right] {$v_o$}
6 ;\end{ circuitikz }
```

There are also two more anchors defined, up and down, for the power supplies:



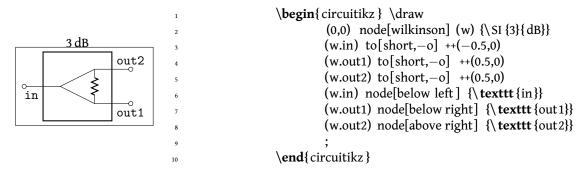
The fully differential op amp defines two outputs:



```
1 \begin{ circuitikz } \draw
2  (0,0) node[fd op amp] (opamp) {}
3  (opamp.+) node[left] {$v_+$}
4  (opamp.-) node[left] {$v_-$}
5  (opamp.out +) node[right] {out +}
6  (opamp.out -) node[right] {out -}
7  (opamp.down) node[ground] {}
8  ;\end{ circuitikz }
```

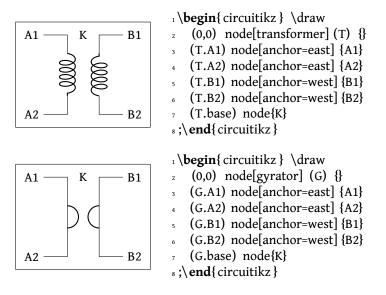
The circulator defined ports 1, 2 and 3:

The Wilkinson divider has:



5.1.5 Double bipoles

All the (few, actually) double bipoles/quadrupoles have the four anchors, two for each port. The first port, to the left, is port A, having the anchors A1 (up) and A2 (down); same for port B. They also expose the base anchor, for labelling:



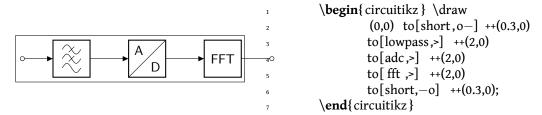
However:

```
\begin{circuitikz} \draw
                                                    (0,0) node[coupler] (c) {\SI {10}{dB}}
                                                    (c.1) to [\text{short}, -0] + (-0.5, 0)
     10 dB
                                                    (c.2) to [short, -o] ++(0.5,0)
                                                    (c.3) to [\text{short}, -\text{o}] ++(0.5,0)
                3
                                                    (c.4) to [\text{short}, -0] ++(-0.5,0)
                                                    (c.1) node[below left] {\texttt {1}}
                                                    (c.2) node[below right] {\ texttt {2}}
1
                                                    (c.3) node[above right] {\ texttt {3}}
                                                    (c.4) node[above left] {\ texttt {4}}
                                           \end{circuitikz}
                                           \begin{circuitikz} \draw
                                                    (0,0) node[coupler2] (c) {\SI {3}{ dB}}
                                                    (c.1) to[short,-0] ++(-0.5,0)
      3 dB
                                                    (c.2) to [\text{short}, -0] ++(0.5,0)
                                                    (c.3) to [short, -o] ++(0.5,0)
                                                    (c.4) to[short,-0] ++(-0.5,0)
                                                    (c.1) node[below left] {\texttt {1}}
                                                    (c.2) node[below right] {\ texttt {2}}}
                                                    (c.3) node[above right] {\texttt {3}}
                                                    (c.4) node[above left] {\texttt {4}}
                                           \end{circuitikz}
                       12
```

5.2 Input arrows

two ports

The new two ports all have a defined direction. If one needs opposite direction, just place them from right to left. With the option > a input arrow is drawn:

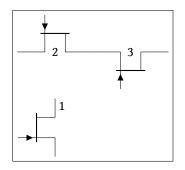


multi ports

Since inputs and outputs can vary, input arrows can be placed as nodes:

5.3 Transistor paths

For syntactical convenience transistors can be placed using the normal path notation used for bipoles. The transitor type can be specified by simply adding a "T" (for transistor) in front of the node name of the transistor. It will be placed with the base/gate orthogonal to the direction of the path:



```
1 \begin{circuitikz } \draw
2 (0,0) node[njfet] {1}
3 (-1,2) to[Tnjfet=2] (1,2)
4 to[Tnjfet=3, mirror] (3,2);
5 ;\end{circuitikz}
```

Access to the gate and/or base nodes can be gained by naming the transistors with the n or name path style:

The name property is available also for bipoles, although this is useful mostly for triac, potentiometer and thyristor (see 3.3.5).

6 Customization

6.1 Parameters

Pretty much all CircuiTikZ relies heavily on pgfkeys for value handling and configuration. Indeed, at the beginning of circuitikz.sty a series of key definitions can be found that modify all the graphical characteristics of the package.

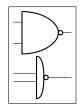
All can be varied using the \ctikzset command, anywhere in the code.

Shape of the components (on a per-component-class basis)

```
\begin{array}{c} 1\Omega \\ - \swarrow \searrow \\ 1\Omega \\ - \swarrow \swarrow \\ - \end{array}
```

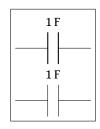
```
1\tikz \draw (0,0) to[R=1<\ohm>] (2,0); \par
2\ctikzset {bipoles/resistor/height=.6}
```

3\tikz \draw (0,0) to[R=1<\ohm>] (2,0);



- 1\tikz \draw (0,0) node[nand port] {}; \par
- 2\ctikzset { tripoles /american nand port/input height=.2}
- 3 \ ctikzset { tripoles /american nand port/port width=.2}
- 4\tikz \draw (0,0) node[nand port] {};

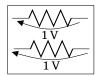
Thickness of the lines (globally)



```
1\tikz \draw (0,0) to[C=1<\farad>] (2,0); \par
```

- 2\ctikzset {bipoles/thickness=1}
- ³\tikz \draw (0,0) to[C=1<\farad>] (2,0);

Global properties Of voltage and current

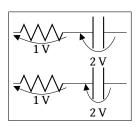


- 1\tikz \draw (0,0) to[R, v=1<\volt>] (2,0); \par
- 2\ctikzset {voltage/distance from node=.1}
- ³\tikz \draw (0,0) to[R, v=1<\volt>] (2,0);



- $_1 \times (0,0)$ to $[C, i=\$ \mathbb{1} (2,0); \par$
- 2\ctikzset {current/distance = .2}
- $_3$ \tikz \draw (0,0) to[C, i=\\imath\\$] (2,0);

However, you can override the properties voltage/distance from $node^7$, voltage/bump b^8 and voltage/european label distance on a per-component basis, in order to fine-tune the voltages:



```
\tikz \draw (0,0) to[R, v=1<\volt>] (1.5,0)

to[C, v=2<\volt>] (3,0); \par

\tikzset {bipoles/capacitor/voltage/%

distance from node/. initial =.7}

\tikz \draw (0,0) to[R, v=1<\volt>] (1.5,0)

to[C, v=2<\volt>] (3,0); \par
```

⁷That is, how distant from the initial and final points of the path the arrow starts and ends.

 $^{^8}$ Controlling how high the bump of the arrow is — how curved it is.

⁹Controlling how distant from the bipole the voltage label will be.

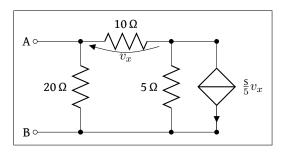
Admittedly, not all graphical properties have understandable names, but for the time it will have to do:



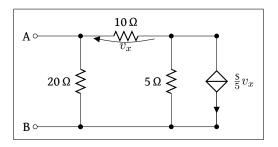
```
1\tikz \draw (0,0) node[xnor port] {};
2\tikzset { tripoles /american xnor port/aaa=.2}
3\tikzset { tripoles /american xnor port/bbb=.6}
4\tikz \draw (0,0) node[xnor port] {};
```

6.2 Components size

Perhaps the most important parameter is \circuitikzbasekey/bipoles/length, which can be interpreted as the length of a resistor (including reasonable connections): all other lengths are relative to this value. For instance:



```
1\ctikzset {bipoles/length=1.4cm}
2\begin{circuitikz}[scale =1.2]\draw
3 (0,0) node[anchor=east] {B}
4 to[short, o-*] (1,0)
5 to[R=20<\ohm>, *-*] (1,2)
6 to[R=10<\ohm>, v=$v_x$] (3,2) -- (4,2)
7 to[cI=$\frac{\si {\siemens}}{5} v_x$, *-*] (4,0) -- (3,0)
8 to[R=5<\ohm>, *-*] (3,2)
9 (3,0) -- (1,0)
10 (1,2) to[short, -o] (0,2) node[anchor=east]{A}
11 ;\end{circuitikz}
```



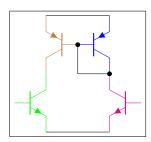
6.3 Colors

The color of the components is stored in the key \circuitikzbasekey/color. CircuiTikZ tries to follow the color set in TikZ, although sometimes it fails. If you change color in the picture, please do not use just the color name as a style, like [red], but rather assign the style [color=red].

Compare for instance

```
| begin{circuitikz} \draw[color=red] | (0,2) node[and port] (myand1) {} | (0,0) node[and port] (myand2) {} | (2,1) node[xnor port] (myxnor) {} | (myand1.out) -| (myxnor.in 1) | (myand2.out) -| (myxnor.in 2) | (myand2.out) | (myxnor.in 2) | (myxnor.in 2)
```

One can of course change the color in medias res:



```
1 \begin{circuitikz} \draw
2  (0,0) node[pnp, color=blue] (pnp2) {}
3  (pnp2.B) node[pnp, xscale=-1, anchor=B, color=brown] (pnp1) {}
4  (pnp1.C) node[npn, anchor=C, color=green] (npn1) {}
5  (pnp2.C) node[npn, xscale=-1, anchor=C, color=magenta] (npn2) {}
6  (pnp1.E) -- (pnp2.E) (npn1.E) -- (npn2.E)
7  (pnp1.B) node[circ] {} |- (pnp2.C) node[circ] {}
8  ;\end{circuitikz}
```

The all-in-one stream of bipoles poses some challanges, as only the actual body of the bipole, and not the connecting lines, will be rendered in the specified color. Also, please notice the curly braces around the to:

```
1Ω
| \begin{circuitikz} \draw
| (0,0) to[V=1<\volt>] (0,2)
| (0,0) to[R=1<\ohm>, color=red] (2,2) }
| (0,0) to[C=1<\farad>] (2,0) --- (0,0)
| (0,0) to[C=1<\farad>] (2,0) --- (0,0)
```

Which, for some bipoles, can be frustrating:

```
1Ω
| \begin{circuitikz} \draw
| (0,0){ to[V=1<\volt>, color=red] (0,2) }
| to[R=1<\ohm>] (2,2)
| to[C=1<\farad>] (2,0) -- (0,0)
| s;\end{circuitikz}
```

The only way out is to specify different paths:

```
1 \( \text{login} \) \( \text{circuitikz} \) \\ \draw[\text{color=red}] \\ \text{2} \) \( \text{(0,0)} \) \( \text{to}[\text{V=1<\ohn>] (2,2)} \\ \text{4} \] \( \text{to}[\text{C=1<\farad>] (2,0) -- (0,0)} \\ \text{5} \) \( \text{end} \) \( \text{circuitikz} \) \\ \end{\text{circuitikz}} \)
```

And yes: this is a bug and not a feature...

7 FAQ

Q: When using \tikzexternalize I get the following error:

! Emergency stop.

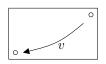
A: The TikZ manual states:

Furthermore, the library assumes that all MFX pictures are ended with $\end{tikzpicture}$.

Just substitute every occurrence of the environment circuitikz with tikzpicture. They are actually pretty much the same.

Q: How do I draw the voltage between two nodes?

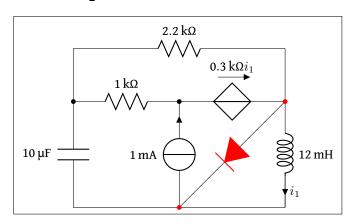
A: Between any two nodes there is an open circuit!



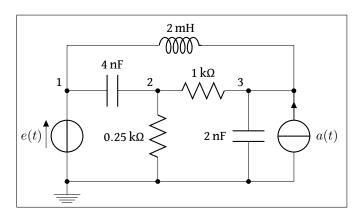
```
1\begin{circuitikz} \draw
2  node[ocirc] (A) at (0,0) {}
3  node[ocirc] (B) at (2,1) {}
4  (A) to[open, v=$v$] (B)
5 ;\end{circuitikz}
```

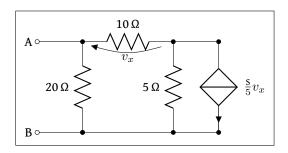
Q: I cannot write to [R = $R_1=12V$] nor to [ospst = open, 3s]: I get errors. A: It is a limitation of the TikZ parser. Use to [R = $R_1=12V$] and to [ospst = open{,} 3s] instead.

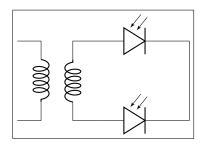
8 Examples

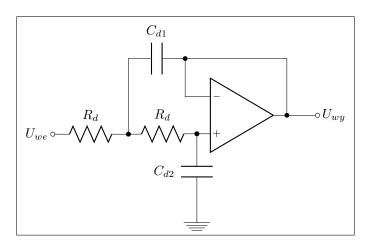


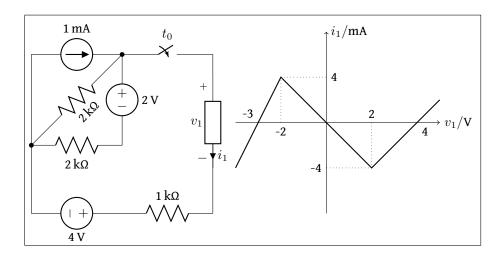
```
1\begin{circuitikz} [scale =1.4]\draw
2 (0,0) to [C, l=10<\micro\farad>] (0,2) -- (0,3)
3 to [R, l=2.2<\kilo\ohm>] (4,3) -- (4,2)
4 to [L, l=12<\milli\henry>, i=$i_1$] (4,0) -- (0,0)
5 (4,2) { to [D*, *-*, color=red] (2,0) }
6 (0,2) to [R, l=1<\kilo\ohm>, *-] (2,2)
7 to [cV, v=$\SI {.3}{\kilo\ohm} i_1$] (4,2)
8 (2,0) to [I, i=1<\milli\ampere>, -*] (2,2)
9 ;\end{circuitikz}
```



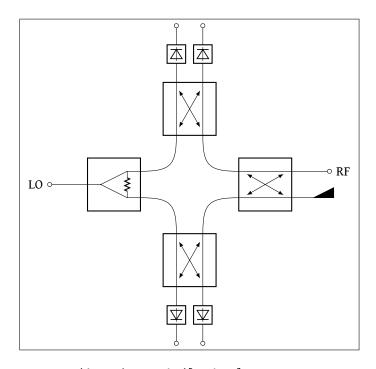








```
1\begin{circuitikz}[ scale =1.2, american]\draw
    (0,2) to [I=1<\milli\ampere>] (2,2)
          to[R, l_=2<\frac{-*}{1000}] (0,0)
          to[R, l_=2<\langle kilo \rangle] (2,0)
          to[V, v_=2<\volt>] (2,2)
          to[cspst, l=$t_0$] (4,2) -- (4,1.5)
          to [generic, i=$i_1$, v=$v_1$] (4,-.5) -- (4,-1.5)
    (0,2) -- (0,-1.5) to [V, v_=4<\volt>] (2,-1.5)
          to [R, l=1<\frac{h}{h}] (4,-1.5);
    \begin{scope}[xshift = 6.5 cm, yshift = .5 cm]
11
     \frac{-}{0} (-2,0) -- (2.5,0) node[anchor=west] {v_1/volt};
12
      \del{draw} [->] (0,-2) -- (0,2) node[anchor=west] <math>{i_1/\S {\milli\ampere}};
13
     \frac{-1,0}{node[anchor=north]} \{-2\} (1,0) node[anchor=south] \{2\}
            (0,1) node[anchor=west] \{4\} (0,-1) node[anchor=east] \{-4\}
15
            (2,0) node[anchor=north west] {4}
16
            (-1.5,0) node[anchor=south east] \{-3\};
17
     \draw[thick] (-2,-1) -- (-1,1) -- (1,-1) -- (2,0) -- (2.5,.5);
      \draw [dotted] (-1,1) -- (-1,0) (1,-1) -- (1,0)
            (-1,1) -- (0,1) (1,-1) -- (0,-1);
20
    \end{scope}
22 \end{circuitikz}
```



```
\begin{circuitikz }[ scale =1]
                  \ctikzset {bipoles/detector/width=.35}
                  \ctikzset {quadpoles/coupler/width=1}
                  \ctikzset {quadpoles/coupler/height=1}
                  \ctikzset { tripoles /wilkinson/width=1}
                  \ctikzset { tripoles /wilkinson/height=1}
                  \ \draw[help lines, red, thin, dotted] (0,-5) grid (5,5);
                  \draw
                  (-2,0) node[wilkinson](w1){}
                  (2,0) node[coupler] (c1) {}
                  (0,2) node[coupler,rotate=90] (c2) {}
                  (0,-2) node[coupler,rotate=90] (c3) {}
                  (w1.out1) .. controls ++(0.8,0) and ++(0,0.8) .. (c3.3)
                  (w1.out2) .. controls ++(0.8,0) and ++(0,-0.8) .. (c2.4)
                  (c1.1) .. controls ++(-0.8,0) and ++(0,0.8) .. (c3.2)
                  (c1.4) .. controls ++(-0.8,0) and ++(0,-0.8) .. (c2.1)
                  (w1.in) to [short, -0] ++(-1,0)
                  (w1.in) node[left=30] {LO}
                  (c1.2) node[match,yscale=1] {}
                  (c1.3) to [short, -o] ++(1,0)
                  (c1.3) node[right=30] {RF}
                  (c 2.3) to [detector,-0] ++(0,1.5)
                  (c 2.2) to [detector,-0] ++(0,1.5)
                  (c 3.1) to [detector, -0] ++(0, -1.5)
24
                  (c 3.4) to [detector, -0] ++(0, -1.5)
25
          \end{circuitikz}
```

9 Revision history

version 0.3.1 (?)

- 1. typo in documentation
- 2. fixed an error in the angle computation in voltage and current routines
- 3. fixed problem with label size when scaling a tikz picture
- 4. added gas filled surge arrester
- 5. added compatibility option to work with Tikz's own circuit library

version 0.3.0 (20121229)

- 1. fixed gate node for a few transistors
- 2. added mixer
- 3. added fully differential op amp (by Kristofer M. Monisit)
- 4. now general settings for the drawing of voltage can be overridden for specific components
- 5. made arrows more homogeneous (either the current/voltage one, or latex' by pgf)
- 6. added the single battery cell
- 7. added fuse and asymmetric fuse
- 8. added toggle switch
- 9. added varistor, photoresistor, thermocouple, push button
- 10. added thermistor, thermistor ptc, thermistor ptc
- 11. fixed misalignment of voltage label in vertical bipoles with names
- 12. added isfet
- 13. added noiseless, protective, chassis, signal and reference grounds (Luigi Luigi «Liverpool»)

version 0.2.4 (20110911).

- 1. added square voltage source (contributed by Alistair Kwan)
- 2. added buffer and plain amplifier (contributed by Danilo Piazzalunga)
- 3. added squid and barrier (contributed by Cor Molenaar)
- 4. added antenna and transmission line symbols contributed by Leonardo Azzinnari
- 5. added the changeover switch spdt (suggestion of Fabio Maria Antoniali)
- 6. rename of context.tex and context.pdf (thanks to Karl Berry)
- 7. updated the email address
- 8. in documentation, fixed wrong (non-standard) labelling of the axis in an example (thanks to prof. Claudio Beccaria)
- 9. fixed scaling inconsistencies in quadrupoles
- 10. fixed division by zero error on certain vertical paths

11. introduced options straighlabels, rotatelabels, smartlabels

version 0.2.3 (20091118).

- 1. fixed compatibility problem with label option from tikz
- 2. Fixed resizing problem for shape ground
- 3. Variable capacitor
- 4. polarized capacitor
- 5. ConTeXt support (read the manual!)
- 6. nfet, nigfete, nigfetd, pfet, pigfete, pigfetd (contribution of Clemens Helfmeier and Theodor Borsche)
- 7. nifet, pifet (contribution of Danilo Piazzalunga)
- 8. pigbt, nigbt
- 9. backward incompatibility potentiometer is now the standard resistor-with-arrow-in-the-middle; the old potentiometer is now known as variable resistor (or vR), similarly to variable inductor and variable capacitor
- 10. triac, thyristor, memristor
- 11. new property "name" for bipoles
- 12. fixed voltage problem for batteries in american voltage mode
- 13. european logic gates
- 14. backward incompatibility new american standard inductor. Old american inductor now called "cute inductor"
- 15. *backward incompatibility* transformer now linked with the chosen type of inductor, and version with core, too. Similarly for variable inductor
- 16. *backward incompatibility* styles for selecting shape variants now end are in the plural to avoid conflict with paths
- 17. new placing option for some tripoles (mostly transistors)
- 18. mirror path style

version 0.2.2 (20090520).

- 1. Added the shape for lamps.
- 2. Added options europeanresistor, europeaninductor, americanresistor and americaninductor, with corresponding styles.
- 3. **Fixed**: error in transistor arrow positioning and direction under negative xscale and yscale.

version 0.2.1 (20090503).

- 1. Op-amps added.
- 2. Added options arrowmos and noarrowmos.

version 0.2 First public release on CTAN (20090417).

1. **Backward incompatibility:** labels ending with : *angle* are not parsed for positioning anymore.

- 2. Full use of TikZ keyval features.
- 3. White background is not filled anymore: now the network can be drawn on a background picture as well.
- 4. Several new components added (logical ports, transistors, double bipoles, ...).
- 5. Color support.
- 6. Integration with siunitx.
- 7. Voltage, american style.
- 8. Better code, perhaps. General cleanup at the very least.

version 0.1 First public release (2007).

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