**--Setup Your Terminal**

source /CMC/ENVIRONMENT/modelsim.env

vlib work

**--Write Your VHDL Code**

**--Compile**

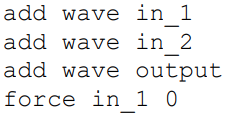
vcom vhdl\_code.vhd

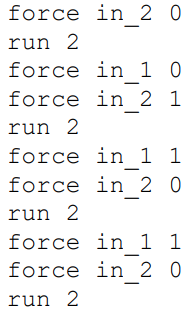
**--Simulate**

vsim vhdl\_entity\_name &

**--Do File**

nano vhdl\_do\_file.do





**--Simulate w/ Do File**

vsim -do ../current\_location/vhdl\_do\_file.do vhdl\_entity\_name &

