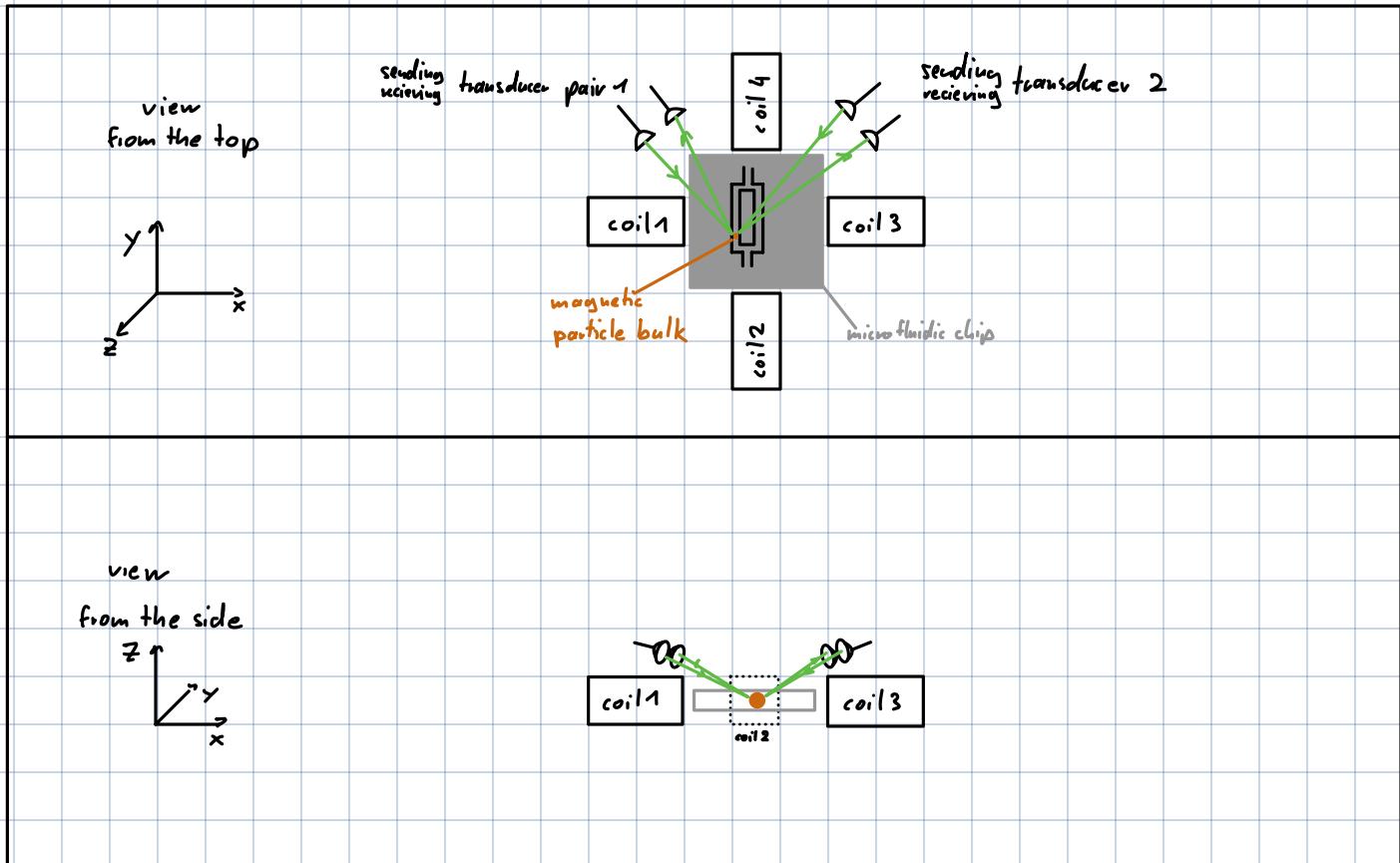


## Platform setup



## Circuit Design

