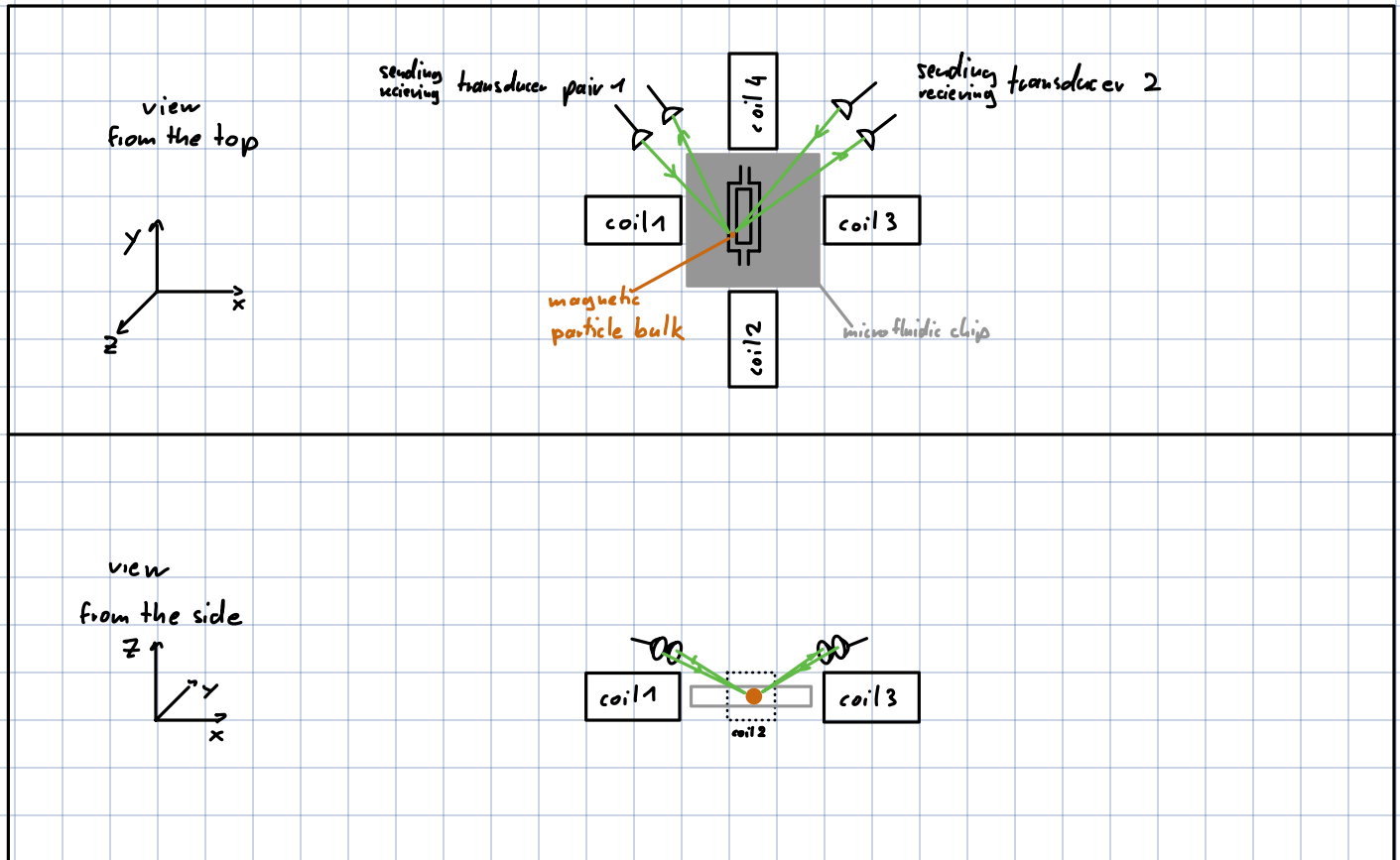


Platform setup



Circuit Design

