Signal										
/rvfpm_tb/dut/ck										
/rvfpm_tb/dut/rst										
/rvfpm_tb/dut/enable										
/rvfpm_tb/dut/instruction	20...	20...	20...	20...	20...	20...	20...	20...	20...	00000000
/rvfpm_tb/dut/id	157	158	159	160	161	162	163	164	165	0
/rvfpm_tb/dut/data_fromXReg	00000000									
/rvfpm_tb/dut/fpu_ready										
/rvfpm_tb/dut/pipelineFull										
registerFile (subset)										
/rvfpm_tb/uin_rvfpm/registerFile[1]	-1.41250									
/rvfpm_tb/uin_rvfpm/registerFile[6]	1.41250									
/rvfpm_tb/uin_rvfpm/registerFile[10]	8.204...	1.4...	-1.4...	1.41250						
/rvfpm_tb/uin_rvfpm/registerFile[11]	3.29400e+31				-1.4...	1.4...	-1.41250			
/rvfpm_tb/uin_rvfpm/registerFile[12]	2.91132e+20							-1.4...	1.41250	
/rvfpm_tb/uin_rvfpm/registerFile[13]	2.21976e-13									1.4... -1.4...

30000 ns   30200 ns   30400 ns

Entity:rvfpm_tb Architecture: Date: Mon Dec 11 17:44:06 CET 2023 Row: 1 Page: 1