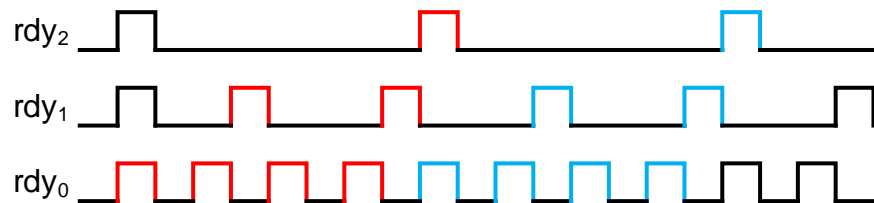


branching output ready signals



Inv_branching input ready signals

