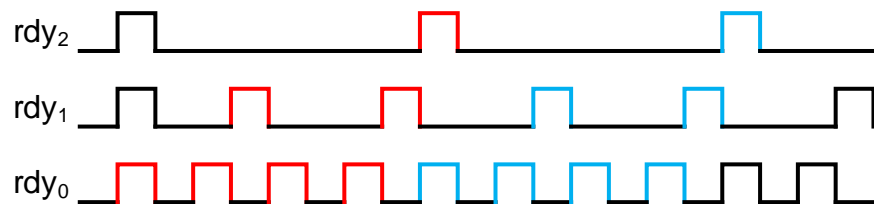


branching output ready signals



Inv\_branching input ready signals

