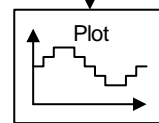
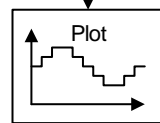
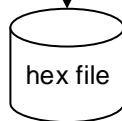
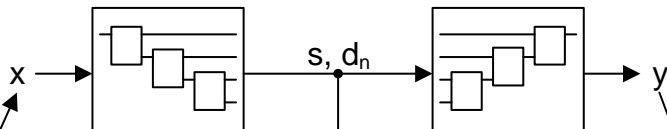


Xilinx Vivado Simulation

VHDL testbench



Stimuli
generation

wavlelet
transform

inverse
wavlelet
transform

MATLAB