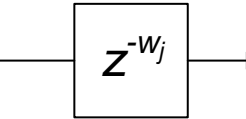
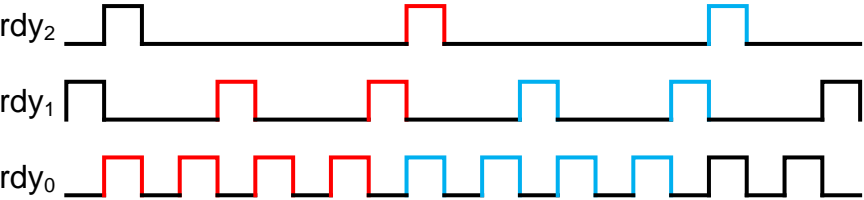


branching output ready signals



inv\_branching input ready signals

