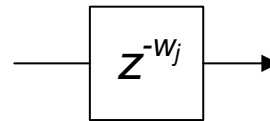
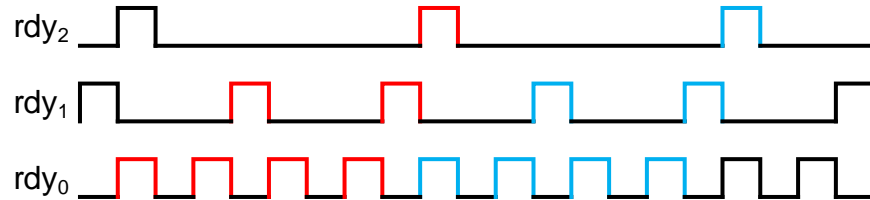


branching output ready signals



inv_branching input ready signals

