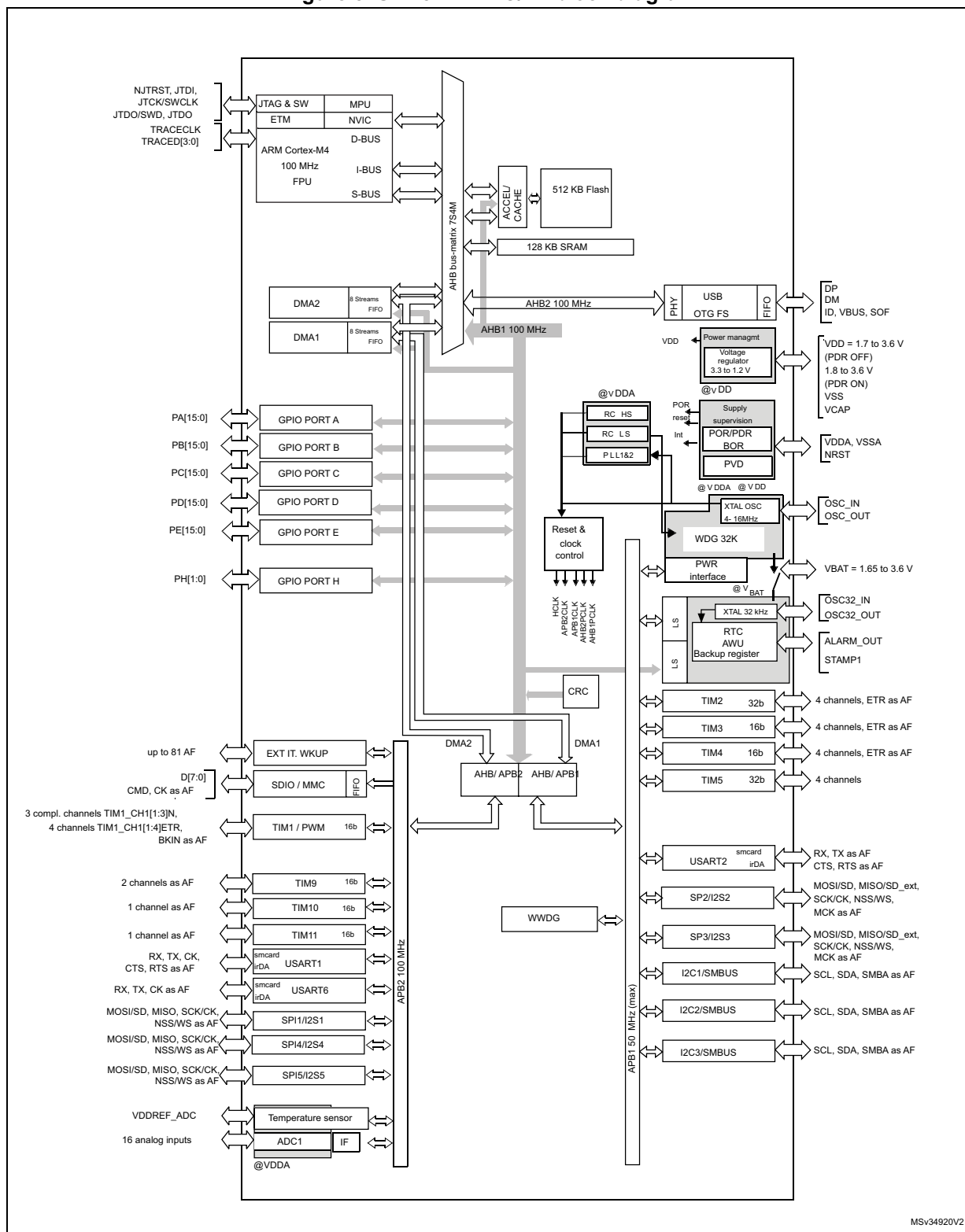


Figure 3. STM32F411xC/xE block diagram



MSV34920V2

1. The timers connected to APB2 are clocked from TIMxCLK up to 100 MHz, while the timers connected to APB1 are clocked from TIMxCLK up to 100 MHz.