

START

GO TO BANK1

Se pasa al banco 1

S0=TRISB0=1
S1=TRISB1=1
S2=TRISB2=1
S3=TRISB3=1
S4=TRISB4=1
MI=TRISC0=0
MIR=TRIC1=0
MD=TRISBC2=0
MDR=TRISC3=0
LR=TRISBC4=0
LAI=TRISBC5=0
LAD=TRISBC6=0

Se definen las entradas (Puerto B [0-4])
y salidas (Puerto C [0-7])

RP0=0

W = Port B

W = d31 = 0b 0 0 0 B4 1 1 1 1

Registro 20x = W

W = W AND 0b 0 0 0 1 0 0 0 0

Registro 21x = W (0 0 0 B4 0 0 0 0)

Registro 21x = Rotar a la derecha registro 21x (0 0 0 0 B4 0 0 0)

Registro 21x = Rotar a la derecha registro 21x (0 0 0 0 0 B4 0 0)

Registro 21x = Rotar a la derecha registro 21x (0 0 0 0 0 0 B4 0)

Registro 21x = Rotar a la derecha registro 21x (0 0 0 0 0 0 0 B4)

W = registro 21x

W = W AND 0b00000001 (0 0 0 0 0 0 0 B4)

Registro 21x = W

Vuelvo al banco 0

S4=B4 -> Registro 21

W = Port B

W = d85 = 0b 0 1 0 1 B3 1 0 1

Registro 20x = W

W = W AND 0b00001000

Registro 22x = W (0 0 0 0 B3 0 0 0)

Registro 22x = Rotar a la derecha registro 22x (0 0 0 0 0 B3 0 0)

Registro 22x = Rotar a la derecha registro 22x (0 0 0 0 0 0 B3 0)

Registro 22x = Rotar a la derecha registro 22x (0 0 0 0 0 0 0 B3)

W = registro 22x

W = W AND 0b00000001 (0 0 0 0 0 0 0 B3)

Registro 22x = W

S3=B3 -> Registro 22

W = Port B

W = d170 = 0b 1 0 1 0 1 B2 1 0

Registro 20x = W

W = W AND 0b00000100

Registro 23x = W (0 0 0 0 0 B2 0 0)

Registro 23x = Rotar a la derecha registro 23x (0 0 0 0 0 0 B2 0)

Registro 23x = Rotar a la derecha registro 23x (0 0 0 0 0 0 0 B2)

W = registro 23x

W = W AND 0b00000001 (0 0 0 0 0 0 0 B2)

Registro 23x = W

S2=B2 -> Registro 23

W = Port B

W = d85 = 0b 0 1 0 1 0 1 B1 1

Registro 20x = W

W = W AND 0b00000010

Registro 24x = W (0 0 0 0 0 0 B1 0)

Registro 24x = Rotar a la derecha registro 24x (0 0 0 0 0 0 0 B1)

W = registro 24x

W = W AND 0b00000001 (0 0 0 0 0 0 0 B1)

Registro 24x = W

S1=B1 -> Registro 24

W = Port B

W = d170 = 0b 1 0 1 0 1 0 1 B0

Registro 20x = W

W = W AND 0b00000001 (0 0 0 0 0 0 0 B0)

Registro 25x = W

S0=B0 -> Registro 25

RP0=0

W = Port B

W = d170 = 0b 1 0 1 B4 1 0 1 0

Registro 20x = W

W = W AND 0b 0 0 0 1 0 0 0 0

Registro 26x = W (0 0 0 B4 0 0 0 0)

Registro 26x = Rotar a la derecha registro 26x (0 0 0 0 B4 0 0 0)

Registro 26x = Rotar a la derecha registro 26x (0 0 0 0 0 B4 0 0)

Registro 26x = Rotar a la derecha registro 26x (0 0 0 0 0 0 B4 0)

Registro 26x = Rotar a la derecha registro 26x (0 0 0 0 0 0 0 B4)

Registro 26x = Complemento 26x (1 1 1 1 1 1 1 B4')

W = registro 26x

W = W AND 0b00000001 (0 0 0 0 0 0 0 B4')

Registro 26x = W

S4'=B4' -> Registro 26

RP0=0

W = Port B

W = d85 = 0b 0 1 0 1 B3 1 0 1

Registro 20x = W

W = W AND 0b 0 0 0 0 1 0 0 0

Registro 26x = W (0 0 0 0 B3 0 0 0)

Registro 27x = Rotar a la derecha registro 26x (0 0 0 0 0 B3 0 0)

Registro 27x = Rotar a la derecha registro 26x (0 0 0 0 0 0 B3 0)

Registro 27x = Rotar a la derecha registro 26x (0 0 0 0 0 0 0 B3)

Registro 27x = Complemento 27x (1 1 1 1 1 1 1 B3')

W = registro 27x

W = W AND 0b00000001 (0 0 0 0 0 0 0 B3')

Registro 27x = W

S3'=B3' -> Registro 27

W = Port B

W = d170 = 0b 1 0 1 0 1 B2 1 0

Registro 20x = W

W = W AND 0b00000100

Registro 28x = W (0 0 0 0 0 B2 0 0)

Registro 28x = Rotar a la derecha registro 28x (0 0 0 0 0 0 B2 0)

Registro 28x = Rotar a la derecha registro 28x (0 0 0 0 0 0 0 B2)

Registro 28x = Complemento (1 1 1 1 1 1 1 B2')

W = registro 28x

W = W AND 0b00000001 (0 0 0 0 0 0 0 B2')

Registro 28x = W

S2'=B2' -> Registro 28

RP0=0

W = Port B

W = d85 = 0b 0 1 0 1 0 1 B1 1

Registro 20x = W

W = W AND 0b 0 0 0 0 0 0 1 0

Registro 29x = W (0 0 0 0 0 0 B1 0)

Registro 29x = Rotar a la derecha registro 26x (0 0 0 0 0 0 0 B1)

Registro 29x = Complemento 29x (1 1 1 1 1 1 1 B1')

W = registro 29x

W = W AND 0b00000001 (0 0 0 0 0 0 0 B1')

Registro 29x = W

S1'=B1' -> Registro 29

W = Port B

W = d170 = 0b 1 0 1 0 1 0 1 B0

Registro 20x = W

W = W AND 0b00000001 (0 0 0 0 0 0 0 B0)

Registro 30x = W

Registro 30x = Complemento Registro 30x (0 0 0 0 0 0 0 B0')

W = registro 30x

W = W AND 0b00000001 (0 0 0 0 0 0 0 B0')

Registro 30x = W

S0'=B0' -> registro 30

No

Yes

