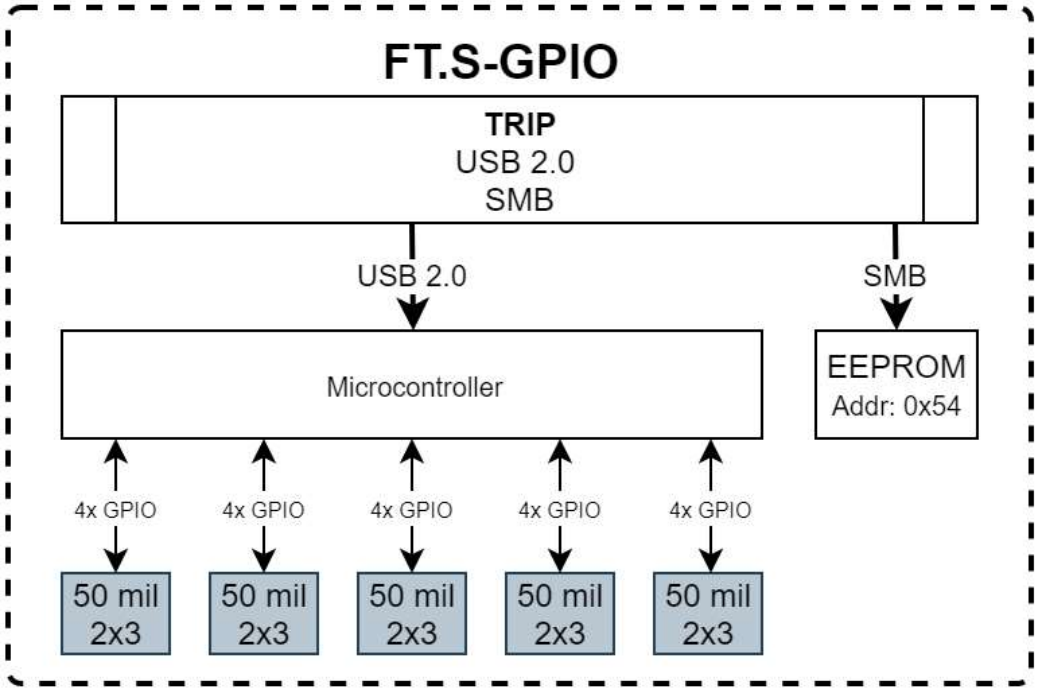
INDEX



BOARD REVISION HISTORY

BOARD REVISION: 1.0

- INITIAL REVISION

BOM ITEMS, NOT IN PCB

PCB1 — PCB, FT.S-GPIO Rev 1.0

LBL1 — P/N = 374F08P08C 8x8mm x 3 Barcode

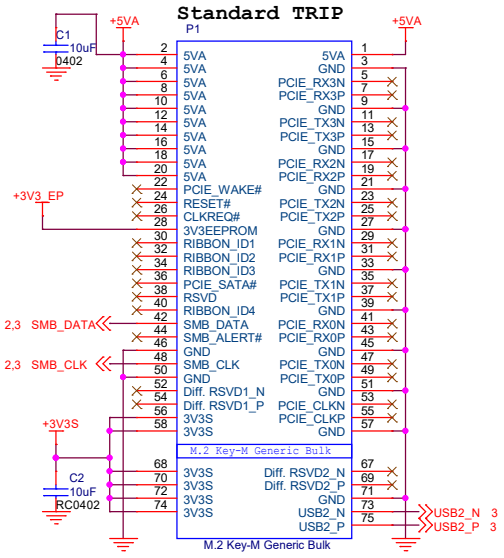
ZZ1 — P/N = 188Z10030 — PARSER_VERSION_1.0

M1 — P/N = 290E010010 — Mech part, Ribbon Latch, Base, Yellow

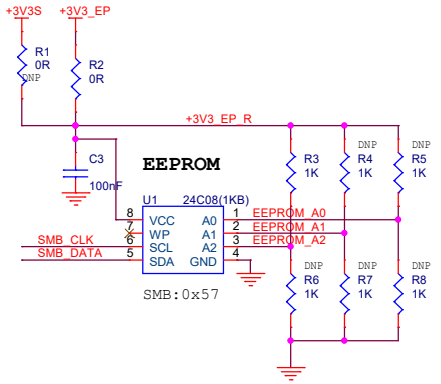
T1 — Plug for the terminal Block — P/N = 178A00500 — Terminal, Plug, 5p, 3.81mm

CompuLab Ltd. (972) 4 8238567
17 HaYetsira Street
Yokneam Illit, Israel 2069208

Size	Title	Rev
A3	INDEX	1.0
	Document Number: <Doc>	
Date: Friday, May 01, 2020	Sheet 1 of 4	



I2C EEPROM

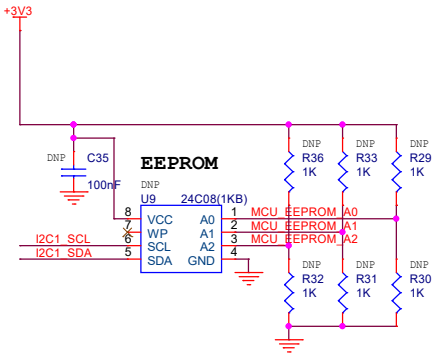


CompuLab Ltd. (972) 4 8238567
17 HaYetsira Street
Yokneam Illit, Israel 2069208

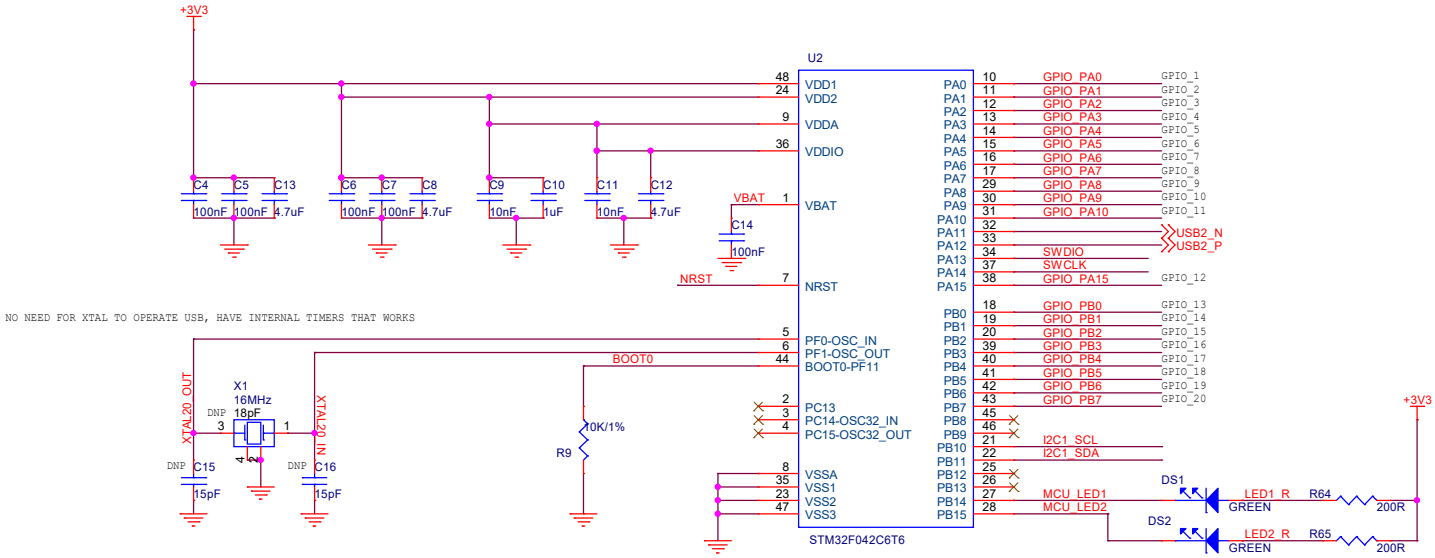
Size	Title	Rev
A3	02- connector	1.0
	Document Number: <Doc>	
Date: Friday, May 01, 2020	Sheet 2 of 4	

MCU EEPROM

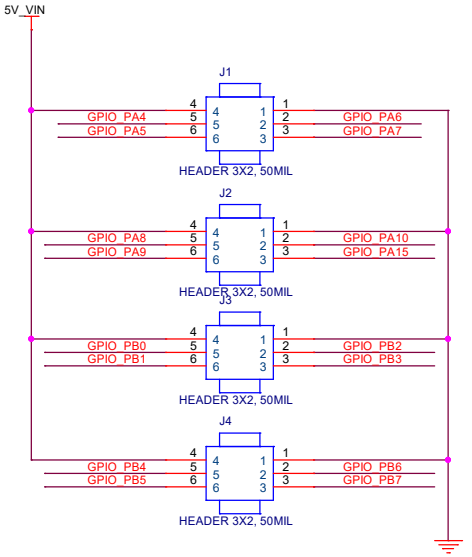
ADDED AS PROVISION TO ADD OPTION FOR STORING THE GPIOS STATE AFTER REBOOT



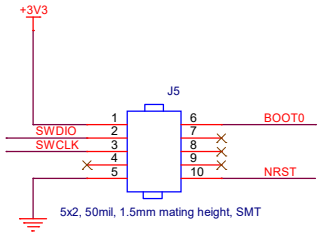
MCU STM32F042C6T6



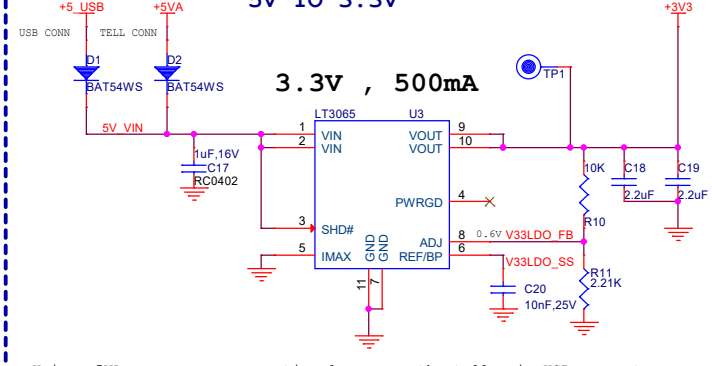
TO FT.V-TERM4



DEBUG CONNECTOR

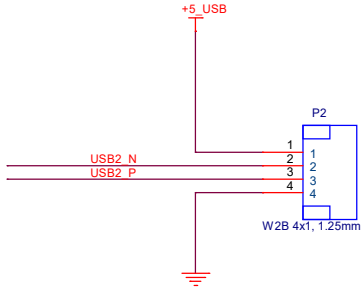


5V TO 3.3V

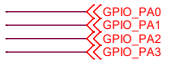


Using +5VA as a source or optionaly source the tell via USB connector

USB CONNECTOR



ON BOARD ISOLATION GPIO



CompuLab Ltd. (972) 4 8238567
17 HaYetsira Street
Yokneam Illit, Israel 2069208

Size	Title	Rev
A3	03-MCU	1.0
	Document Number: <Doc>	
Date: Friday, May 01, 2020	Sheet 3 of 4	

GPIO

GPIO_PA0
GPIO_PA1
GPIO_PA2
GPIO_PA3

ISOLATION BYPASS

ASSEMBLE ONLY WHEN ISOLATION BLOCK NOT ASSENBLED

R14 DNP 0R R1206 GPIO_PA0 — GPIO_ISO_1
R17 DNP 0R R1206 GPIO_PA1 — GPIO_ISO_2
R18 DNP 0R R1206 GPIO_PA2 — GPIO_ISO_3
R19 DNP 0R R1206 GPIO_PA3 — GPIO_ISO_4
R20 0R R1206

U6 IP4220CZ6: 1 IO1, 3 IO2, 4 IO3, 6 IO4, 2 GND, VBUS 5

ISOLATION BLOCK

5V_VIN

U4 ADM3260ARSZ
C26 100nF, C21 10uF, C22 100nF, RC0402 — PLACE CLOSE TO CHIP
9 VIN, 4 VDDP
GPIO_PA0 → 2 SCL2
GPIO_PA1 → 3 SDA2
PDIS2 → 8 PDIS, R15 10K/1%
1 GNDP1, 5 GNDP2, 7 GNDP3, 10 GNDP4
6 NC1
12 VISO, 17 VDDISO
C23 10uF, C24 100nF, C25 100nF, RC0402 — PLACE CLOSE TO CHIP
ISO_VOUT
19 SCL1 → GPIO_ISO_1
18 SDA1 → GPIO_ISO_2
13 VSEL → VSEL1
11 GNDISO1, 14 GNDISO2, 16 GNDISO3, 20 GNDISO4
15 NC2

ISO_VOUT
C27 100nF
R16 10K/1%
U5 NC7SB3157 0.08$
VCC, A, GND, B1, B0, SEL
1 R_5V1, 3 R_3V1, 6 OUT_SELECT
R12 31.6K/1%, R13 16.5K/1%
ISO_VOUT

U7 ADM3260ARSZ
5V_VIN
C29 100nF, C33 10uF, C28 100nF, RC0402 — PLACE CLOSE TO CHIP
9 VIN, 4 VDDP
GPIO_PA2 → 2 SCL2
GPIO_PA3 → 3 SDA2
PDIS2 → 8 PDIS, R23 10K/1%
1 GNDP1, 5 GNDP2, 7 GNDP3, 10 GNDP4
6 NC1
12 VISO, 17 VDDISO
C30 10uF, C31 100nF, C32 100nF, RC0402 — PLACE CLOSE TO CHIP
ISO_VOUT
19 SCL1 → GPIO_ISO_3
18 SDA1 → GPIO_ISO_4
13 VSEL → VSEL2
11 GNDISO1, 14 GNDISO2, 16 GNDISO3, 20 GNDISO4
15 NC2

ISO_VOUT
C34 100nF
R24 10K/1%
U8 NC7SB3157 0.08$
VCC, A, GND, B1, B0, SEL
1 R_5V2, 3 R_3V2, 6 OUT_SELECT
R21 31.6K/1%, R22 16.5K/1%
ISO_VOUT

OUTPUT VOLTAGE SELECT

ACTIVE HIGH

SEL	A
L	B0
H	B1

ISO_VOUT
TP2 DNP
R26 10K/1%
OUT_SELECT
PLACE ON SHARED PAD
R27 10K/1%
SW1 DIP Switch
1 — 2 SWITCH_ON
ISO_VOUT
PLACE ON SHARED PAD
R25 0R
R28 0R DNP

TERMINAL BLOCK

PLACE HOLDER. CONNECTOR WILL CHANGE

GPIO_ISO_1
GPIO_ISO_2
GPIO_ISO_3
GPIO_ISO_4
P4: 1, 2, 3, 4, 5
Terminal, 5P, 3.81mm, RA

CompuLab Ltd. (972) 4 8238567
17 HaYetsira Street
Yokneam Illit, Israel 2069208

Size	Title	Rev
A3	04 - ISOLATION	1.0
	Document Number: <Doc>	
Date: Friday, May 01, 2020	Sheet 4 of 4	