UNIVERSITY OF CALIFORNIA, SAN DIEGO

Electrical and Computer Engineering Department ECE 65 – Fall 2022

Components and Circuits lab

Midterm Exam Solutions

Closed books, one one-sided cheat sheet, and calculators are allowed

Electronic devices are not allowed.

Please put all answers in the provided sheets.

Be sure to write your name and PID on all pages.

Please do not begin until told.

Show your work.

Good luck.

Problem 1. (12 points)

You will use the last digit of your PID to answer this question.

 $V_1 = -1 \times$ the last digit of your PID. (The unit is Volts)

 $V_2 = -2 \times$ the last digit of your PID. (The unit is Volts)

For example, if your PID is A14126523, solve this problem with $V_1 = -3 V$, and $V_2 = -6 V$.

If the last digit of your PID is zero, solve this problem with $V_1 = -2 V$, and $V_2 = -4 V$.

a) Design a diode waveform shaping circuit such that the amplitude of its output voltage signal is always between V_1 and V_2 . This means

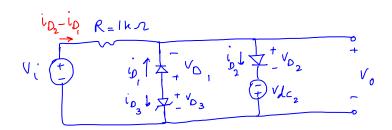
 $V_2 \leq amplitude \ of \ output \ voltage \leq V_1$

- b) Parametrically solve your designed circuit.
 - Find the transfer function of the circuit.
 - Draw the transfer function graph.

Assume you have access to regular PN junction diodes ($V_{D0} = 0.7 V$), Zener diodes (any desired V_Z), and other circuit elements.

You must use Zener diode(s) in your design.

Show your work.



$$V_{1} = -1 V, \quad V_{2} = -2 V$$

Case 1: D1 is ON and D2 Off and D3 in Zener

$$V_{O_1} = V_{O_0} = 0.7 \, \text{V}, \quad \dot{v}_{O_1} \geqslant 0 \quad , \quad V_{P_2} < V_{O_0} \quad , \quad \dot{v}_{O_2} = 0$$

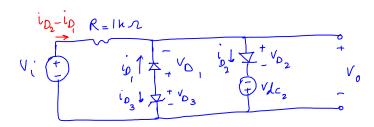
$$V_{O_3} = -V_Z \quad , \quad \dot{v}_{O_3} \leq 0$$

We want $V_0 = -2 V$ when D_1 conducts.

$$V_0 = -0.7 \ V - V_{z_1} = -2 \longrightarrow V_{z_1} = 1.3 \ V$$

$$V_{i} = -i_{D,x} 1k -0.7 V_{-1.3}$$

$$\frac{c_{0}}{c_{0}} = \frac{-V\dot{c} - 0.7 - 1.3}{1kn} \quad \Rightarrow \quad V\dot{c} \leqslant -2V$$



$$V_0 = -V_{D_1} + V_{D_3}$$

Case 2 D_1 and D_3 are off and D_2 is D_1 $V_{D_2} = V_{D_0} = 0.7 \, V, \quad c_{D_2} > 0, \quad V_{D_1} < V_{D_0}, \quad c_{D_1} = 0$ $-V_2 < V_{D_3} < V_{D_0}, \quad c_{D_3} = 0$

We want
$$V_0 = -1$$
 when D_2 conducts.

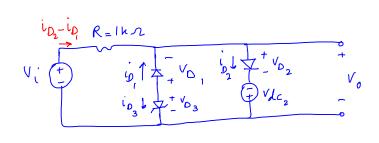
$$V_0 = V_{00} - V_{dc_2} = 0.7 - V_{dc_2} = -1 V$$

 $\Rightarrow V_{dc_2} = 1.7 V$

$$V_{i} = 1 \text{k.n.x } i_{D_{2}} + V_{D_{2}} - V_{dc_{2}}$$

$$V_{i} = 1 \text{k.n.x } i_{D_{2}} + 0.7 \text{V} - 1.7 \text{V}$$

$$i_{D_{2}} = \frac{V_{i} + 1 \text{V}}{(\text{k.n.})} > 0 \rightarrow V_{i} > -1 \text{V}$$



$$V_1 = -1 V$$
, $V_2 = -2 V$

$$V_o = -V_{D_1} + V_{D_3}$$

Also,
$$V_i = (i_{D_2} - i_{D_i})_X (kx + V_{D_2} - Vdc_2)$$

Case 3 D1 and D2 and D3 are off

$$V_{0_{2}} < 0.7 \lor$$
, $i_{0_{2}} = 0$, $V_{0_{1}} < V_{0_{0}}$, $i_{0_{1}} = 0$, $-V_{2} < V_{0_{3}} < V_{0}$, $i_{0_{3}} = 0$

$$V_{i} = (i_{D_{2}} - i_{D_{1}}) \times (k \cdot n - V_{D_{1}} + V_{D_{3}}) = -V_{D_{1}} + V_{D_{3}}$$

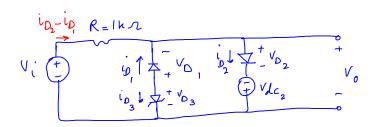
$$-\infty \langle V_{D_1} \langle 0.7 \rightarrow -0.7 \langle -V_{D_1} \rangle \langle \infty$$
$$-V_{Z} \langle V_{D_3} \langle 0.7 \rangle$$

we need V; > - 2 V for this case ==

$$-V_{Z}-0.7=-2 \longrightarrow V_{Z}=1.3 V$$

Name

PID



 $V_1 = -1 V$, $V_2 = -2 V$

Core 3

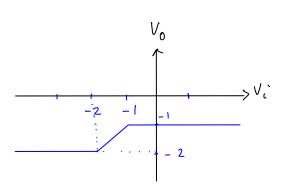
$$V_i > -2 V$$
 and

$$V_{i} = (i_{D_{2}} - i_{D_{i}}) \times \{kx + V_{D_{2}} - Vdc_{2} = V_{D_{2}} - 1.7V$$

$$V_{D_{2}} = V_{i} + 1.7V < 0.7 \longrightarrow V_{i} < -1V$$

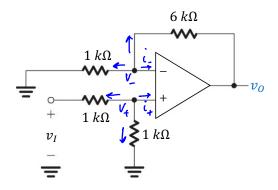
$$V_{i} < -1V \quad \text{and} \quad V_{i} > -2V \longrightarrow -2 < V_{i} < -1$$

$$\begin{cases} V_{i} \geqslant -1 \ V & V_{o} = -1 \\ -2 \langle V_{i} | \langle -1 \rangle & V_{o} = -1 \\ V_{i} | \langle -2 \rangle & V_{o} = -2 \rangle \end{cases}$$



Problem 2. (5 points)

Assuming an ideal op-amp with $V_{sat} = \pm 15V$ in the below circuit, find (derive) an expression for the voltage gain.



Show your work.

ideal op-amp and negative feedback:
$$V_{+} = V_{-}$$

also $i_{+} = i_{-} = 0$

KCL at the + input terminal:

$$\frac{V_{+}}{1kn} + \frac{V_{+} - V_{z}}{1kn} + \dot{i}_{+} = 0 \qquad \Rightarrow \qquad \frac{2V_{+}}{1kn} = \frac{V_{z}}{1kn} \Rightarrow \qquad V_{+} = \frac{1}{2}V_{z}$$

KCL at the - input terminal:

$$\frac{V_{-}}{1kn} + \frac{V_{-} - V_{0}}{6kn} + \dot{c}_{-} = 0 \rightarrow \frac{V_{-}}{1kn} + \frac{V_{-}}{6kn} - \frac{V_{0}}{6kn} = 0$$

$$6 V_{-} + V_{-} = V_{0} \rightarrow 7 V_{-} = V_{0}$$

$$\rightarrow V_{-} = \frac{1}{7} V_{0}$$

$$V_{+} = V_{-}$$

$$V_{+} = \frac{1}{2} V_{T} \implies \frac{1}{7} V_{0} = \frac{1}{2} V_{T} \implies V_{0} = \frac{7}{2} V_{T}$$

$$V_{-} = \frac{1}{7} V_{0}$$