

ECE 65: Components & Circuits Lab

Lecture 4

Diode introduction and review of circuit theory

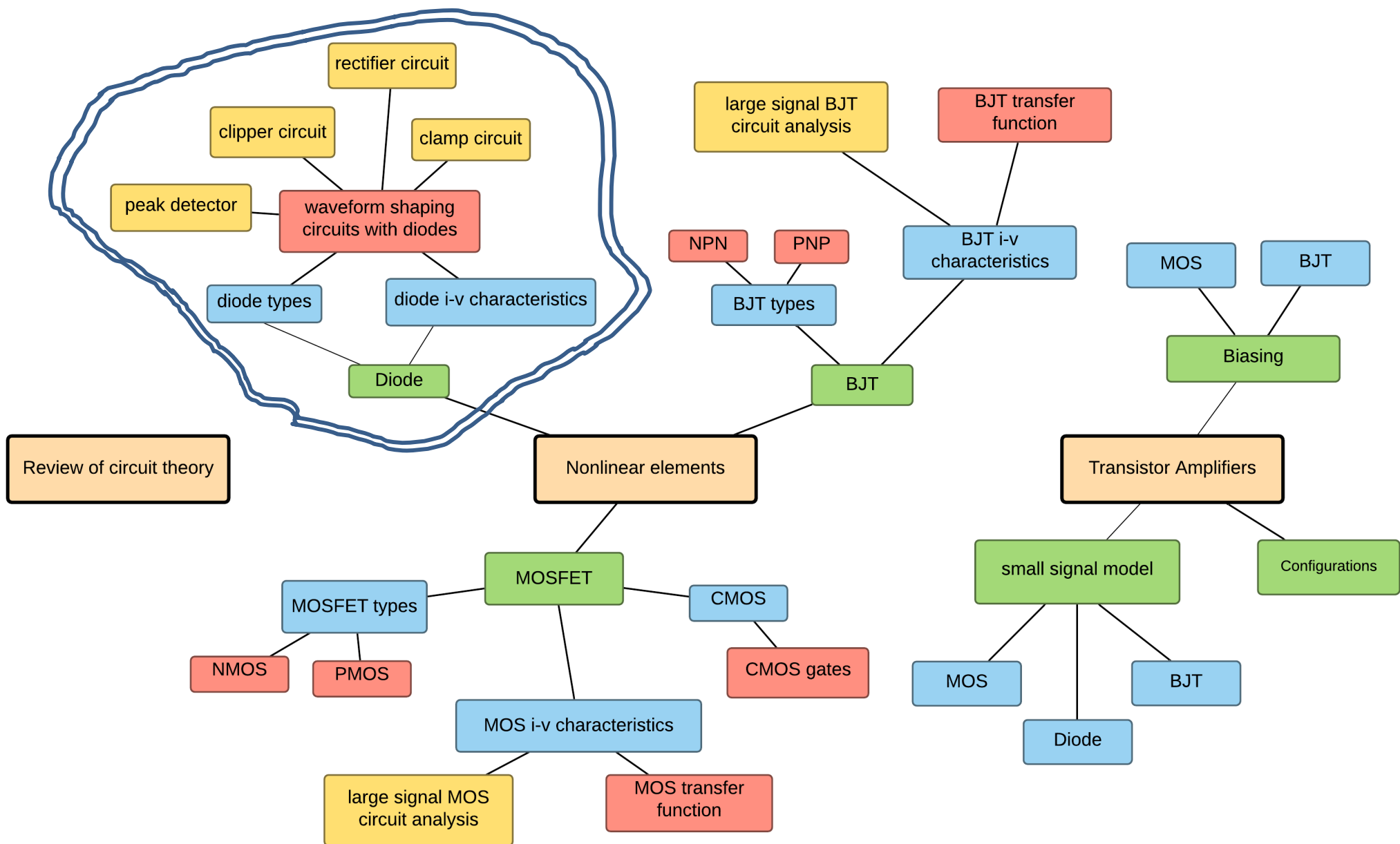
Reference notes: sections 2.1-2.8

Sedra & Smith (7th Ed): sections 4.3-4.4

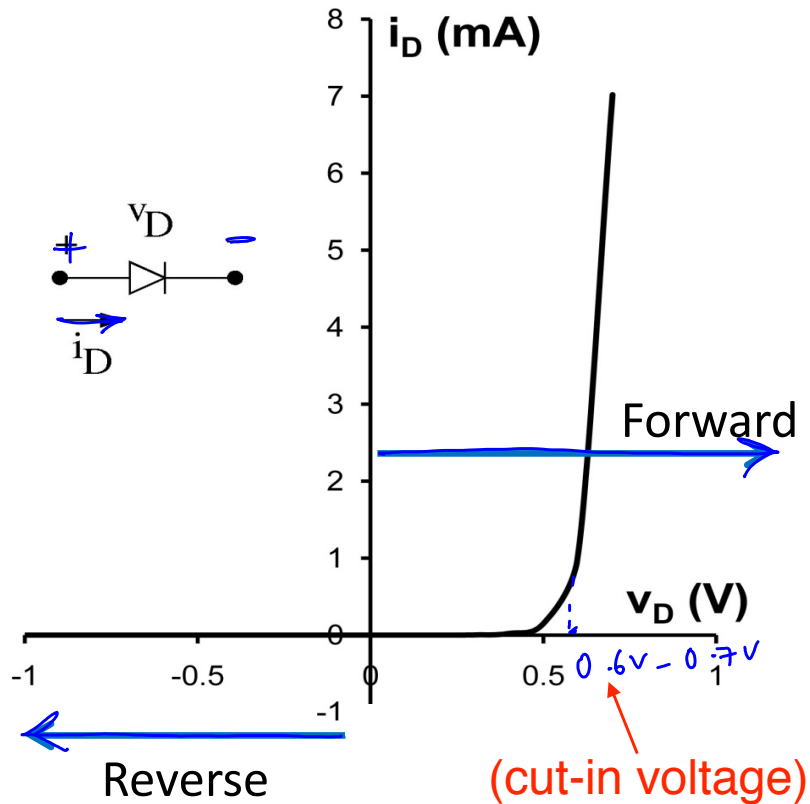
Saharnaz Baghdadchi

Course map

2. Diodes



The *iv* characteristics of a silicon junction diode



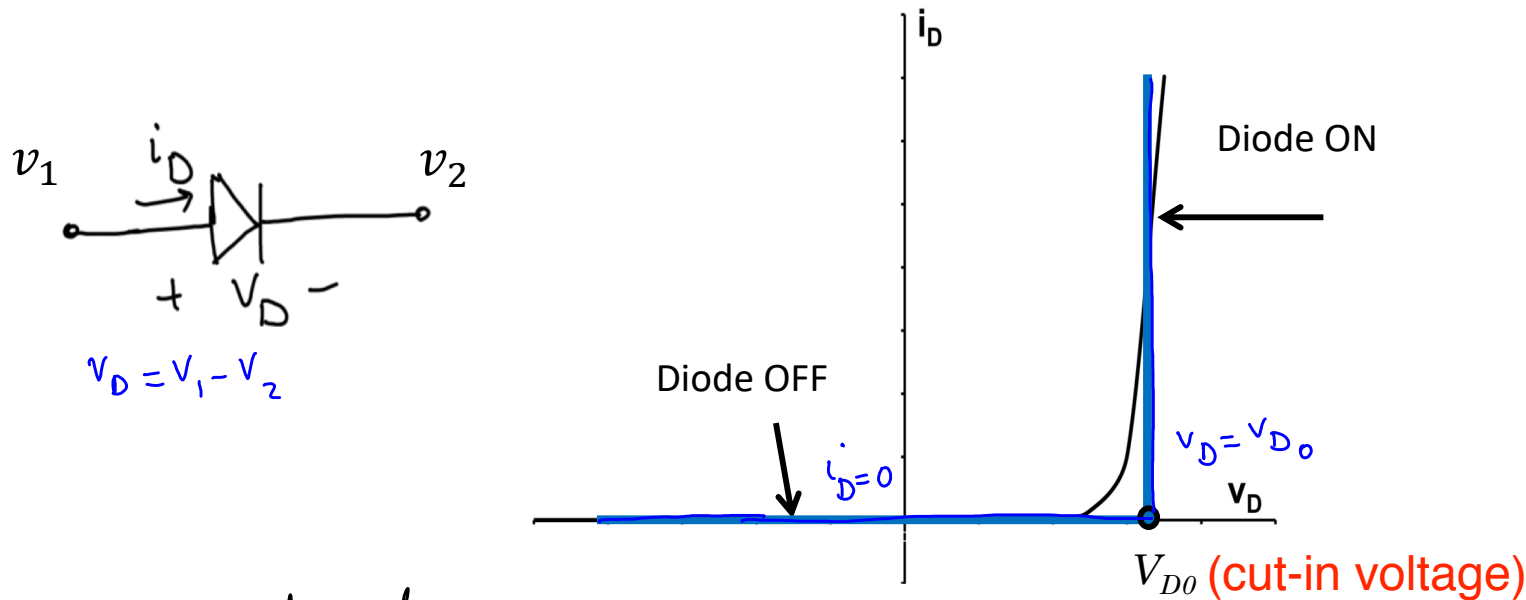
When the diode conducts,
 $i_D \gg I_S$ and:

$$i_D = I_S e^{v_D/V_T}$$

I_S : Saturation Current
(10^{-9} to 10^{-18} A)

V_T : Thermal voltage = 26mV at room temp.

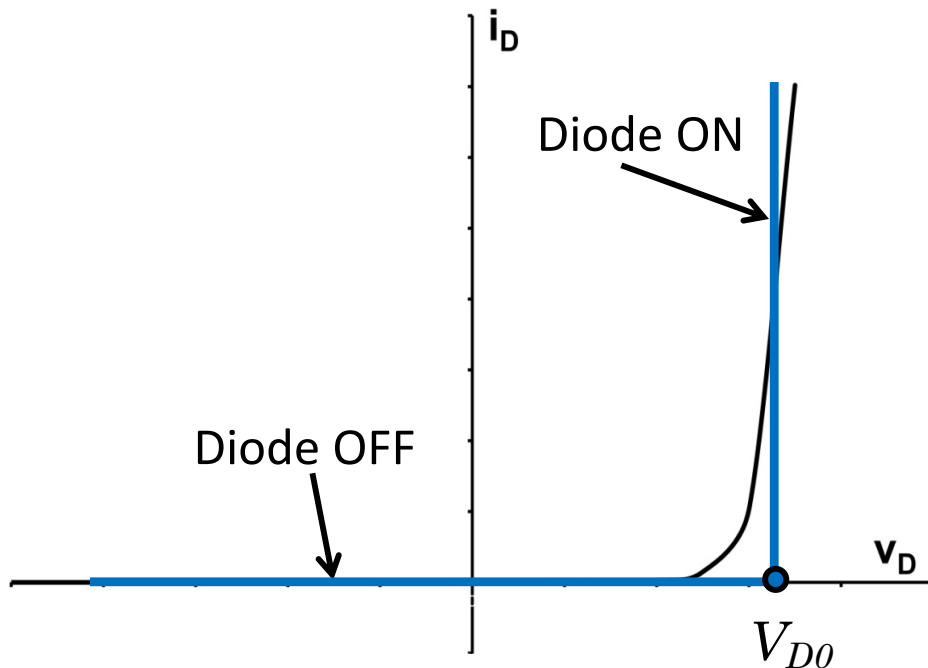
Diode piecewise-linear model:



Assume Si Diode with $V_{D0} = 0.7V$

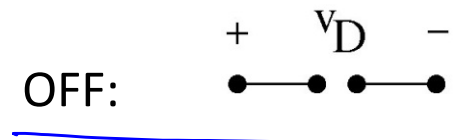
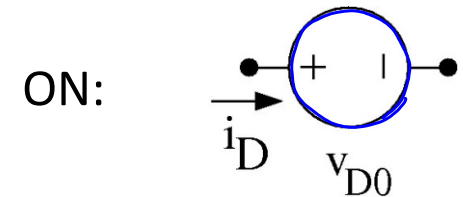
- (A) When $v_1 - v_2 < 0 \rightarrow$ diode is reverse-biased $\Rightarrow i_D = 0$
- (B) When $0 < v_1 - v_2 < 0.7 \rightarrow$ diode is forward-biased, but it's not ON, yet $\Rightarrow i_D = 0$
- (C) When $v_1 - v_2 = 0.7 \rightarrow$ diode is forward biased and it's ON $\Rightarrow i_D \geq 0$ & $V_D = V_{D0} = 0.7$

Diode piecewise-linear model:



Constant Voltage drop Model:

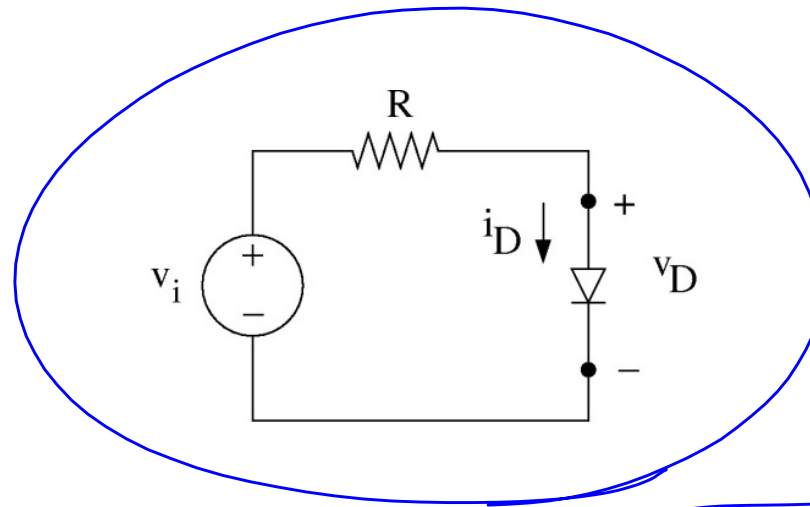
Circuit Models:



$$\left\{ \begin{array}{ll} \text{Diode ON:} & v_D = V_{D0} \quad \text{and} \quad i_D \geq 0 \\ \text{Diode OFF:} & i_D = 0 \quad \text{and} \quad v_D < V_{D0} \end{array} \right.$$

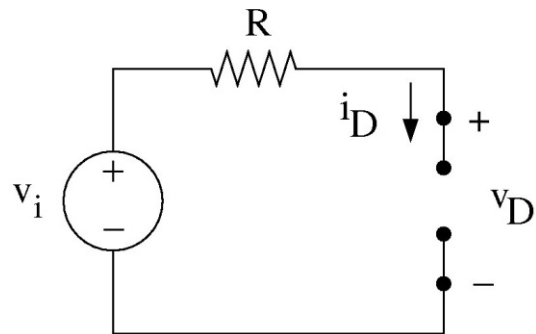
(cut-in voltage) $V_{D0} = 0.6 - 0.7 \text{ V}$ for Si

Diode circuit models:



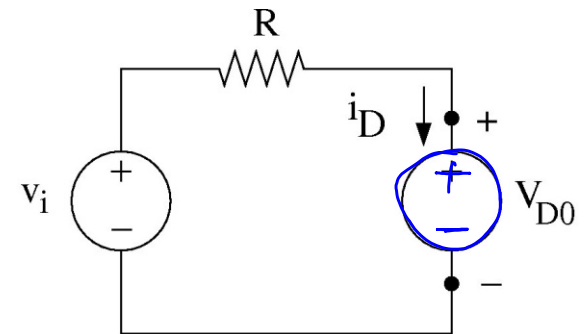
When Diode is OFF:

$i_D = 0$ and $v_D < V_{D0}$



When Diode is ON:

$i_D \geq 0$ and $v_D = V_{D0}$



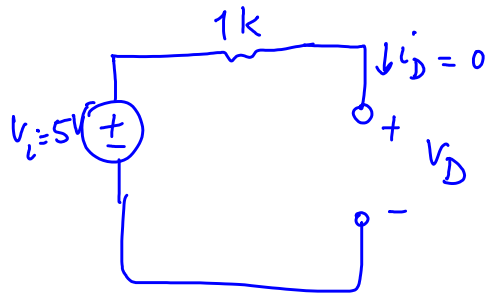
Example 1:

Find i_D and v_D for $R = 1k$, $V_i = 5V$, and Si Diode ($V_{D0} = 0.7V$).

Solution with diode circuit models:

Assume Diode is off

$$i_D = 0, V_D < V_{D0}$$



$$V_i = 1k \times i_D + V_D$$

$$V_i = V_D = 5V > 0.7 \rightarrow \text{Assumption was wrong.}$$

Assume Diode is ON

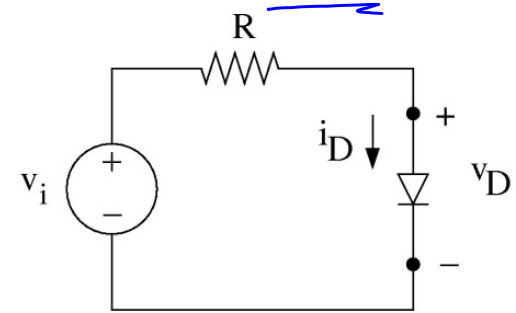
$$i_D \geq 0, V_D = V_{D0}$$



$$V_i = 1k \times i_D + 0.7V$$

$$\rightarrow i_D = \frac{5 - 0.7}{1k} = \underline{4.3mA} > 0$$

The diode is ON.



Example 2 :

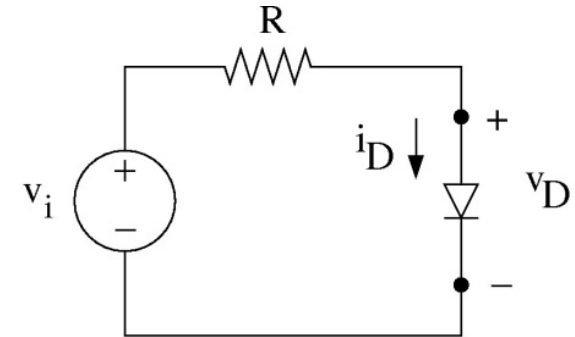
For what range of V_i the diode will be ON and for what range it will be OFF?

Find i_D and v_D for each range.

When diode is ON : $i_D \geq 0$, $V_D = V_{D_0}$

$$V_i = R i_D + V_D = R i_D + 0.7$$

$$i_D = \frac{V_i - 0.7}{R} \geq 0 \rightarrow V_i \geq 0.7 \text{ V}$$



For $V_i \geq 0.7 \text{ V}$, diode is ON, $V_D = V_{D_0}$, $i_D \geq 0$

When the diode is off : $i_D = 0$, $V_D < V_{D_0}$

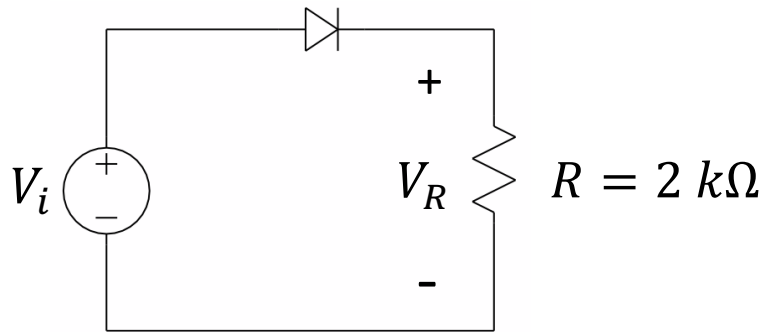
$$V_i = R i_D + V_D \rightarrow V_i = V_D, \quad V_D < V_{D_0} \rightarrow V_i < V_{D_0}$$

For $V_i < V_{D_0}$, diode is off, $i_D = 0$, $V_D < V_{D_0}$

Lecture 4 reading quiz

In this diode circuit, what is the value of V_R for $V_i = 5\text{ V}$ and $V_i = -5\text{ V}$?

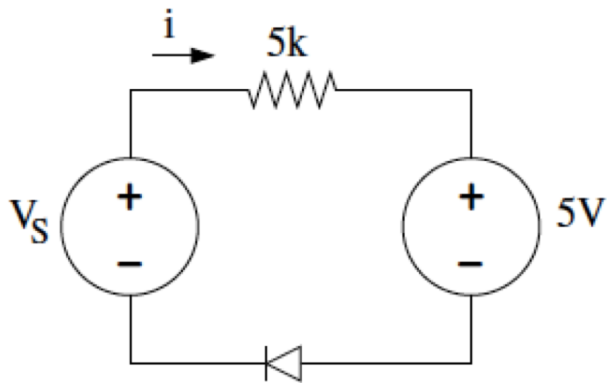
Assume $V_{D0} = 0.7\text{ V}$



Clicker question 1.

For what range of v_s , will the diode in the following circuit be ON?

(Si diodes with $V_{D0} = 0.7\text{ V}$)



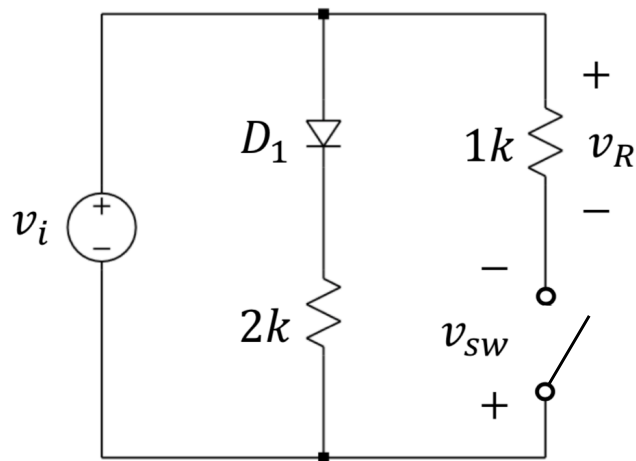
A. $v_s \geq 5\text{ V}$

B. $v_s \geq 5.7\text{ V}$

C. $v_s \geq 4.3\text{ V}$

Clicker question 2.

In the below circuit, find v_{SW} and v_R when $v_i = 5\text{ V}$.

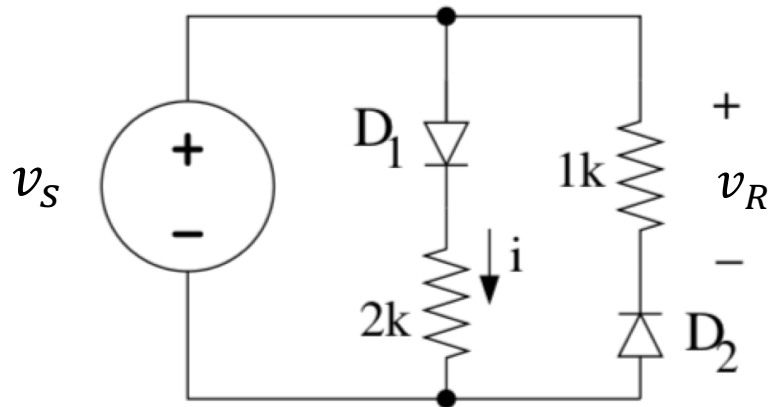


- A. $v_{SW} = 0\text{ V}$ and $v_R = 0\text{ V}$
- B. $v_{SW} = 5\text{ V}$ and $v_R = 0\text{ V}$
- C. $v_{SW} = -5\text{ V}$ and $v_R = 5\text{ V}$
- D. $v_{SW} = -5\text{ V}$ and $v_R = 0\text{ V}$

Clicker question 3.

Find v_R and i in the below circuit for $v_s = 3V$.

(Assume Si diodes with $V_{D0} = 0.7 V$)



- A. $v_R = 2.3 V, i = 1.15 mA$
- B. $v_R = 0 V, i = 1.15 mA$
- C. $v_R = 0.6 V, i = 0.3 mA$
- A. $v_R = -2.3 V, i = 1.15 mA$