

ECE 65: Components & Circuits Lab

Lecture 5

Zener Diode

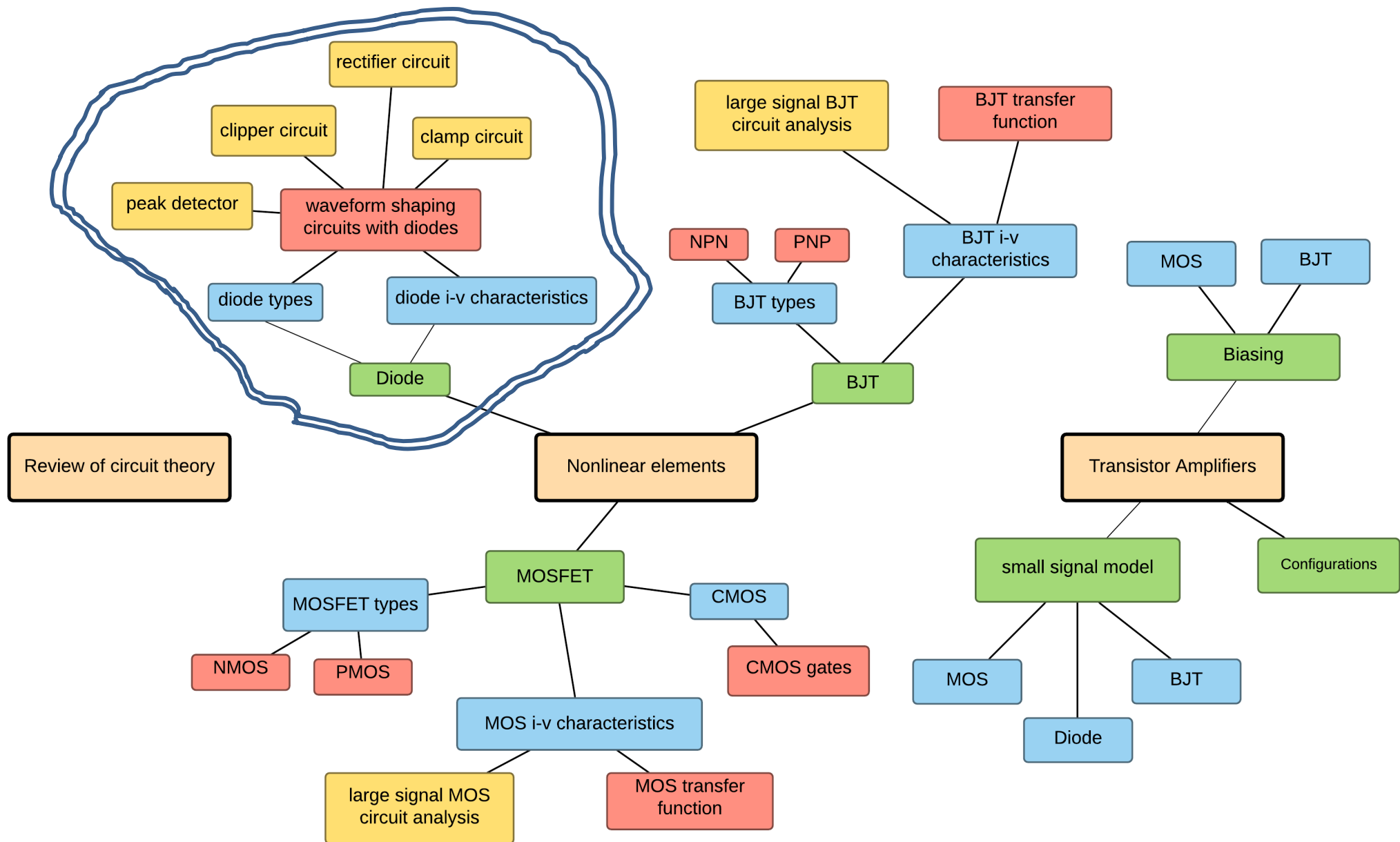
Reference notes: sections 2.1-2.8

Sedra & Smith (7th Ed): sections 4.3-4.4

Saharnaz Baghdadchi

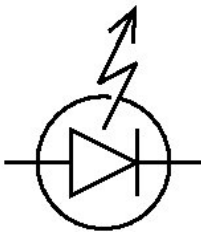
Course map

2. Diodes



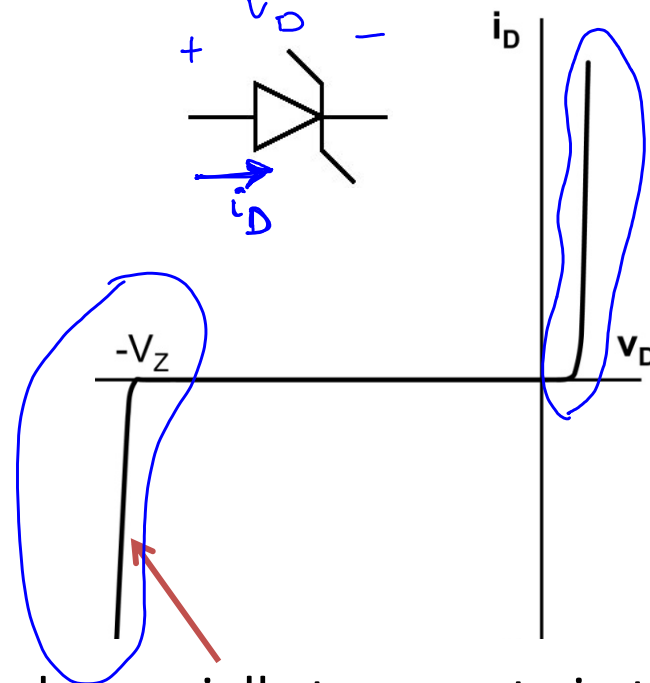
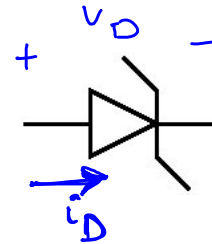
Other types of diodes

Light-emitting diode (LED)



$$V_{D0} = 1.7 - 1.9 \text{ V}$$

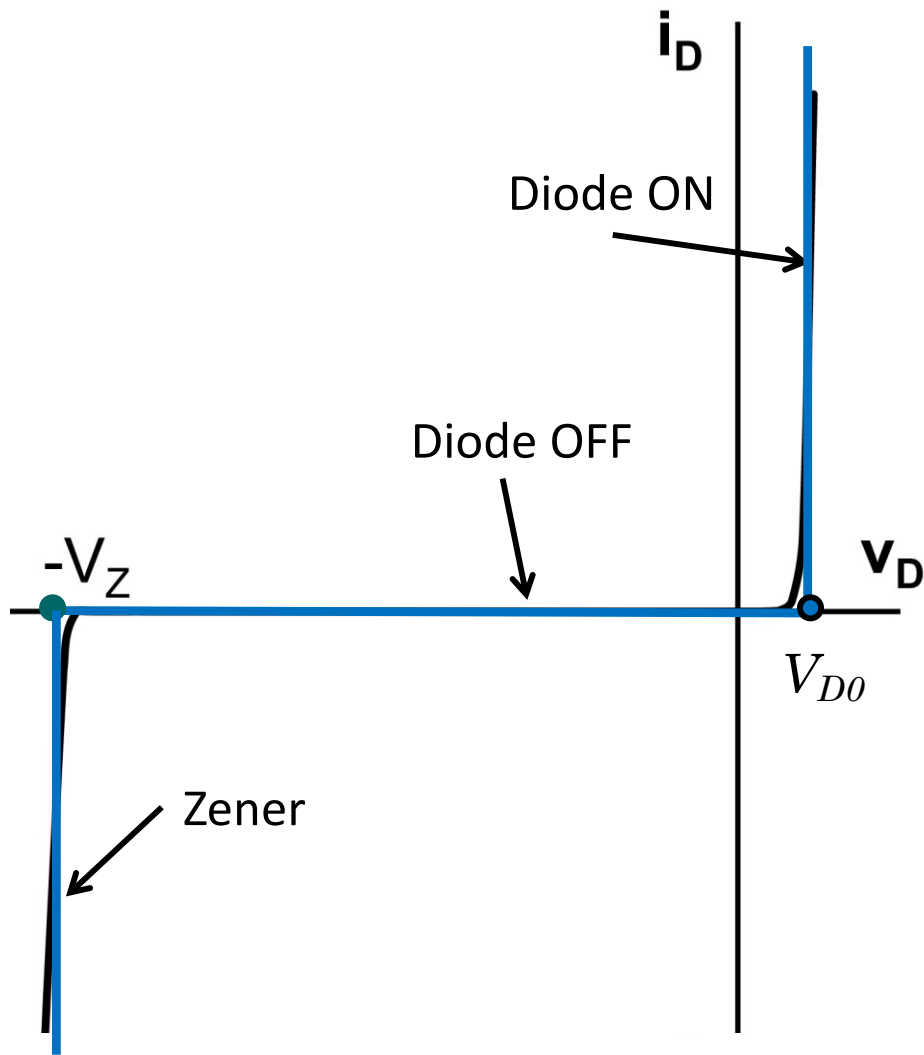
Zener Diode



Made specially to operate in the reverse breakdown region.

Useful as a “reference” voltage in many circuits.

Zener Diode piecewise-linear model

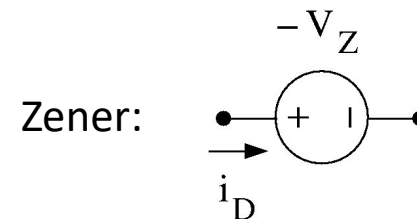
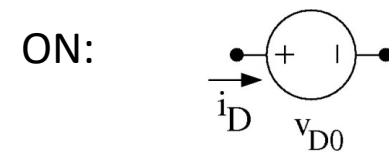


Diode ON: $v_D = V_{D0}$ and $i_D \geq 0$

Diode OFF: $i_D = 0$ and $-V_Z < v_D < V_{D0}$

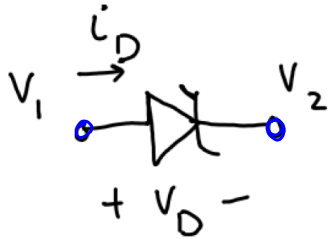
Zener: $v_D = -V_Z$ and $i_D \leq 0$

Circuit Models:



Zener Diode piecewise-linear model

Assume Zener diode with $V_D = 0.7\text{ V}$ and $V_Z = 5\text{ V}$.



- Ⓐ When $0 < V_1 - V_2 < 0.7$, diode is forward-biased, but it's not on, yet. $\Rightarrow i_D = 0$
- Ⓑ When $-5 < V_1 - V_2 < 0$, diode is reverse-biased, but it's not in the Zener region, so it's off. $\Rightarrow i_D = 0$
- Ⓒ When $V_1 - V_2 = 0.7$, diode is forward-biased and it's on $\Rightarrow i_D \geq 0$ and $V_D = V_{D_0} = 0.7\text{ V}$.
- Ⓓ When $V_1 - V_2 = -5\text{ V}$, diode is in Zener region, it conducts, $\Rightarrow i_D \leq 0$ and $V_D = -V_Z = -5\text{ V}$

Example:

In the following circuit, find i_L and v_L for $v_s = 10\text{ V}$. For what range of v_s and R_L the Zener diode will be in the Zener region and the circuit can operate as a voltage regulator? ($V_Z = 3\text{ V}$, $V_{D0} = 0.7\text{ V}$)

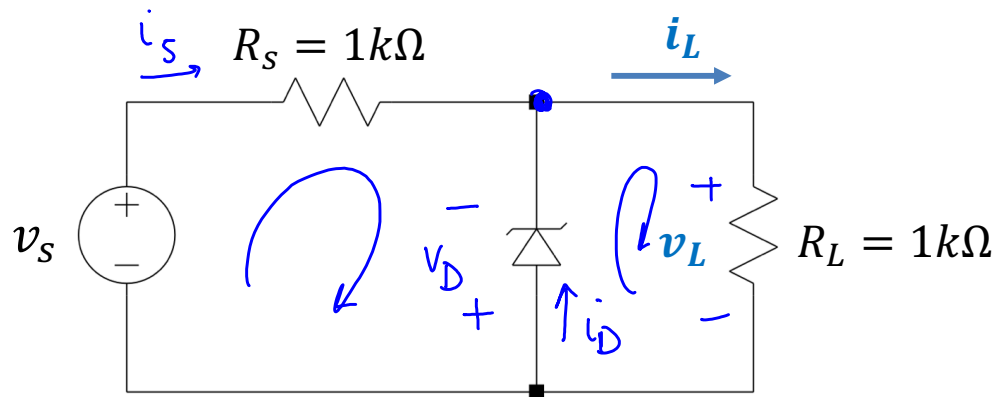
KCL:

$$\{ i_s + i_D - i_L = 0$$

KVL:

$$\{ v_s = R_s i_s - v_D$$

$$\{ v_D + v_L = 0 \rightarrow v_L = -v_D$$



Assume the diode is in the Zener region:

$$v_D = -V_Z, \quad i_D \leq 0$$

$$v_L = V_Z = 3\text{ V}$$

$$i_L = \frac{v_L}{R_L} = \frac{3\text{ V}}{1\text{ k}} = 3\text{ mA}, \quad i_L = 3\text{ mA}$$

$$i_s = \frac{v_s + v_D}{R_s} = \frac{10\text{ V} - 3\text{ V}}{1\text{ k}}$$

$$i_s = 7\text{ mA}$$

$$i_D = i_L - i_s = 3\text{ mA} - 7\text{ mA} = -4\text{ mA} < 0$$

when the diode is in the Zener region, $i_D \leq 0$.

$$i_D = i_L - i_S \leq 0 \longrightarrow i_S \geq i_L$$

$$V_L = V_Z, \quad i_L = \frac{V_L}{R_L} = \frac{V_Z}{R_L}$$

$$i_S = \frac{V_S - V_Z}{R_S}$$

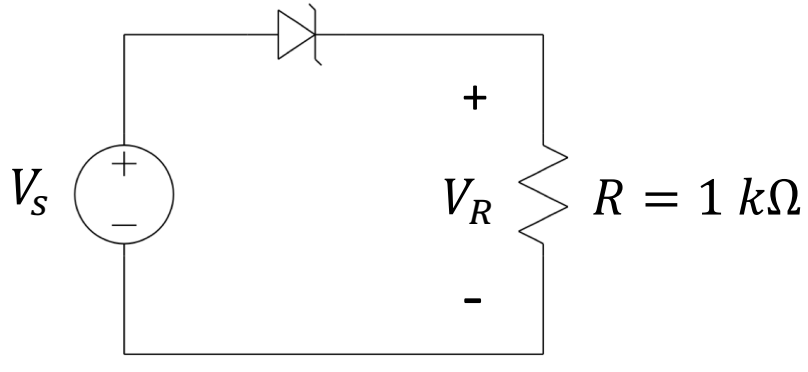
$$\frac{V_S - V_Z}{R_S} \geq \frac{V_Z}{R_L}$$

$$\frac{V_S}{R_S} \geq V_Z \left(\frac{1}{R_L} + \frac{1}{R_S} \right) \longrightarrow V_S \geq \underline{\underline{V_Z \left(1 + \frac{R_S}{R_L} \right)}}$$

$$\frac{V_S/V_Z - 1}{R_S} \geq \frac{1}{R_L} \longrightarrow R_L \geq \frac{R_S}{V_S/V_Z - 1}$$

Lecture 5 reading quiz

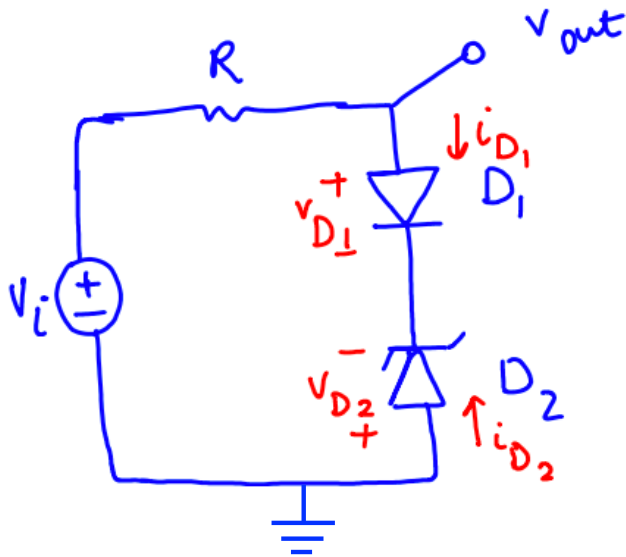
In the following circuit, find V_R for $V_S = +5\text{ V}$ and $V_S = -5\text{ V}$. Assume $V_Z = 3\text{ V}$ and $V_{D0} = 0.7\text{ V}$.



Discussion question 1.

In the below circuit find the range of v_i for which D_1 is ON and D_2 is in the Zener region. Calculate v_{out} .

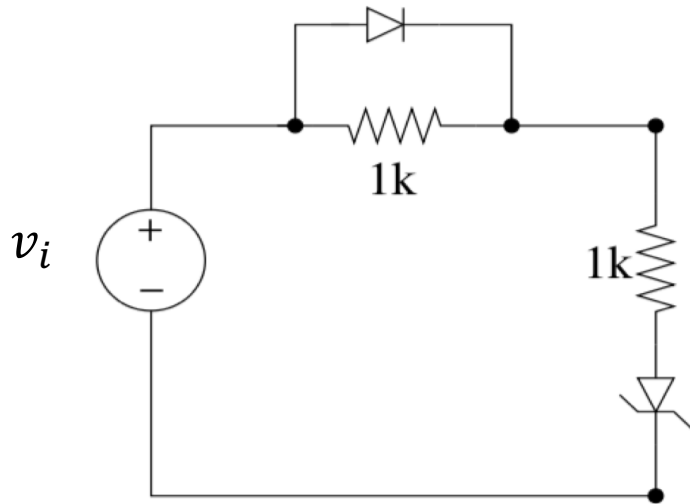
Are there any other possible states at which D_1 and D_2 can operate?
Assume $V_{D0} = 0.7\text{ V}$ and $V_Z = 5\text{ V}$.



Extra activity: find v_{out} for all ranges of v_i (solve the circuit parametrically).

Clicker question 1:

What is the range of v_i for which both diodes are ON? Assume $V_Z = 4\text{ V}$ and $V_{Do} = 0.7\text{ V}$.



A. $v_i \geq 2.1\text{ V}$

B. $v_i \geq 1.4\text{ V}$

C. $v_i \geq 0.7\text{ V}$

D. Both diodes cannot be ON simultaneously.