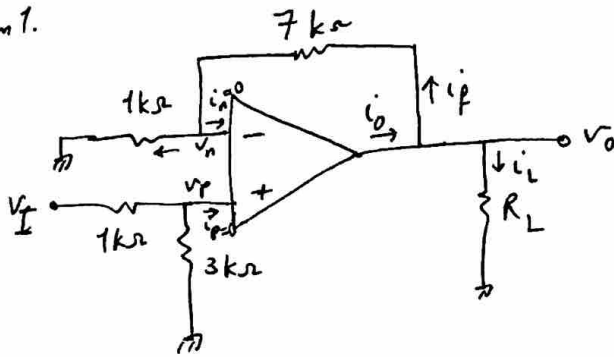


ECE 65 - Winter 2019 Final exam solutions

Problem 1.



a) ideal op-amp : $i_n = i_p = 0$, negative feedback is present $\Rightarrow v_n = v_p$

KCL at the inverting node:

$$\frac{v_n}{1k} + \frac{v_n - v_o}{7k} = 0 \quad \rightarrow \quad \frac{v_o}{7k\Omega} = \left(\frac{1}{1k} + \frac{1}{7k} \right) v_n$$

$$\rightarrow v_o = \left(1 + \frac{7k\Omega}{1k\Omega} \right) v_n$$

$$v_p = \frac{3k\Omega}{3k\Omega + 1k\Omega} \times v_I$$

$$\Rightarrow v_n = v_p$$

$$v_o = \frac{3}{4} (1 + 7) v_I = 6 v_I$$

$$\rightarrow \boxed{v_o = 6 v_I}$$

b) checking the positive peak of $v_I = 2V$:

@ $v_I = 2V$: $v_o = 6 \times 2 = 12V < V_{sat} \Rightarrow$ we are not limited by the saturation voltage for the max positive v_I .

@ $V_I = 2V$: $i_o = i_L + i_f$

$$i_L = \frac{V_o}{R_L} = \frac{12V}{1k\Omega} = 12mA, \quad i_f = \frac{V_o}{8k\Omega} = \frac{12V}{8k\Omega} = 1.5mA$$

$$i_o = 12mA + 1.5mA = 13.5mA < 25mA \rightarrow \text{we are not limited by the max output current for the max positive } V_I.$$

checking the negative peak of $V_I = -3V$:

@

@ $V_I = -3V \rightarrow V_o = 6 \times (-3) = -18V$ and $|-18V| > |V_{sat}|$

\Rightarrow we are limited by the saturation voltage for the max negative V_I .

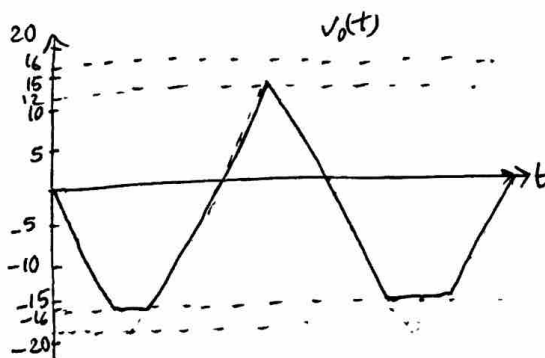
cannot go below $V_{sat} = -16V$
 $V_{in} \Rightarrow V_{out}$

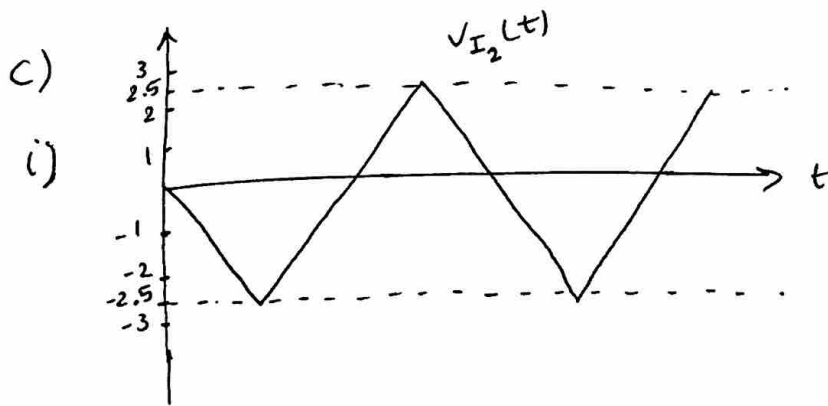
checking for the max output current at $V_{out} = -16V$:

$$i_o = i_L + i_f, \quad i_L = \frac{V_o}{R_L} = \frac{-16V}{1k} = -16mA$$

$$i_f = \frac{-16V}{8k\Omega} = -2mA \rightarrow i_o = -16mA - 2mA = -18mA$$

$$|i_o| < |i_{o,max}|$$





ii)

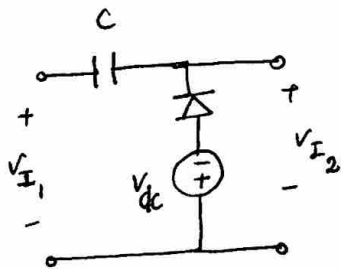
The max positive and negative values of V_I are $2.5V$ and $-2.5V$

$$\text{For } |V_I| = 2.5V \rightarrow |V_o| = 2.5 \times 6 = 15V$$

$$|i_{o\max}| = \frac{|V_{o\max}|}{R_{L\min}} + \frac{|V_{o\max}|}{8k\Omega} = \frac{15V}{R_{L\min}} + \frac{15V}{8k\Omega} = \frac{15V}{R_{L\min}} + 1.875 \text{ (mA)} = 2.9 \text{ mA}$$

$$\frac{15V}{R_{L\min}} = 23.125 \text{ mA} \rightarrow R_{L\min} \approx 649 \Omega$$

iii)



Assume using a diode with $V_{D_0} = 0.7V$

$$V_{I_2} = V_{I_1} + (V_P - V_{DC} - V_{D_0})$$

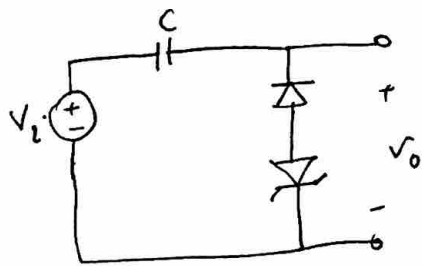
$$V_{I_2} = V_{I_1} + 0.5V$$

$$\rightarrow V_P - V_{DC} - V_{D_0} = 0.5V$$

$$3V - V_{DC} - 0.7V = 0.5V$$

$$\boxed{V_{DC} = 1.8V}$$

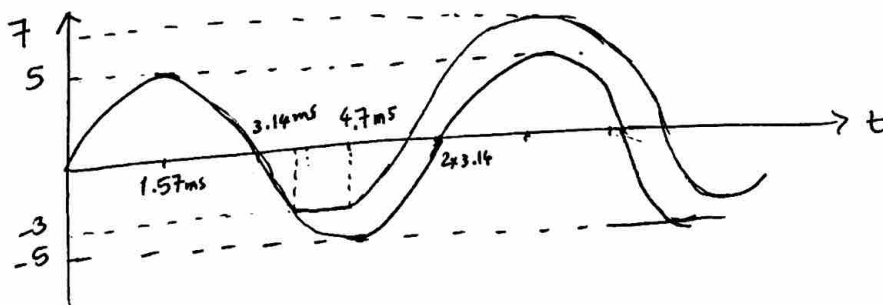
Problem 2.



The diodes will conduct when $|V_i| > |(2.3V + 0.7V)|$ until V_i reaches its max negative peak.

The period of the input signal $T = \frac{2\pi}{\omega} = 2 \times 3.14 \text{ ms}$

at $t = T/2$, V_i will



a) at $t = 2 \text{ ms} \rightarrow V_i > -3V \rightarrow$ diodes are off $\rightarrow V_o = V_i$

$$V_o(t = 2 \text{ ms}) = V_i(t = 2 \text{ ms}) = 5 \times \sin(2) \approx 4.55 \text{ (V)}$$

$$\boxed{V_o(t = 2 \text{ ms}) = 4.55 \text{ V}}$$

b and c) The diodes conduct when $V_i = -3V \rightarrow 5 \sin(\omega t) = -3V$
 $\rightarrow t \approx 3.78 \text{ ms}$

The negative peak happens at $t = \frac{3}{2} \times 3.14 \text{ ms} \approx 4.71 \text{ ms}$

$t = 6 \text{ ms} > 4.71 \text{ ms} \rightarrow$ peak has passed $\rightarrow V_o = V_i + (V_p - \frac{V_c}{2} - V_{D_o})$

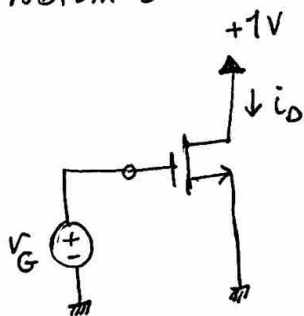
$$V_o = V_i + 2V$$

$$V_o(t = 6 \text{ ms}) = V_i(t = 6 \text{ ms}) + 2V = 0.6V \rightarrow \boxed{V_o(t = 6 \text{ ms}) = 0.6V}$$

at $t = 4 \text{ ms} \rightarrow$ diodes conduct $\rightarrow V_o = -V_{D_o} - V_{D_c} = -3V$

$$\boxed{V_o(t = 4 \text{ ms}) = -3V}$$

Problem 3.



a) b) when $V_G < V_t \rightarrow$ Mos is off $i_D = 0$

$$V_G = V_{GS}, \quad V_D = V_{DS} = 1V$$

when

$V_{GS} - V_t \leq V_{DS} \rightarrow$ Mos is in saturation

$$0.5V \leq V_G \leq 1V + 0.5V \rightarrow 0.5V \leq V_G \leq 1.5V \quad \text{Mos is in saturation}$$

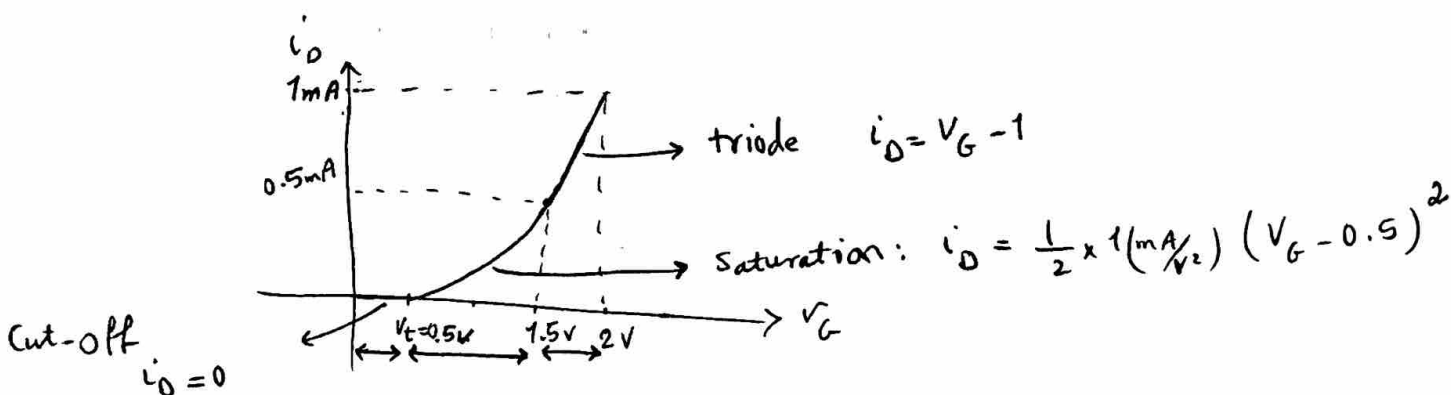
$$i_D = \frac{1}{2} k_n (V_{GS} - V_t)^2 = \frac{1}{2} \times 1 \left(\frac{mA}{V^2} \right) \times (V_G - 0.5)^2$$

when

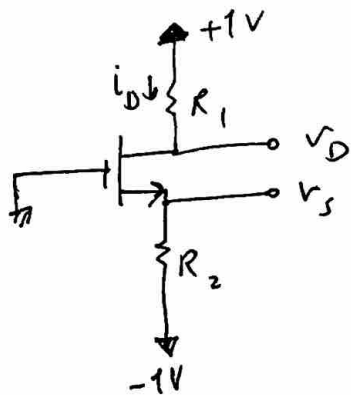
$V_G > 1.5V \rightarrow$ Mos is in triode:

$$i_D = \frac{1}{2} k_n (2V_{ov} V_{DS} - V_{DS}^2) = \frac{1}{2} \times 1 \frac{mA}{V^2} (2 \times (V_G - 0.5) - 1)$$

$$i_D = V_G - 1$$



Problem 4.



$$V_D = 0.3 \text{ V} \quad \text{and} \quad i_D = 0.1 \text{ mA}$$

$$\rightarrow R_1 = \frac{1 \text{ V} - V_D}{i_D} = \frac{1 \text{ V} - 0.3 \text{ V}}{0.1 \text{ mA}} = 7 \text{ k}\Omega$$

$$\rightarrow \boxed{R_1 = 7 \text{ k}\Omega}$$

$$V_{DG} = V_{DS} - V_{GS} = 0.3 \text{ V} \quad \rightarrow V_{DS} = V_{GS} + 0.3 \text{ V}$$

$$V_{OV} = V_{GS} - 0.5 \text{ V}$$

$$\Rightarrow V_{DS} > V_{OV}$$

\Rightarrow MOS is in Saturation

$$i_D = \frac{1}{2} k_n V_{OV}^2 = 0.1 \text{ mA}$$

$$\frac{1}{2} \times 5 \left(\frac{\text{mA}}{\text{V}^2} \right) \times V_{OV}^2 = 0.1 \text{ mA} \quad \rightarrow \boxed{V_{OV} = 0.2 \text{ V}}$$

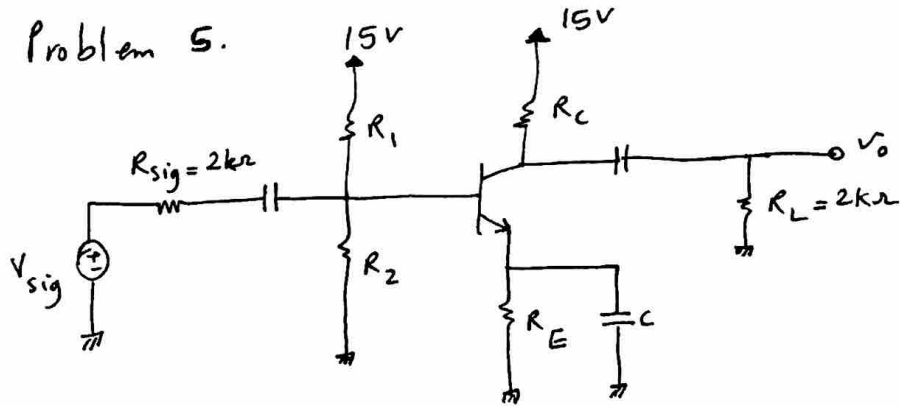
$$V_{GS} = V_{OV} + V_t = 0.7 \text{ V} \quad \rightarrow \boxed{V_S = -0.7 \text{ V}}$$

$$\rightarrow R_2 = \frac{V_S - (-1)}{i_D} = \frac{-0.7 + 1}{0.1} = 3 \text{ k}\Omega$$

$$\boxed{R_2 = 3 \text{ k}\Omega}$$

$$R_D = R_1 = 7 \text{ k}\Omega, \quad R_S = R_2 = 3 \text{ k}\Omega$$

Problem 5.

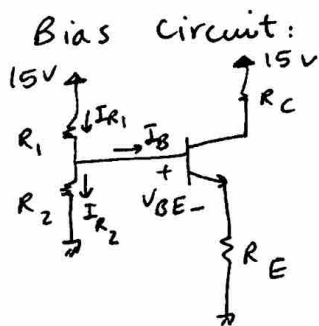


Common-emitter configuration $\rightarrow \left. \frac{V_o}{V_i} \right|_{R_L=\infty} = -g_m R_C$

$$R_o = R_C$$

$$R_i = R_B \parallel r_{\pi}$$

$$\frac{V_o}{V_{sig}} = \left(\frac{R_i}{R_i + R_{sig}} \right) \left(\frac{R_L}{R_L + R_o} \right) A_{V_o}$$



$$V_B = \frac{1}{3} \times 15V = 5V$$

$$V_{BE} = 0.7V \rightarrow V_E = V_B - 0.7V = 4.3V$$

$$I_E = 2mA \rightarrow R_E = \frac{V_E}{I_E} \rightarrow \boxed{R_E = 2.15k\Omega}$$

$$I_{R_2} = \frac{1}{10} \times I_E = 0.2mA$$

$$R_2 = \frac{V_B}{I_{R_2}} = \frac{5V}{0.2mA} = 25k\Omega \Rightarrow \boxed{R_2 = 25k\Omega}$$

$$I_B = \frac{I_E}{1+\beta} = 19.8\mu A, \text{ KCL: } I_{R_1} = I_B + I_{R_2} = 0.2mA + 19.8\mu A = 0.2198mA$$

$$R_1 = \frac{15V - 5V}{0.2198mA} \Rightarrow \boxed{R_1 = 45.49k\Omega}$$

$$R_i = R_B \parallel r_{\pi} \quad , \quad R_B = R_1 \parallel R_2 = 45.4k \parallel 25k = 16.13k\Omega$$

$$r_{\pi} = \frac{V_T}{I_B} = \frac{25mV}{I_E/(1+\beta)} = \frac{25mV \times 101}{2mA} = 1.26k\Omega$$

$$R_i = 16.13k \parallel 1.26k\Omega \approx 1.17k\Omega \quad , \quad g_m = \frac{\beta}{r_{\pi}} = \frac{100}{1.26k\Omega} = 79.36mA/V$$

$$- \frac{V_o}{V_{sig}} = \frac{R_i}{R_i + R_{sig}} \times \frac{R_L}{R_L + R_o} \times A_{V_o} = \left(\frac{1.17k}{1.17k + 2k} \right) \times \left(\frac{2k}{2k + R_c} \right) \times 79.36mA/V \times R_c$$

$$\rightarrow \frac{58.58 R_c}{2k + R_c} = 40 \quad \rightarrow 58.58 R_c = 80k + 40 R_c$$

$$\rightarrow R_c \approx 4.3k\Omega$$

Signal parameters:

$$r_{\pi} = 1.26k\Omega \quad , \quad g_m = 79.36mA/V \quad , \quad r_o = \infty$$

Signal circuit:

