REV.	Description		Date
S00	102H-225012	SPEC ISSUE (Modify from ADP-100WB BA)	05/13/2022
00	102H-235009	Transfer to MP	05/12/2023
		257	
		ale but	

A NEL		子工業股份? ELECTRONIC	DESCRIPTION : 測試注意事項(Test Specification	on )	
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Date	Drawn	Design (EE)	Design (ME)	DOCUMENT NAME. :	REV.
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### ADP-100WB BE

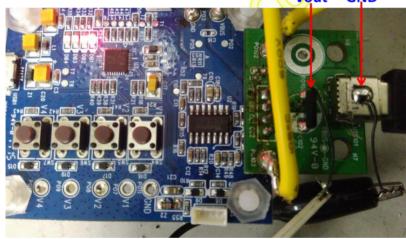
- 1. 測試注意事項內容,未經工程師許可,不可任意變更。
  - Test Notice contents shall not be changed or revised without engineer permission.
- 2. 此測試規格用於主線測試,所有項目均需被測試,因設備或線速限制無法全數測試時,需測試worst case, QC 需按抽樣標準做抽樣測試.

The production line shall perform all or worst test items/conditions, and QC shall follow the sampling plan to perform the sampling test.

#### 3. 測試注意事項:

- a. 功能測試時,電子負載需設定 Von 點為 3V.
- **b.** 所有 ATS 測試時,須偵測 CONNECTOR 端之電壓.
- c. ATE 測試治具板:Weltrend P/N:WPD002,F/W Rev:2.0
- d. 在測試 regulation&ripple 時,治具上各組輸出須接有 0.1uF 電容和 10uF 鉭電容
- e. ESD 測試時,使用的治具版本為 V1.02 版,同時上電后要把治具小板移除后再測 ESD。測條件為接觸±8kV,空氣±15kV.
- f. 測試 AVE EFF 和與輸出電壓 Regulation 有關 item 時,治具的電壓採樣點離 cable 末端距離要盡量短,可參考如下圖所示的電壓採樣點來測試.
- g. CC pin pulse width 測試請參考附件一.

Vout GND



	台達電	台達電子工業股份有限公司		DESCRIPTION:	
A NELI	DELTA	ELECTRONIC	CS, INC.	測試注意事項(Test Specification)	
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#### 4. 燒錄 IC:

- a. 預燒錄 IC 之 Location Name= IC131。
- **b.** 燒錄注意事項: IC 內建
- c. 所使用之特殊燒錄器形式及特殊 Socket 之規格:TBD
- d. 燒錄程式名稱& Check SUM: TBD
- 5. 小板(小板名稱)動態測試:N/A
- 6. 手調測試站:
  - a. Full Load Test:

檢查滿載輸出是否符合規格

b. Min. Load Test:

檢查輕載輸出是否符合規格

c. OTP Test:

Load Condition :5V/3A,9V/3A,15V/3A,20V/5A

敘述測試方法,例如: 短路 NTC131,檢查輸出電壓是否 shutdown and Latch off.

d. OVP Test:

Load Condition: 5V/3A,9V/3A,15V/3A,20V/5A

敘述測試方法,例如: 短路光耦 IC51 的 pin1&pin2, 檢查輸出電壓是否滿足以下要求:

<7.5V(5V/3A)

<11.7V(9V/3A)

<19.5V(15V/3A)

<26V (20V/5 A)

OVP 動作模式為 Latch off.

e. LED 狀態:

N/A

f. AC On/Off:

Power supply 連續開關機 On= 1 秒, Off= 1 秒, 5 次後, 必須無損壞情形。

For bench AC ON/OFF test:判定规格為 no damage

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A NEL		ELECTRONIC		測試注意事項(Test Specification )	
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# 7. Function test (ATS) procedure : (20VDC)

## **Input Specification Table:**

INPUT VOLTAGE	MINIMUM	MAXIMUM	NOMINAL(RATED)
LOW RANGE	90 VAC	137 VAC	100~127VAC
HIGH RANGE	180V AC	265V AC	200~240VAC

OUTPUT	MINIMUM	MAXIMUM	NOMINAL(RATED)
Voltage (Vdc)	19	21	20
Loading (Amp)	0	5	5

Test Item	Vin	Load	Spec	Remark
Line Regulation	90/265V 47/63Hz	0A	19~21V	
Line Regulation	90/265V 47/63Hz	5A	19~21V	
Load/Combine Regulation	90V/265V 47/63Hz	0.A,2.5A,5A	19~21V	
Ripple & Noise	90V/47Hz 265V/63Hz	5A	<350mVp-p	0.1uF//10uF 鉭電容 25℃
Dynamic load	100V/47Hz 240V/63Hz	0.00A ~ 5A 0.20A ~ 1.5A 1.00A ~ 3A 1.00A ~ 5A	18.0~22.0V	S/R: 2.5A/uS, Load On/off= 4ms/6ms Load On/off=50mS/50mS
Step load	90V/47Hz 265V/63Hz	0.0A ~ 1.0A 1.0A ~ 2.0A 2.0A ~ 3.0A 3.0A ~ 4.0A 4.0A ~ 5.0A	18.6~21.4V	S/R:0.01A/uS ON/OFF time=50mS/50mS
Short Circuit Protection	90/265Vac	Min. and Max.Load Turn on then short	(Vo>=4.75V)	
Short Circuit Protection	265V/63Hz	Min. and Max.Load Turn on then short	Auto-recovery (Vo>=4.75V)	
Over Current Protection	90/265Vac	5A	OCP<=7.5A Auto-recovery (Vo>=4.75V)	
Average Efficiency	115V/60Hz 230V/50Hz	25%, 50%, 75% & 100% Load	>87%	Cold For 產線測試
Full load Efficiency	90V/60Hz	100%	>85%	Cold For 產線測試
Peak Load-1	90V/47Hz 265V/63Hz	5A~7.5A	18.0~22.0V	Band width:20MHz S/R:2.5A/uS, On/Off=1000mS/5mS 如 load 有最小占空比限制,可以设置 45mS/5mS
Peak Load-2	90V/47Hz	6A~5A	18.0~22.0V	Band width:20MHz S/R:2.5A/uS, On/Off=2S/8S
Peak Load-3	90V/47Hz	5A~6.5A	18.0~22.0V	Band width:20MHz S/R:2.5A/uS, on=4S,not be repeated
Output discharge time	90V/47Hz 265V/63Hz	3A/0A	0~275ms	Discharge time 20V->5V <275ms for no load and 3A
Hold up time	115Vac/60Hz	5A	>10ms	

A NEL		子工業股份? ELECTRONIC	月派公司	DESCRIPTION : 測試注意事項(Test Specification	on )
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# 8. Function test (ATS) procedure : (15VDC)

## **Input Specification Table:**

INPUT VOLTAGE	MINIMUM	MAXIMUM	NOMINAL(RATED)
LOW RANGE	90 VAC	137 VAC	100~127VAC
HIGH RANGE	180V AC	265V AC	200~240VAC

OUTPUT	MINIMUM	MAXIMUM	NOMINAL(RATED)
Voltage (Vdc)	14.25	15.75	15
Loading (Amp)	0	3	3

Test Item	Vin	Load	Spec	Remark
Line Regulation	90/265V 47/63Hz	0A	14.25~15.75V	
Line Regulation	90/265V 47/63Hz	3A	14.25~15.75V	do
Load/Combine Regulation	90V/265V 47/63Hz	0.A,1.5A,3A	14.25~15.75V	The state of the s
Ripple & Noise	90V/47Hz 265V/63Hz	3A	<350mVp-p	0.1uF//10uF 鉭電容 25℃
Dynamic load	100V/47Hz 240V/63Hz	0.0A ~ 2.7A 0.3A ~ 3.0A	13.5~16.5V	S/R: 2.5A/uS, Load On/Off= 4ms/6ms Load On/Off=50mS/50mS
Step load	90V/47Hz 265V/63Hz	0.0A ~ 1.0A 1.0A ~ 2.0A 2.0A ~ 3.0A	14.25~15.75V	S/R: 0.01A/uS, Freq: 1kHz
Short Circuit Protection	90V/47Hz	Min. and Max.Load Turn on then short	Auto-recovery (Vo>=4.75V)	
Short Circuit Protection	265V/63Hz	Min. and Max.Load Turn on then short	Auto-recovery (Vo>=4.75V)	
Over Current Protection	90/265Vac	3A	OCP<=4.5A Auto-recovery (Vo>=4.75V)	
Average Efficiency	115V/60Hz 230V/50Hz	25%, 50%, 75% & 100% Load	>86.5%	Cold For 產線測試
Full load Efficiency	90V/60Hz	100%	>84.5%	Cold For 產線測試
Output discharge time	90V/47Hz 265V/63Hz	3A/0A	<275ms	Discharge time 15V->5V <275ms for no load and full load
Peak load	90V/Hz~ 265V/63Hz	3A~4.5A	13.5V~16.5V	S/R:2.5A/uS On/Off time=999mS/1mS 如 load 有最小占空比限制, 可以設置 19mS/1mS

ロ廷电丁工表版切角版公司		DESCRIPTION : 測試注意事項(Test Specification	on )		
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Date	Drawn	Design (EE)	Design (ME)	DOCUMENT NAME. :	REV.
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# 9. Function test (ATS) procedure : (9VDC)

## **Input Specification Table:**

INPUT VOLTAGE	MINIMUM	MAXIMUM	NOMINAL(RATED)
LOW RANGE	90 VAC	137 VAC	100~127VAC
HIGH RANGE	180V AC	265V AC	200~240VAC

OUTPUT	MINIMUM	MAXIMUM	NOMINAL(RATED)
Voltage (Vdc)	8.55	9.45	9
Loading (Amp)	0	3	3

Test Item	Vin	Load	Spec	Remark
Line Regulation	90/265V 47/63Hz	0A	8.55~9.45V	
Line Regulation	90/265V 47/63Hz	3A	8.55~9.45V	an All
Load/Combine Regulation	90V/265V 47/63Hz	0.A,1.5 A, 3.0A	8.55~9.45V	27/10
Ripple & Noise	90V/47Hz 265V/63Hz	ЗА	<350mVp-p	0.1uF//10uF 鉭電容 25℃
Dynamic load	100V/47Hz 240V/63Hz	Load1=0A~2.7A Load2=0.3A~3A	8.1~9.9V	S/R: 2.5A/uS, On/Off time=4mS/6mS On/Off time=50mS/50mS 示波器測輸出電壓通道設定為 0.2V/div。
Step load	90V/47Hz 265V/63Hz	0.0A ~ 1.0A 1.0A ~ 2.0A 2.0A ~ 3.0A	8.37~9.63V	S/R: 0.01A/uS, Freq: 1kHz 示波器測輸出電壓通道設定為 0.2V/div。
Short Circuit Protection	90V/47Hz	Min. and Max.Load Turn on then short	Auto-recovery (Vo>=4.75V)	
Short Circuit Protection	265V/63Hz	Min. and Max.Load Turn on then short	Auto-recovery (Vo>=4.75V)	
Over Current Protection	90/265Vac	3A	OCP<=4.5A Auto-recovery (Vo>=4.75V)	
Average Efficiency	115V/60Hz 230V/50Hz	25%, 50%, 75% & 100% Load	>84.5%	Cold For 產線測試
Full load Efficiency	90V/60Hz	100%	>82.5%	Cold For 產線測試
Output discharge time	90V/47Hz 265V/63Hz	3A/0A	<275ms	Discharge time 9V->5V <275ms for no load and full load

A NEL	□ 互连电丁上未放切为依公可		DESCRIPTION : 測試注意事項(Test Specification)		
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# 10. Function test (ATS) procedure : (5VDC)

**Input Specification Table:** 

INPUT VOLTAGE	MINIMUM	MAXIMUM	NOMINAL(RATED)
LOW RANGE	90 VAC	137 VAC	100~127VAC
HIGH RANGE	180V AC	265V AC	200~240VAC

OUTPUT	MINIMUM	MAXIMUM	NOMINAL(RATED)
Voltage (Vdc)	4.75	5.25	5
Loading (Amp)	0	3	3

Test Item	Vin	Load	Spec	Remark
Inrush Current	265V/50Hz;	3A	No component damage	Cold start
Line Regulation	90/265V 47/63Hz	0A	4.75~5.25V	Solo
Line Regulation	90/265V 47/63Hz	3A	4.75~5.25V	Marie
Load/Combine Regulation	90V/265V 47/63Hz	0A,1.5 A, 3.0A	4.75~5.25V	
Ripple & Noise	90V/47Hz 265V/63Hz	3A	<350mVp-p	0.1uF//10uF 鉭電容 25℃
Dynamic load	100V/47Hz 240V/63Hz	Load1=0A~2.7A Load2=0.3A~3A	4.10~5.50V	S/R: 2.5A/uS, On/Off time=4mS/6mS On/Off time=50mS/50mS
Step load	90V/47Hz 265V/63Hz	0.0A ~ 0.75A 0.75A ~ 1.5A 1.50A ~ 2.25A 2.25A~3.0A	4.40~5.45V	S/R:2.5A/uS 10Hz
Short Circuit Protection	90V/47Hz	Min. and Max.Load Turn on then short	Auto-recovery (Vo>=4.75V)	
Short Circuit Protection	265V/63Hz	Min. and Max.Load Turn on then short	Auto-recovery (Vo>=4.75V)	
Over Current Protection	90/265Vac	3A	OCP<=4.5A Auto-recovery (Vo>=4.75V)	
Average Efficiency	115V/60Hz 230V/50Hz	25%, 50%, 75% & 100% Load	>78.7%	Cold For 產線測試
Pin at No Load	90V/47Hz 230V/50Hz	No Load	<0.15W	For 手測站 3332 測試· 產品與治具不連接

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Test Item	Vin	Load	Spec	Remark
Full load Efficiency	90V/60Hz	100%	>76.5%	Cold For 產線測試
Pin at standby Load	115V/60Hz 230V/50Hz	0.25W 0.5W	<0.5W <1.0W	穩定時間 15s
Capacitive Load	90V/47Hz	3A	4.75~5.25V	Parallel 100uF Cap at output
Capacitive Load	265V/63Hz	3A	4.75~5.25V	Parallel 100uF Cap at output
Start up delay time	90V/47Hz 265V/63Hz	ЗА	<3S	
Output rise time	90V/50Hz	0-500mA	<10mS	
Peak load	90V/Hz~ 265V/63Hz	3A~4.5A	4.10V~5.50V	S/R:2.5A/uS On/Off time=999mS/1mS 如 load 有最小占空比限制, 可以設置為 19mS/1mS

Note 1: After completes required DVT test matrix, identifies and selects the worst case condition for the Production MTR. It is not required that all test conditions be tested. Example: If worst case condition for Output Ripple is minimum input voltage and maximum dc load, then that is the test condition to be used.

#### 11. Safety

#### 11.1 Hi-POT test:

PRIMARY to SECONDARY use DC 4242 V test, (上昇時間: DC=1S).
Test time=1sec.Arcing sense = (EVT1:10mA · EVT2/DVT:10mA · PVT/MP:10mA).
Hi-Limit current=50µA. Lo-Limit current = 0.01uA.

#### 11.2 Insulation Resistance (IR) test:

PRIMARY to SECONDARY use 500Vdc test; Insulation resistance limit:>100M ohm •

#### 12. Burn-in 測試作業規範

P/R 階段 B/I 32 小時,B/I 電壓和時間分別如下:

5V 8 小時

9V 8 小時

15V 8 小時

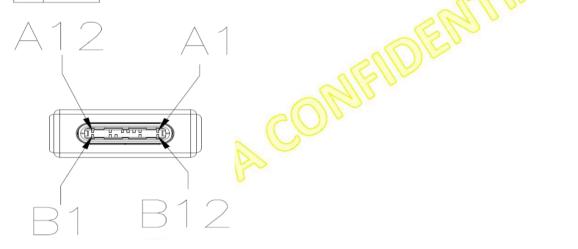
20V 8 小時

MP 階段根據品質穩定狀況調整 B/I 時間

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- 13. I2C 測試作業規範 NA
- 14. FRU Data Barcode Read/Write 測試作業規範NA
- 15. DC Cable Connector pin define and related test request.
  - A. Power cord 之 24pin connector,每 PIN 皆需做短路导通测试。
  - B. 各 PIN 功能如下,其他皆为空 PIN:

LIDD			
USB PD	USB PIN		
CONTROLLER	A4,A9,B4,B9	VBUS	RED(+)
GPIO CC	→ A1,A12,B1,B12	GND	BLACK(-)
	A5	CC	ID BLUE
CNID	A6,A7,B6,B7	D+/D-	
GND			

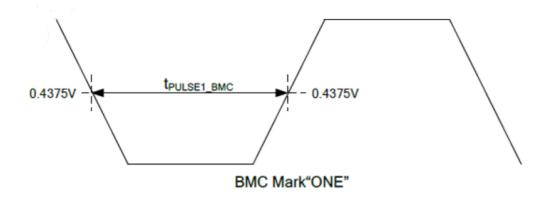


#### 附件一

此 t<sub>PULSE1\_BMC</sub> 於電壓 0.4375V level 時需符合 1.0μS~2.0μS 範圍內。

(1)pulse width of transmitted BMC "ONE" signal

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#### 16. Noise 測試方法:

- A. 测试 5V noise 时用 220Vac 5V/3A 热机 20min
- B. Load 調節以每次 0.1A 為最小單位進行調節 (Lenovo 测试规范)。
- C. The adapter shall not make any acoustical noise. The noise level is less than 25 dB at 50 mm distance at any condition included dynamic and step load in the development stage, and less than 25 dB at 100 mm distance in MP stage. And less than 10dB at 8k/10k/12.5kHz band at 300 mm distance. Measure with well warm up, and with setting adapter both position of "its label down and up".
- D. Need test and consider Lenovo PA noise standard.

### 17. Leakage current 測試方法:

- A. 測試 Leakage current 时,輸入線用 15CM 長的短線;
- B. 另外防止設備誤差,Leakage current 不超過 30uA 即可。

#### 18. 客户规格 CASE 温升测试

测试条件:All case's rise temperature ≤50°C when 90Vac or 265Vac input.

All case's rise temperature ≤45°C when 100Vac or 240Vac input.

Ambient temperature 40°C

测试方法:测试样品放置于厚度为 3mm 木板上,木板至于铁板上不架空。

#### 19. ESD 测试标准

AIR 15kV/CONTACT 8kV, Criteria B.

口连电丁上未放仍为胶公口			DESCRIPTION : 測試注意事項(Test Specification )		
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# 20. Function test (ATS) procedure : (PPS: 5V-21V)

ORT 手动抽测,产线不测。

## **Input Specification Table:**

INPUT VOLTAGE	MINIMUM	MAXIMUM	NOMINAL(RATED)
LOW RANGE	90 VAC	137 VAC	100~127VAC
HIGH RANGE	180V AC	265V AC	200~240VAC

•			
OUTPUT	MINIMUM	MAXIMUM	
Voltage (Vdc)	5V	21V	
Loading (Amp)	1.1A	3A	

Test Item	Vin	Load	Vout	Remark
Voltage	90/265V 47/63Hz	0A	5V	Load : CC mode
Voltage	90/265V 47/63Hz	3A	5V	Load : CC mode
Voltage	90/265V 47/63Hz	0A	8V V8	Load : CC mode
Voltage	90/265V 47/63Hz	3A	8V	Load : CC mode
Voltage	90/265V 47/63Hz	OA	12V	Load : CC mode
Voltage	90/265V 47/63Hz	3A	12V	Load : CC mode
Voltage	90/265V 47/63Hz	0A	16V	Load : CC mode
Voltage	90/265V 47/63Hz	3A	16V	Load : CC mode
Voltage	90/265V 47/ <mark>63</mark> Hz	0A	21V	Load : CC mode
Voltage	90/2 <mark>65V</mark> 47/63Hz	ЗА	21V	Load : CC mode
Current	90/265V 47/63Hz	5.5V	3.0A	Load : CV mode
Current	90/265V 47/63Hz	20.5V	3.0A	Load : CV mode
Current	90/265V 47/63Hz	5.5V	2.0A	Load : CV mode
Current	90/265V 47/63Hz	20.5V	2.0A	Load : CV mode
Current	90/265V 47/63Hz	5.5V	1.5A	Load : CV mode

口连电丁上未放仍为胶公可				DESCRIPTION : 測試注意事項(Test Specification )	
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Test Item	Vin	Load	Vout	Remark
Current	90/265V 47/63Hz	20.5V	1.5A	Load : CV mode
Current	90/265V 47/63Hz	5.5V	1.1A	Load : CV mode
Current	90/265V 47/63Hz	20.5V	1.1A	Load : CV mode

Note 1: After completes required DVT test matrix, identifies and selects the worst case condition for the Production MTR. It is not required that all test conditions be tested. Example: If worst case condition for Output Ripple is minimum input voltage and maximum dc load, then that is the test condition to be used.



	台達電	子工業股份在	DESCRIPTION:		
<b>NEL</b>	DELTA	TA ELECTRONICS, INC.		測試注意事項(Test Specification)	
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