

XP10

	←	
Eth_MII_TX_CLK	1	PC3
Eth_MII_TX_EN	2	PB11
Eth_MII_TXD0	3	PB12
Eth_MII_TXD1	4	PB13
Eth_MII_TXD2	5	PC2
Eth_MII_TXD3	6	PB8
Eth_MII_RXD0	7	PC4
Eth_MII_RXD1	8	PC5
Eth_MII_RXD2	9	PB0
Eth_MII_RXD3	10	PB1
Eth_MII_RX_ER	11	PB10
Eth_MII_RX_DV	12	RX_DV2
Eth_MII_CLK	13	PA1
Eth_MII_RX_COL	14	PA3
Eth_MII_RX_CRS	15	PA0
Eth_MDC	16	PC1
Eth_MII_MDIO	17	PA2
RESET	18	NRST
3,3V	19	→ +3,3V
GND	20	

BH-20

DD10

Eth_MII_TX_CLK	PC3	7	TXC
Eth_MII_TX_EN	PB11	2	TXEN
Eth_MII_TXD0	PB12	3	TXD0
Eth_MII_TXD1	PB13	4	TXD1
Eth_MII_TXD2	PC2	5	TXD2
Eth_MII_TXD3	PB8	6	TXD3
Eth_MII_RXD0	PC4	21	RXD0
Eth_MII_RXD1	PC5	20	RXD1
Eth_MII_RXD2	PB0	19	RXD2
Eth_MII_RXD3	PB1	18	RXD3
Eth_MII_RX_ER	PB10	14	RXER
Eth_MII_RX_DV	RX_DV2	2	RXDV
Eth_MII_RX_RMII_REF_CLK	PA116	1	RXC
Eth_MII_COL	PA3	1	COL
Eth_MII_CRS	PA0	23	CRS
Eth_MDC	PC1	25	MDC
Eth_MII_MDIO	PA2	26	MDIO
RESET	NRST	27	NC

37	ANE
38	DUPLEX
39	SPEED
40	RPTR
41	LDPS
43	ISOLATE
44	MIU/SN

RTL8201BL

Ethernet



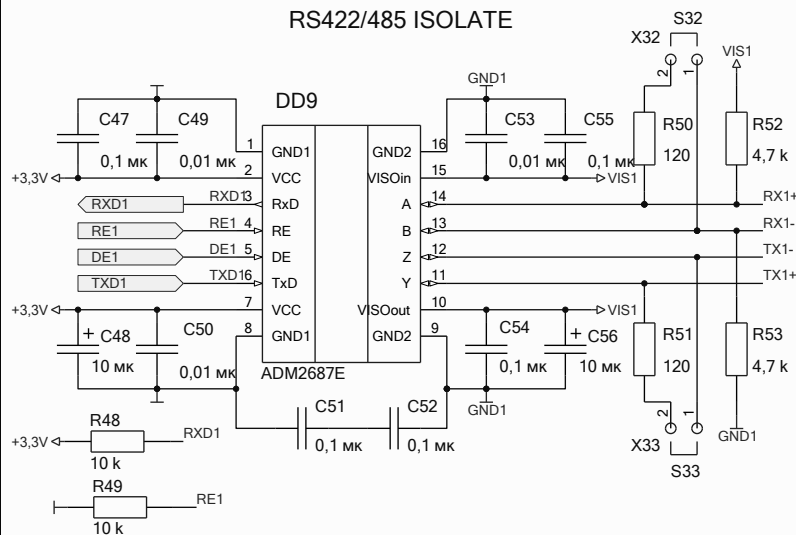
XS6

	←	
1	TD+ (TX+)	
2	TD- (TX-)	
3	CT	
4		
5		
6	CT	
7	RD+ (RX+)	
8	RD- (RX-)	
9	HL1+	
10	HL1-	
11	HL2-	
12	HL2+	
13	SHIELD	
14	SHIELD	

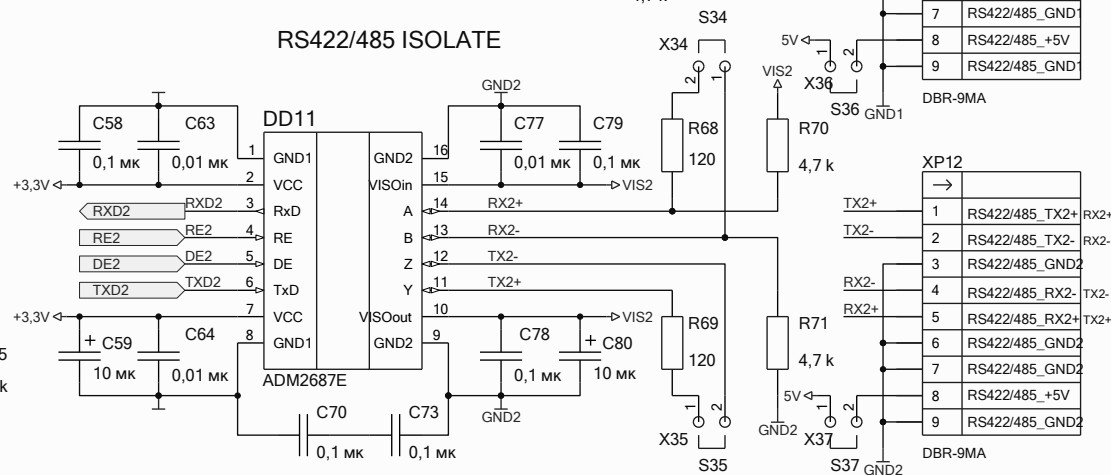
L11T516-43LF

LU1T516-43LF

RS422/485 ISOLATE



RS422/485 ISOLATE



Изм.	Лист	№ докум.	Подп.	Дата
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