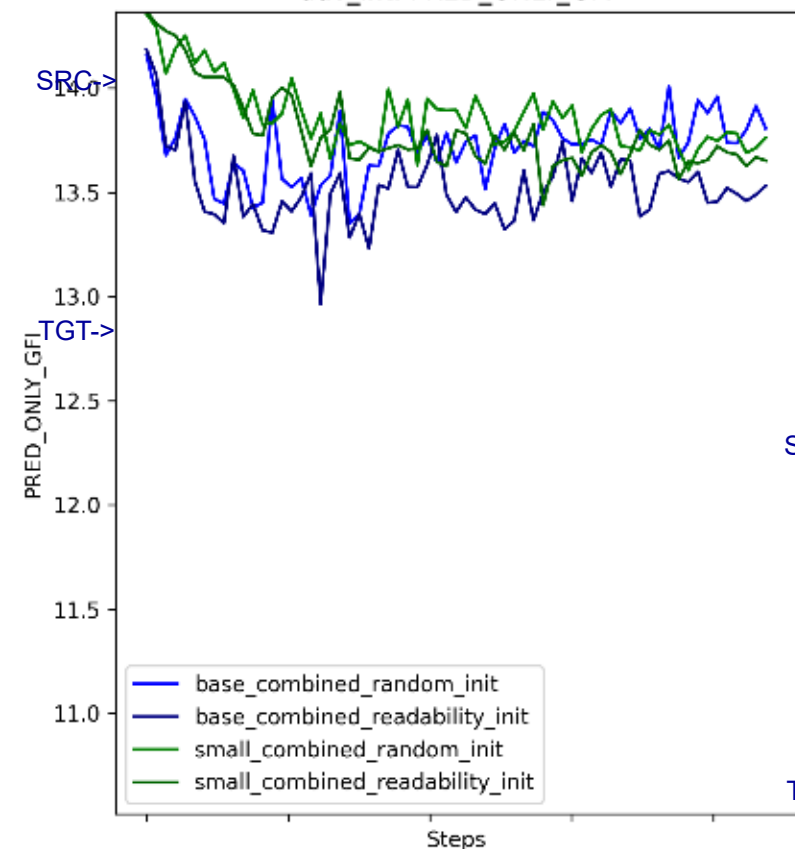
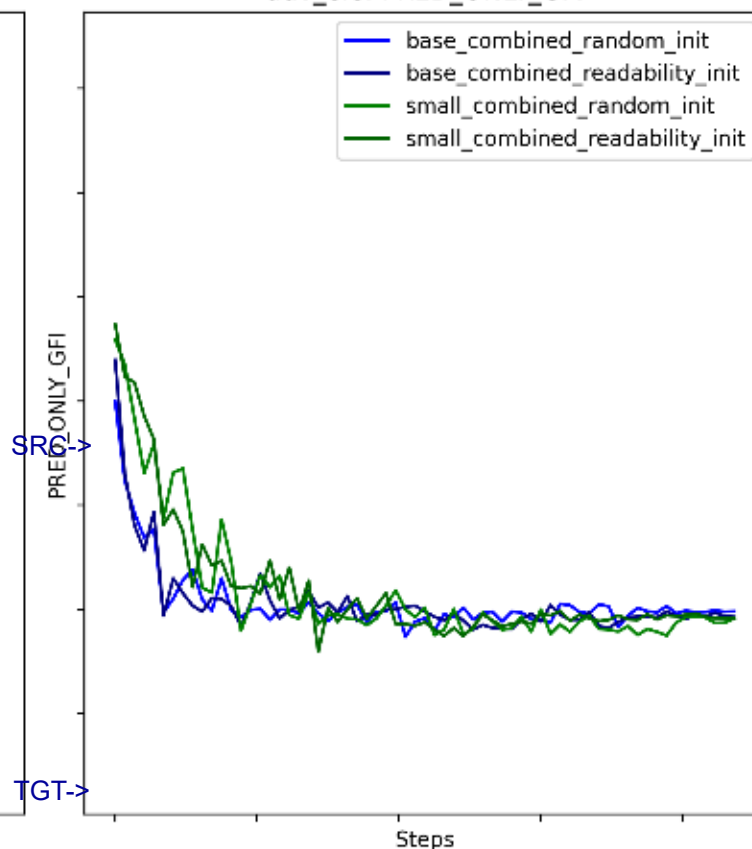


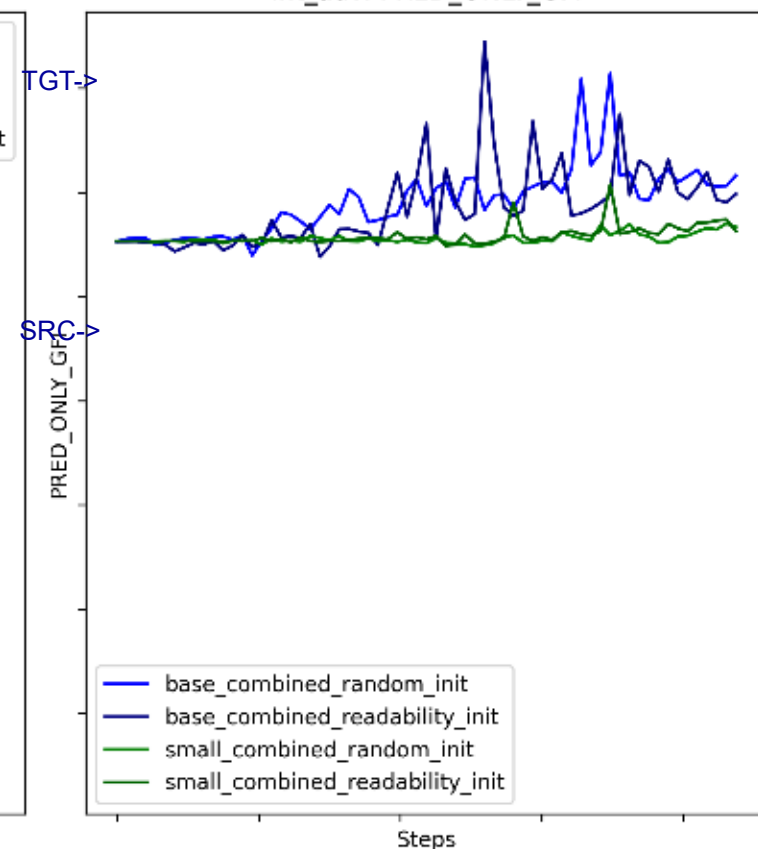
adv\_int: PRED\_ONLY\_GFI



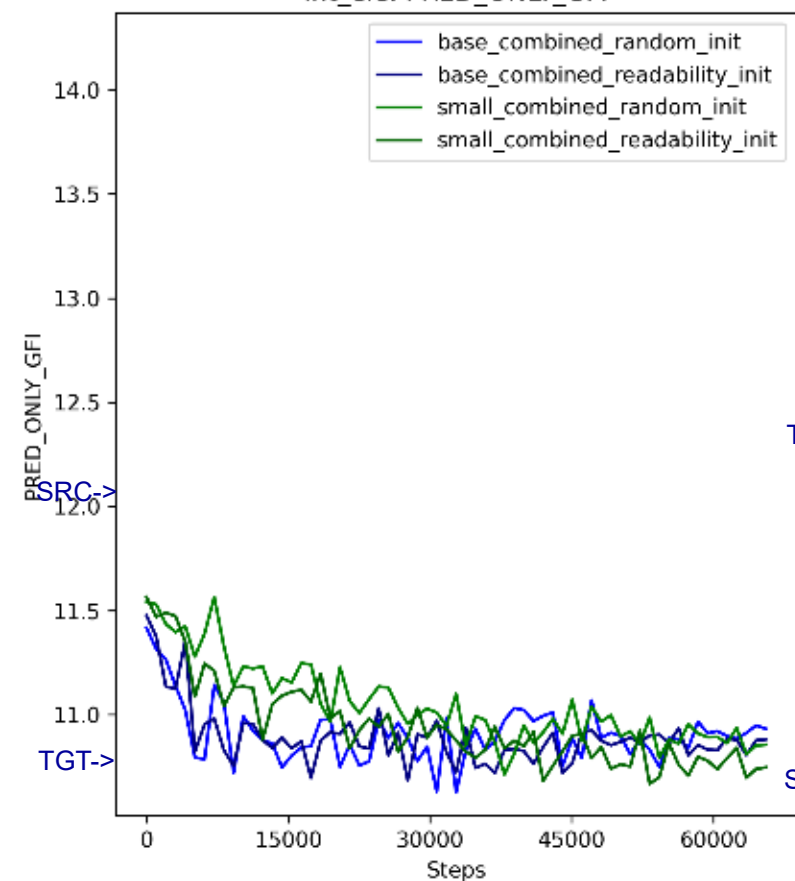
adv\_ele: PRED\_ONLY\_GFI



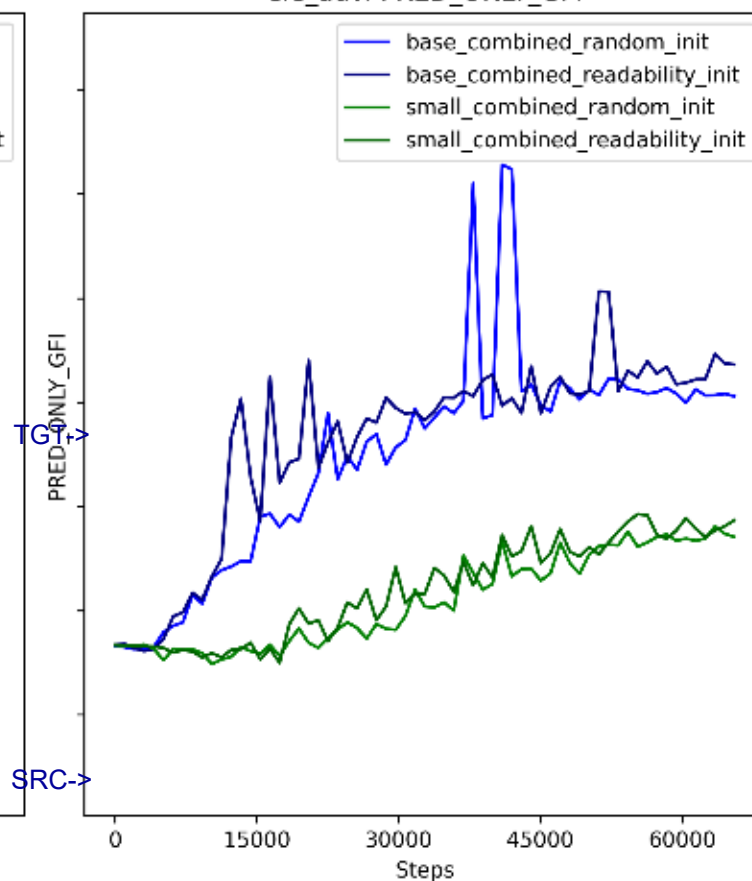
int\_adv: PRED\_ONLY\_GFI



int\_ele: PRED\_ONLY\_GFI



ele\_adv: PRED\_ONLY\_GFI



ele\_int: PRED\_ONLY\_GFI

