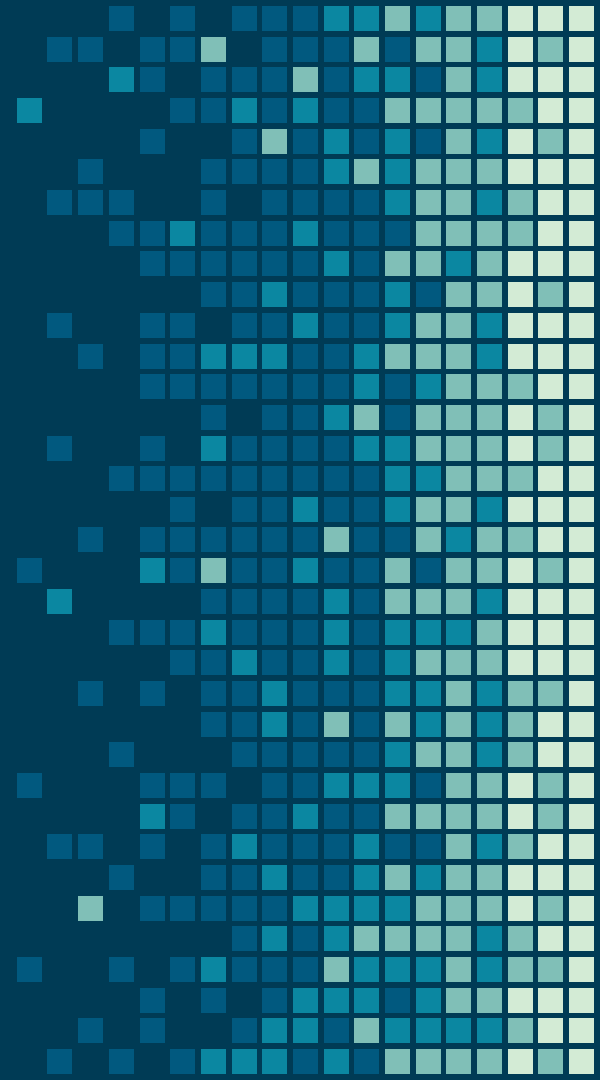


*Breaking your
computer by
taking it apart and
poking at it with
wires*



1. wat

Whatcha talkin' 'bout, Jeffrey?



Trust in your own technology

- How can you trust that the software you use follows your interests?
- Obviously just use open source software and read all the code and then you know it's doing what you want, and that's all we need to do



Trust in your own technology



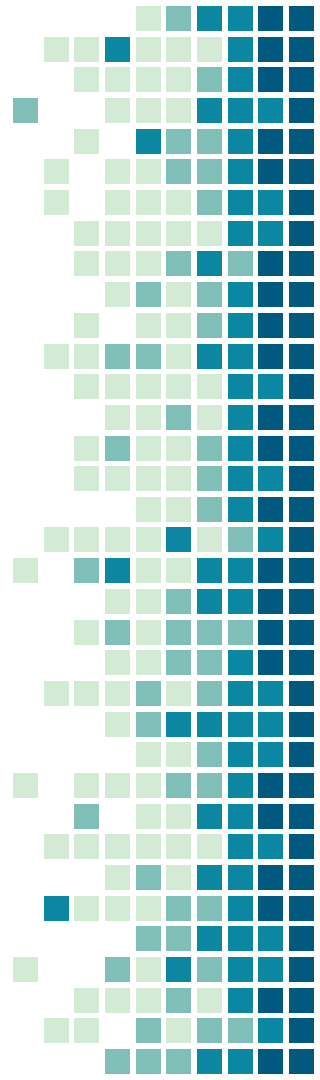
If you're using x86



If you're using x86

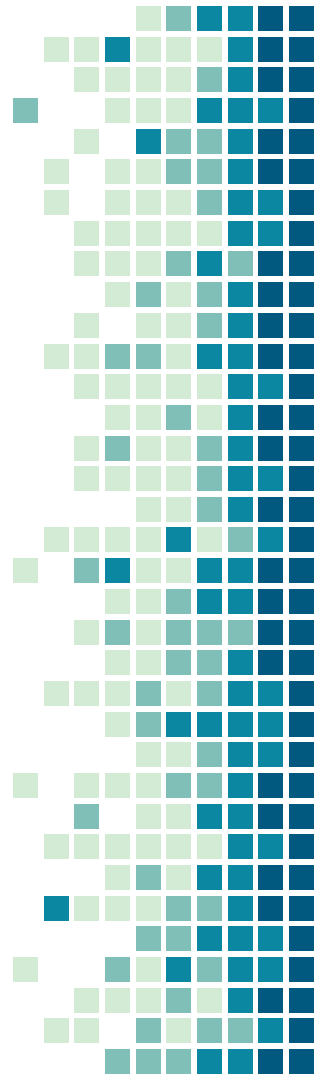
The Intel Management Engine

- Basically a separate computing system inside your processor that has full access to all your stuff
- “Ring -1”
- Even if you verify trustworthiness of your software, OS, firmware (with, say, coreboot), your CPU and UEFI are closed off from you



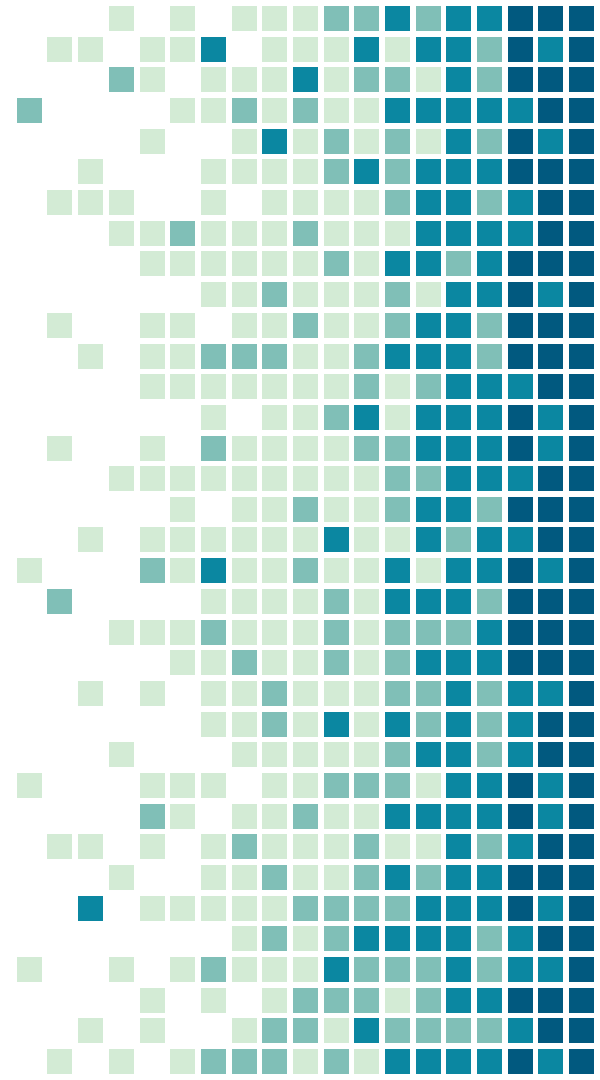
The Intel Management Engine

- Allows for remote administration, remote destruction, and a few other legitimate features
- But... what happens when bad people take it over?
 - And does Intel count as bad people?
- We also can't read its code
 - Compressed in the JEFF format – we don't know how it is compressed, so we can't just read the code as instructions
 - Some of the code is on the CPU die itself, so, have fun?



2. I don't want that

Get rid of it



You can break things with wires

This is the boy

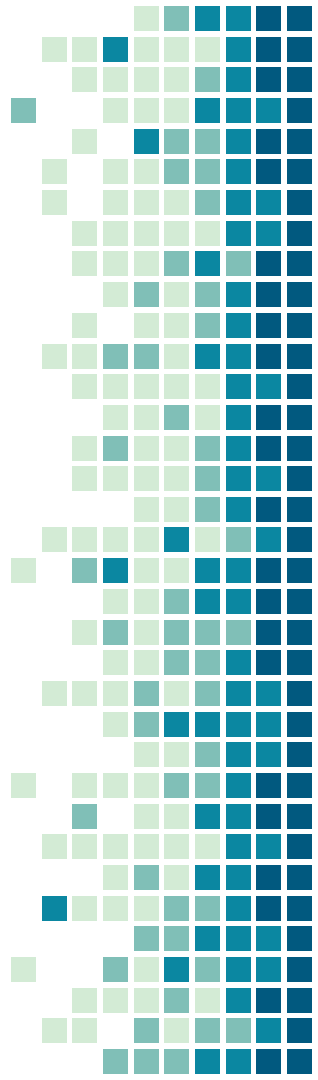
He contains
the firmware



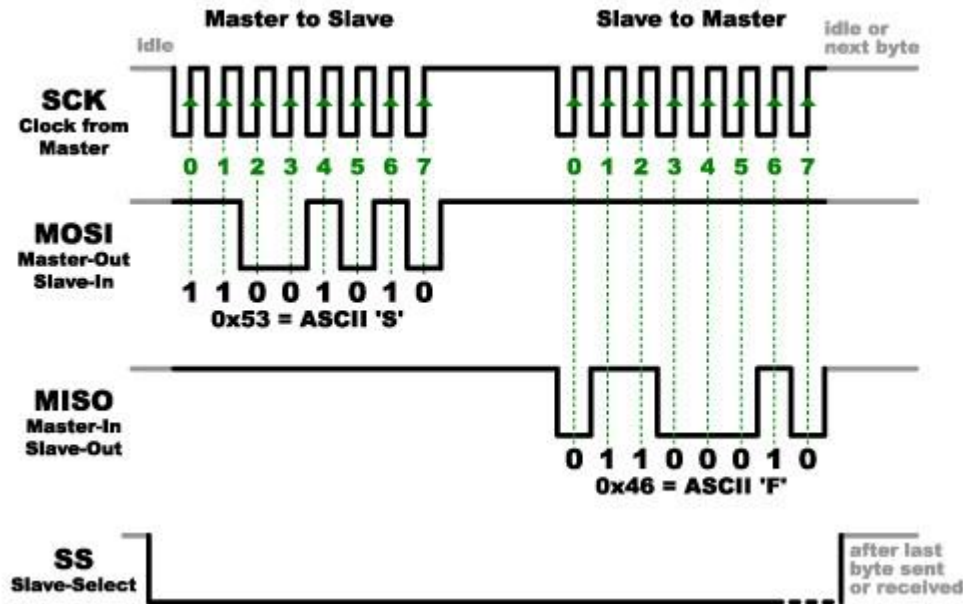
Do not anger him

SPI: Serial Peripheral Interface

- A master-slave, bussed synchronous serial communication interface
- Often used for LCD displays and SD cards
- All kinds of implementations, but all usually have at least four wires

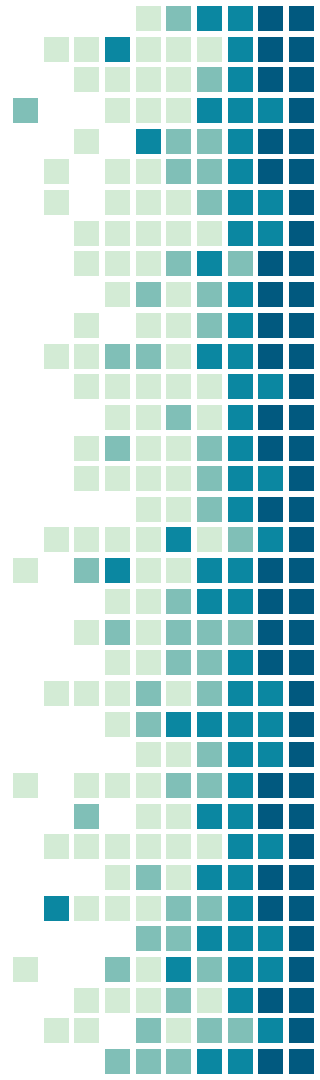


SPI: Serial Peripheral Interface



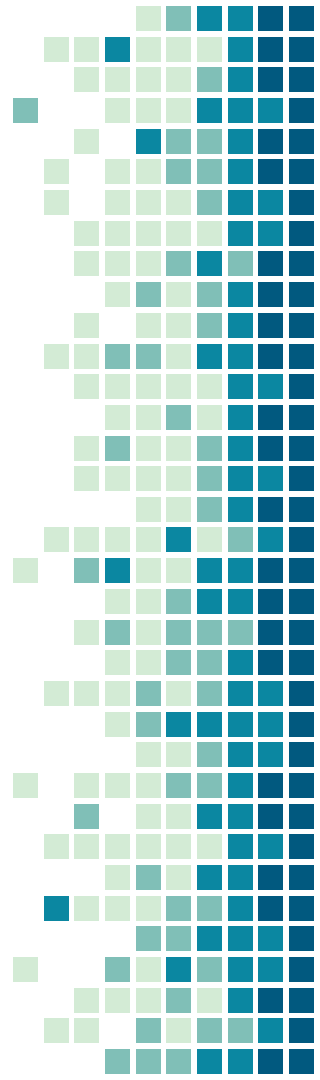
SPI: Serial Peripheral Interface

- Your computer firmware is usually stored on a bit of memory that communicates over SPI
- For my laptop, the first 12KB are a descriptor, followed by ~5MB of the IME firmware, followed by ~3MB of rewritable memory, followed by ~96KB of bootblock
- You generally can't change this stuff while the computer is running, but if you are willing to poke things with wires...



External programmer

- If you poke things with wires, and pray, all your dreams can come true
- Lots of things can talk SPI – if you're patient, you can probably even use a computer with a parallel port to do it
- But I'm going to use a Pi Zero, since it has SPI already

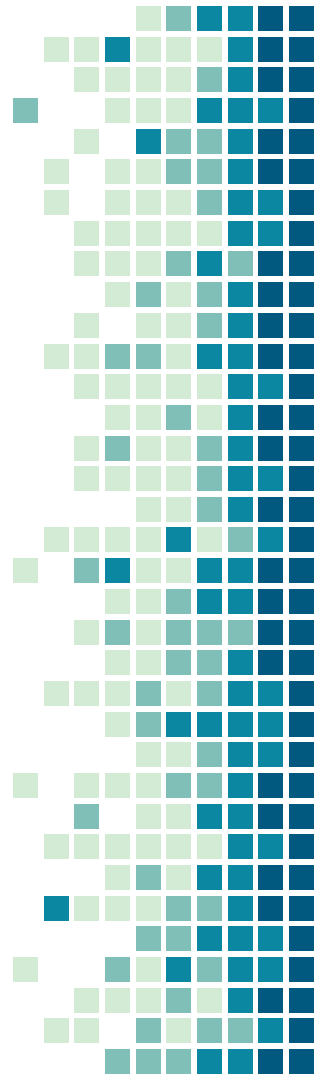


“

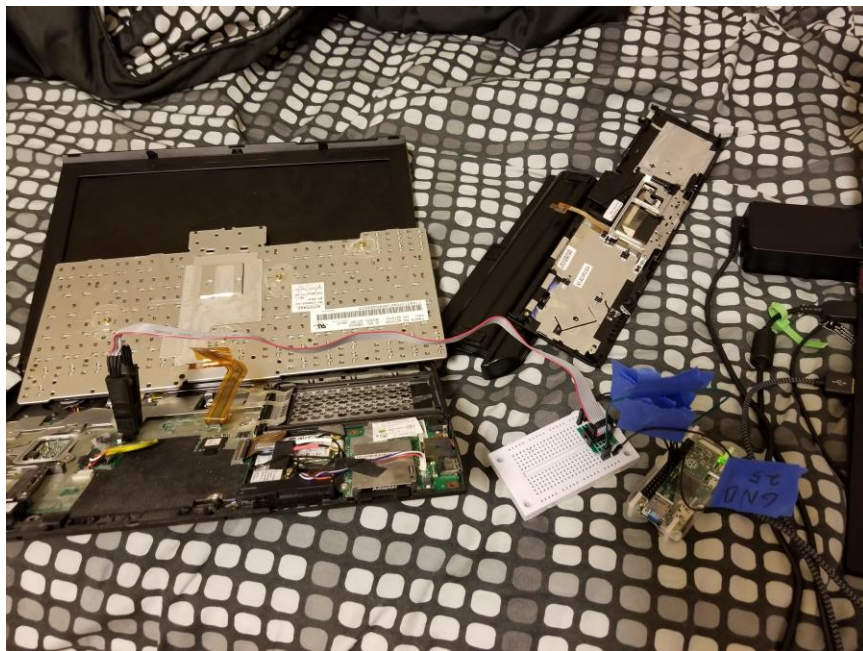
why would Kim Jong-un insult me by
calling me "old," when I would NEVER call
him "short and fat?" Oh well, I try so
hard to be his friend - and maybe
someday that will happen!

Read twice, write once

- It is not recommended to corrupt your firmware, and errors can happen during reading and writing, so first read the firmware TWICE
- Then diff them
- This is at least some reassurance that your read was correct
- I will use [flashrom](#) to read and write the firmware, following these pinouts: [pi laptop](#)

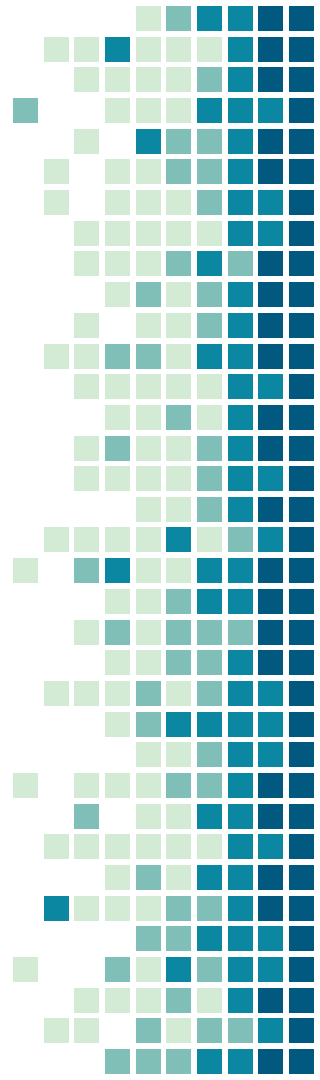


Setup



Reading at a fourth grade level

- I used the trick I did for my last lug talk, where I ssh into a pi0 over USB
- I started reading, and the pi0 switched interface names for no reason
- I hate Ubuntu
- Eventually I read it twice and the two reads matched...



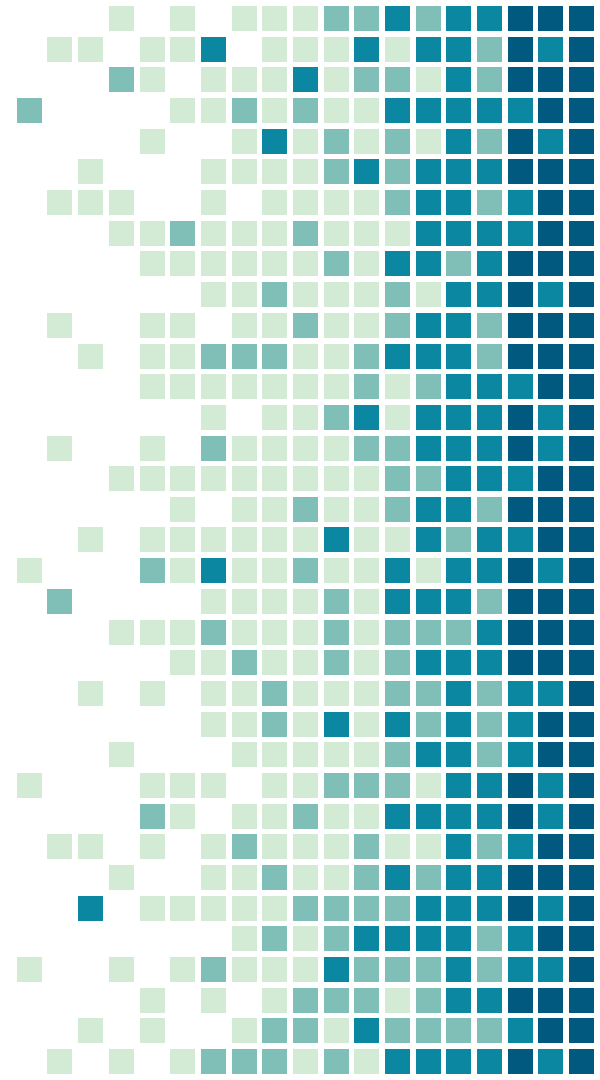
Kill it with fire

- Next up was using [me_cleaner](#) to edit the firmware dump
- This was straightforward, it was just a python script
- Finally, I again used flashrom to write back the modified file – you don't have to write twice, and it will automatically verify the write was successful by reading and checking



3. How'd it go

Did you break it like you promised?



Black screen forever



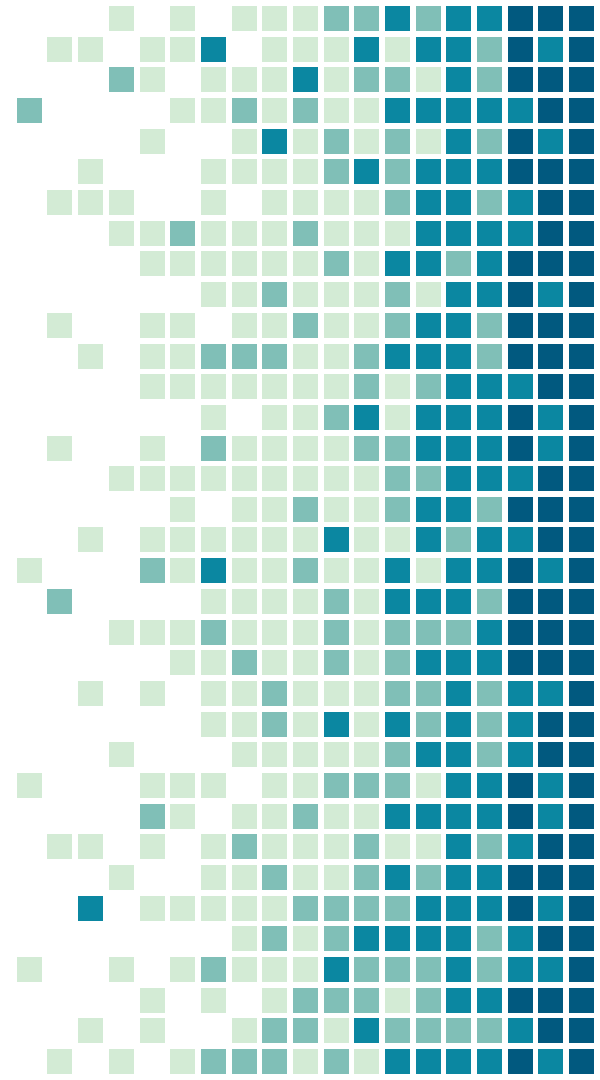
Questions?

Oh wait it started doing the thing



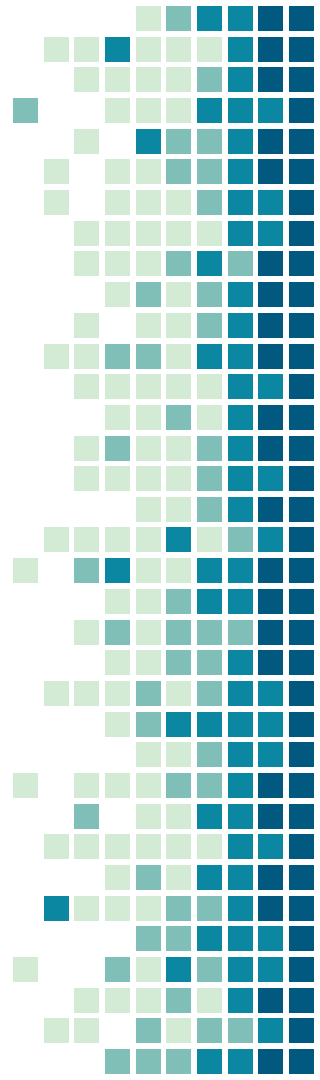
4. Recent developments

I feel like someone mentioned this thing
in the lug chat before...



MINIX

- Made by Prof. [Abstract Syntax Tree](#)
- Microkernel
- Influenced early Linux
- Post 2015, all intel chips run MINIX in the IME



```
Administrator: Intel DAI Python CLI
```

```
>>> itp.devicelist:
DID BP TP SC Alias                               Type                               Step Idcode BusType P/D/C J/T Enabled
0 0 0 1 SKI_THUNK0                               SKI_THUNK                               0xb4760013 3TAG 0/0/-/- Yes
1 0 0 0 SPT0                                       SPT0                                    C1 0xbA506013 3TAG 0/1/-/- Yes
2 0 0 1 SPT_MASTER0                               SPT_MASTER                              AB 0xb2000001 3TAG 0/1/-/- Yes
3 0 2 2 SPT_MASTER0                               SPT_TPS0                                AB 0xb0002003 3TAG 0/1/-/- Yes
4 0 0 0 SPT_XP00                                    SPT_XP00                                AB 0xb0002007 3TAG 0/1/-/- Yes
5 0 4 0 SPT_RIGHT0P0                               SPT_RIGHT0P                             AB 0xb2000003 3TAG 0/1/-/- Yes
6 0 5 0 SPT_PARCSHEA0                               SPT_PARCSHEA                             AB 3TAG 0/1/-/- Yes
7 0 0 0 PB                                         LMT2                                     AB 0xb2800013 3TAG 0/1/0/0 Yes
8 0 7 0 SPT_PARCSHEA_RETINE0                       SPT_PARCSHEA_RETINE                     AB 3TAG 0/1/-/- Yes
9 0 8 0 SPT_RGL000                                    SPT_RGL000                              AB 0xb2000005 3TAG 0/1/-/- Yes
10 0 9 0 SPT_PARISH0                                SPT_PARISH                               AB 0xb2000021 3TAG 0/1/-/- Yes
11 0 10 0 SPT_PARISH_RETINE0                       SPT_PARISH_RETINE                       AB 0xb0000000 3TAG 0/1/-/- Yes
12 0 11 0 SPT_AGG0                                SPT_AGG                                  AB 0xb0000000 3TAG 0/1/-/- Yes

>>> itp.threads[0].halt():
[LMT2_C0_T0] Multithreaded break at 0x8:00000000000065AAA in task 0xb0028
>>> itp.threads[0].step():
[LMT2_C0_T0] Multithreaded break at 0x8:000000000000620EF in task 0xb0028
>>> itp.threads[0].asm("%", 5)
0x8:00000000000020F0 00
0x8:00000000000020F1 00
0x8:00000000000020F2 00
0x8:00000000000020F3 00
0x8:00000000000020F4 7541
0x8:00000000000020F6 803D35C090000
cmp edi, 0x20
jnz $+0x43 ;a-82117
cmp byte ptr [0xb00095c1c], 0x00

>>> itp.threads[0].mendum("0xf00000040", 2, 4)
0xb0000000f00000040: 90000255 00000000

>>> itp.threads[0].mendum("0xf00000100", 4, 4)
0xb0000000f00000100: 8210E166 00000400 800E4000 00000000

>>>
```



Maxim Goryachy
@h0t_max



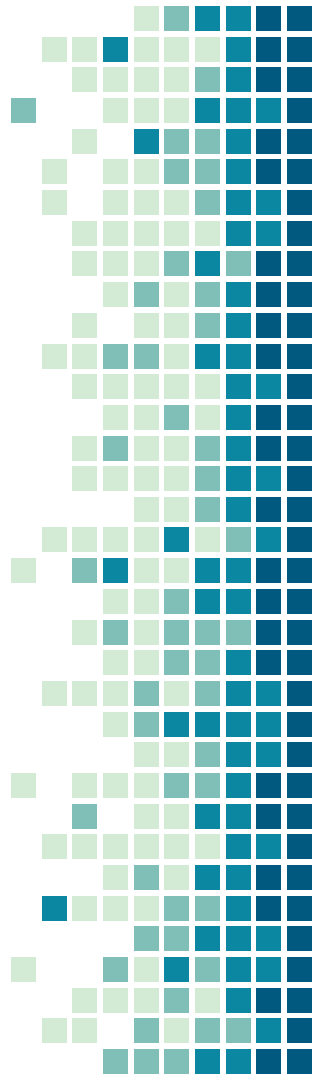
Game over! We (I and @_markel_) have obtained fully functional JTAG for Intel CSME via USB DCI. #intelme #jtag #inteldci

9:33 AM - Nov 8, 2017

109 1,974 2,482

JTAG

- Meant for testing boards – grants a serial debug port
- Also commonly used for firmware programming on chips
- Commonly grants power to do things like execute instructions and read/write registers and RAM
- So basically, “ring -2”





Questions?