

ZPUino Soft Processor - Papilio Pro - Vanilla - v2.0

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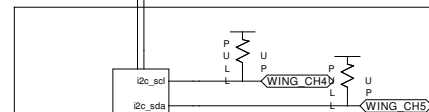
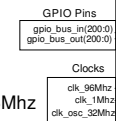
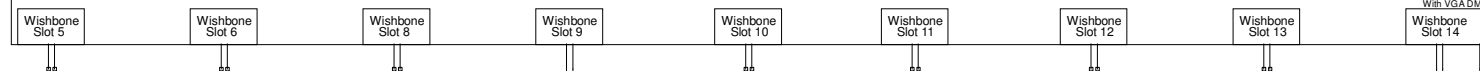
Used Resources:
11 of 32 Block RAM (BRAM)
34% of available Slices
1 of 2 PLL (clock)
0 of 2 DCM (clock)

8MB of Program Code Space
(code runs out of SDRAM)

Free Resources
21 Block RAM (BRAM)
66% of Slices
1 PLL (clock)
2 DCM (clock)

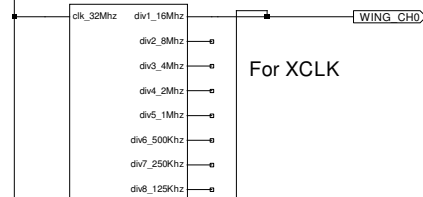
Visit Papilio.cc for Documentation

System Clock runs at 96Mhz

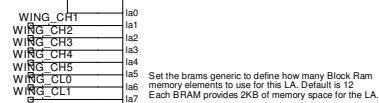


COMM_zpuino_wb_I2C

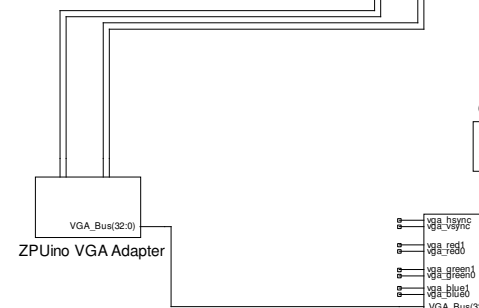
clk_divider_32Mhz



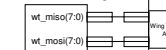
For XCLK



Sump Blaze Logic Analyzer - JTAG - x8



GPIO Wing



GPIO Wing



GPIO Wing



GPIO Wing



GPIO Wing



GPIO Wing



Papilio Default Pinout

