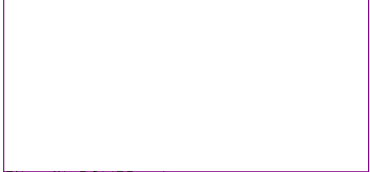


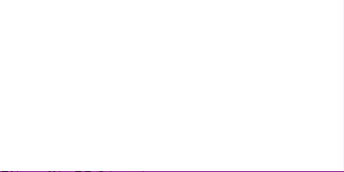
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A						
B						
C						
D						
	1	2	3	4	5	6

Sheet: sheetPower



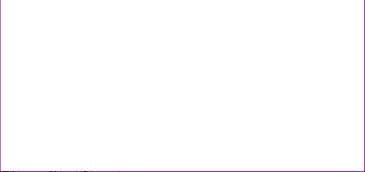
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File: fileFPGA.sch

Sheet: sheetIO



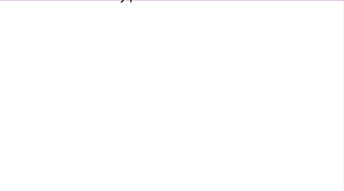
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File: fileSDMMC.sch

Sheet: sheetHyperRAM



File: fileHyperRAM.sch

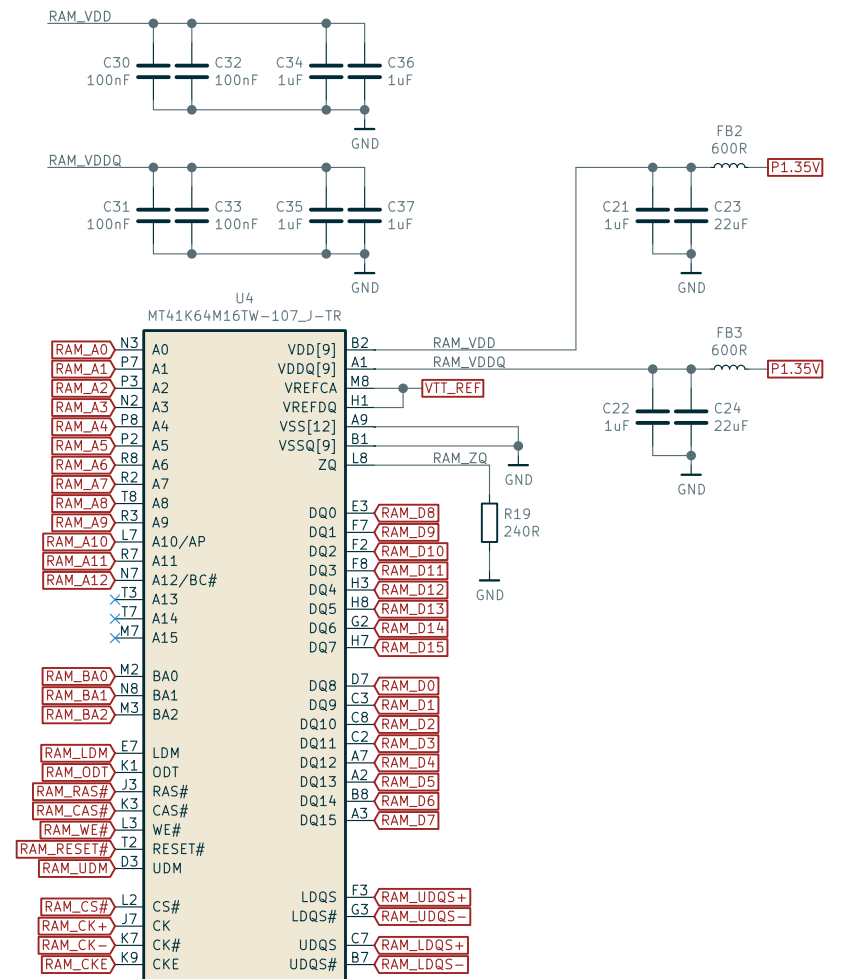
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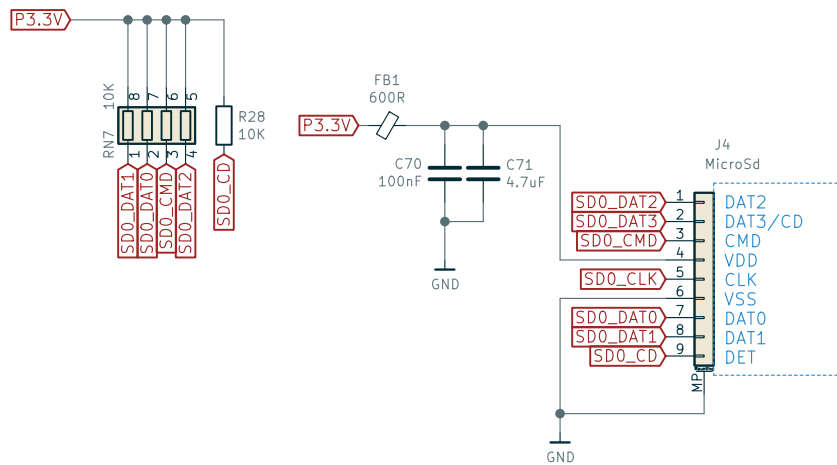
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GsD		
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Title: Orange Crab		
Size: A4	Date: 2019-06-28	Rev: r0.1
KiCad E.D.A. kicad 5.1.5-52549c584ubuntu18.04.1		Id: 1/7



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Id: 3/7



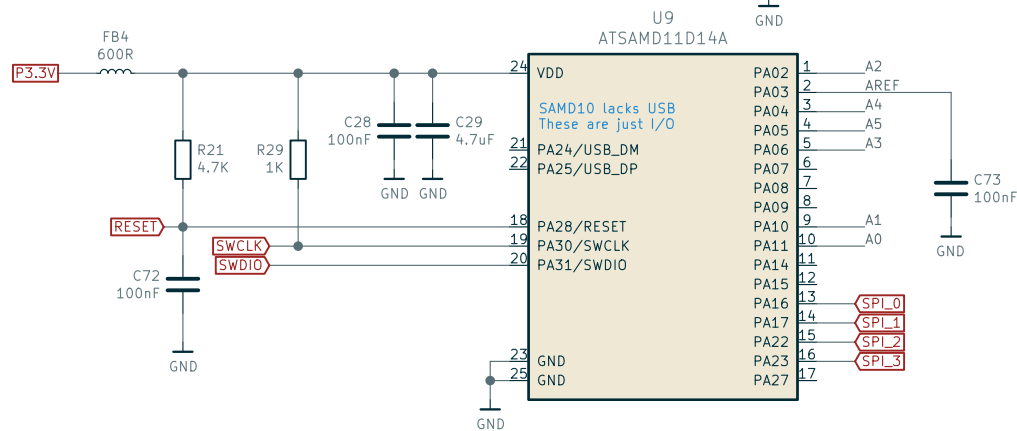
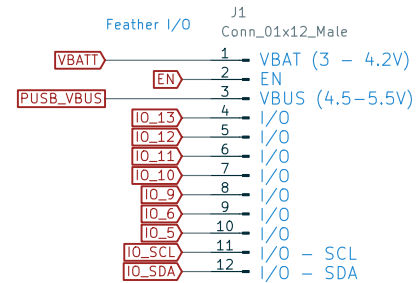
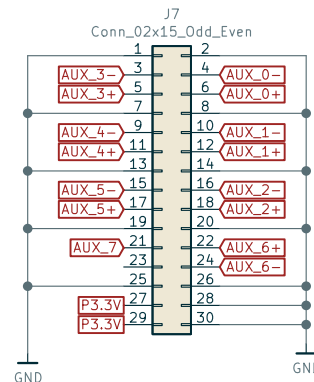
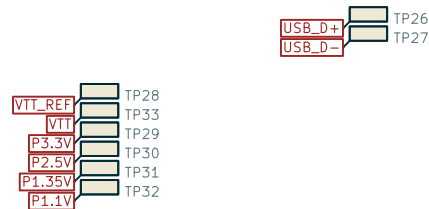
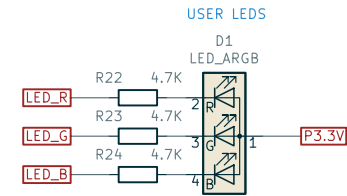
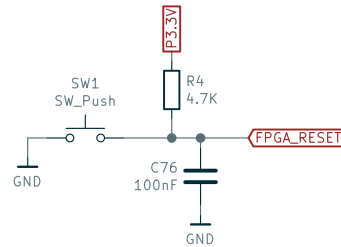
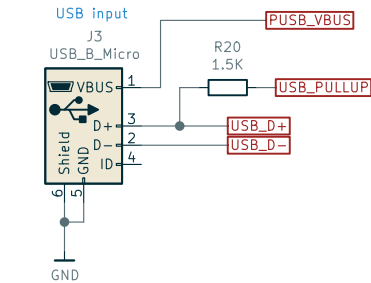
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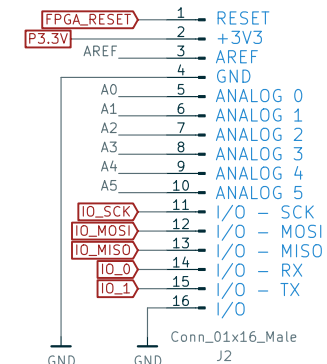
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KiCad E.D.A. kicad 5.1.5-52549c584ubuntu18.04.1

Rev: r0.1
Id: 4/7



Either SAMD11 or SAMD10 in QFN24 package
Used as a basic ADC/DAC + MUX.
Accessed by the FPGA over SPI



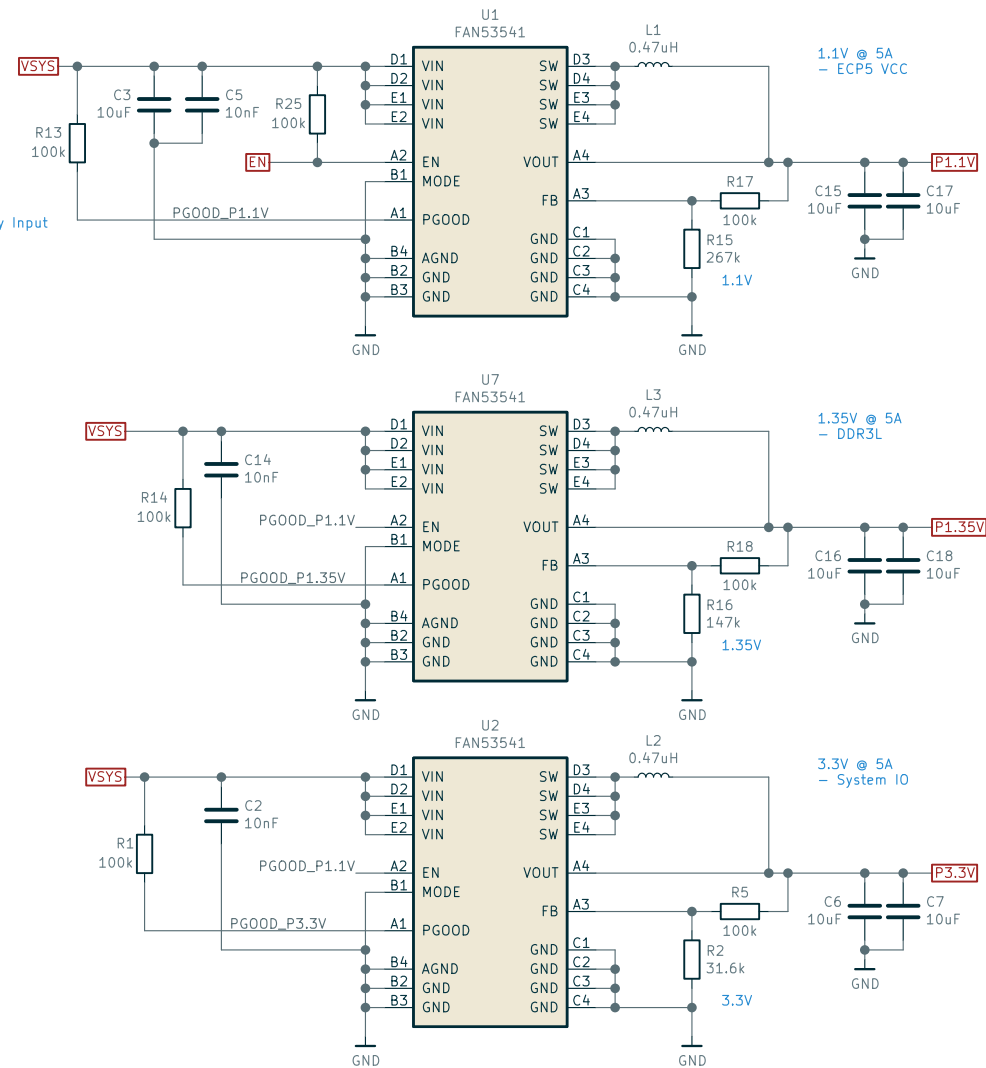
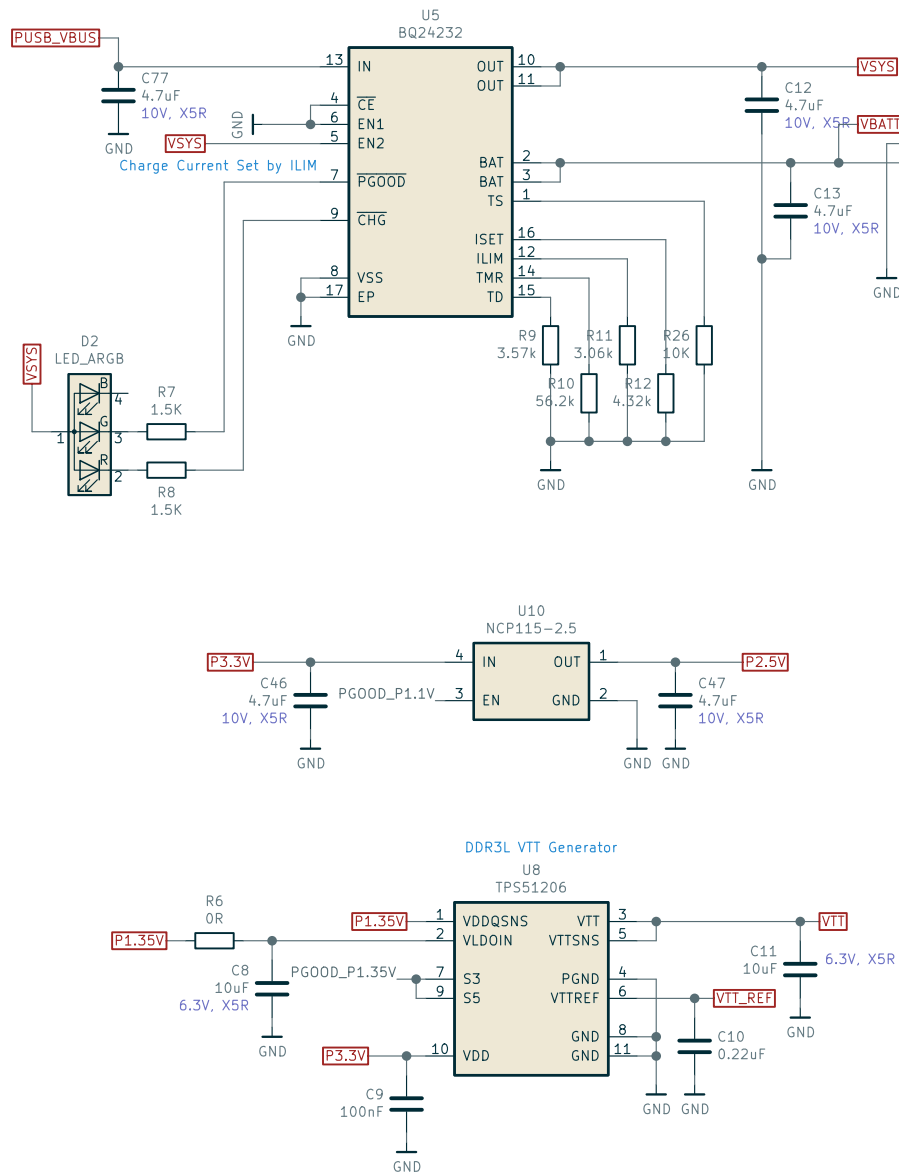
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Rev: r0.1
Id: 5/7



GsD

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Date: 2019-06-28

Rev: r0.1

Id: 6/7

