

FPGA I/O

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Sheet: /FPGA I/O/

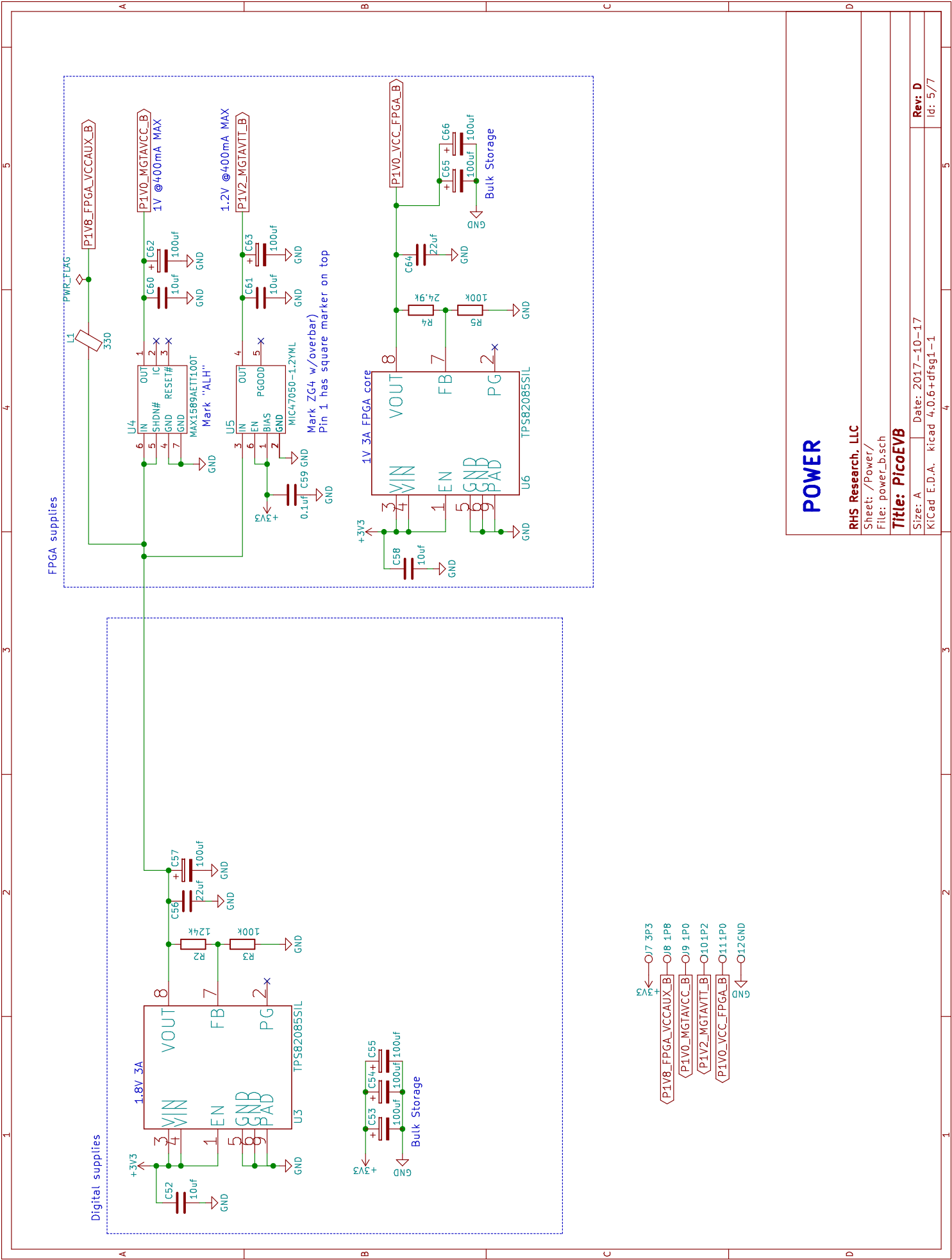
File: fpga_io_bis.ch

Title: PicoEVb

Size: A Date: 2017-10-17

Kicad E.D.A. kicad 4.0.6+dfsg1-1

Rev: D Id: 4/7



POWER

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Sheet: /Power/

File: power_b.sch

Title: PicoEVb

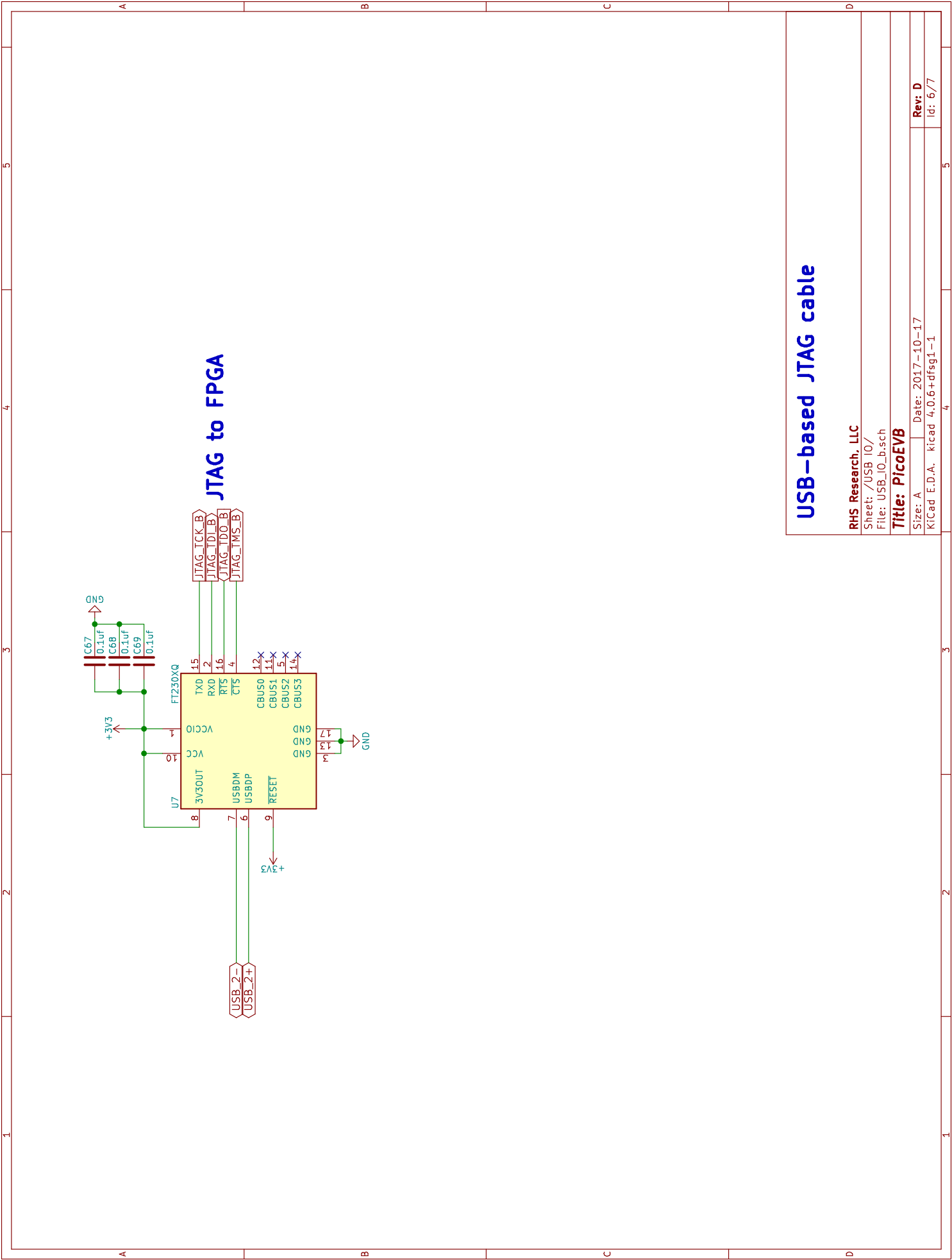
Size: A

Date: 2017-10-17

Kicad E.D.A. kicad 4.0.6+dfsg1-1

Rev: D

Id: 5/7



USB-based JTAG cable

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Sheet: /USB 10/

File: USB_IO_b.sch

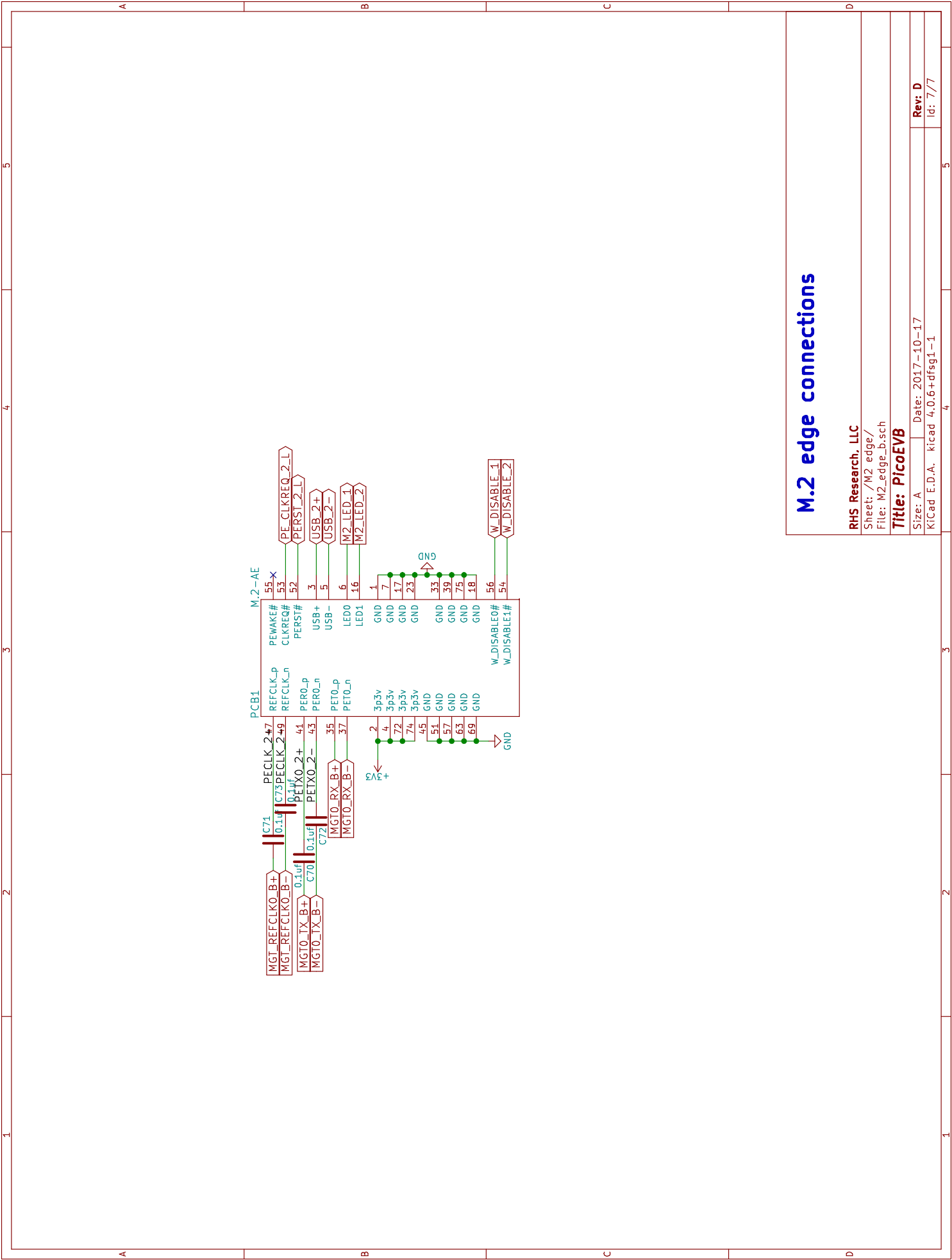
Title: PicoEVB

Size: A Date: 2017-10-17

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Rev: D

Id: 6/7



M.2 edge connections

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Sheet: /M2 edge/
File: M2_edge_b.sch

Title: PicoEVB

Size: A Date: 2017-10-17

Kicad E.D.A. kicad 4.0.6+dfsg1-1

Rev: D

Id: 7/7