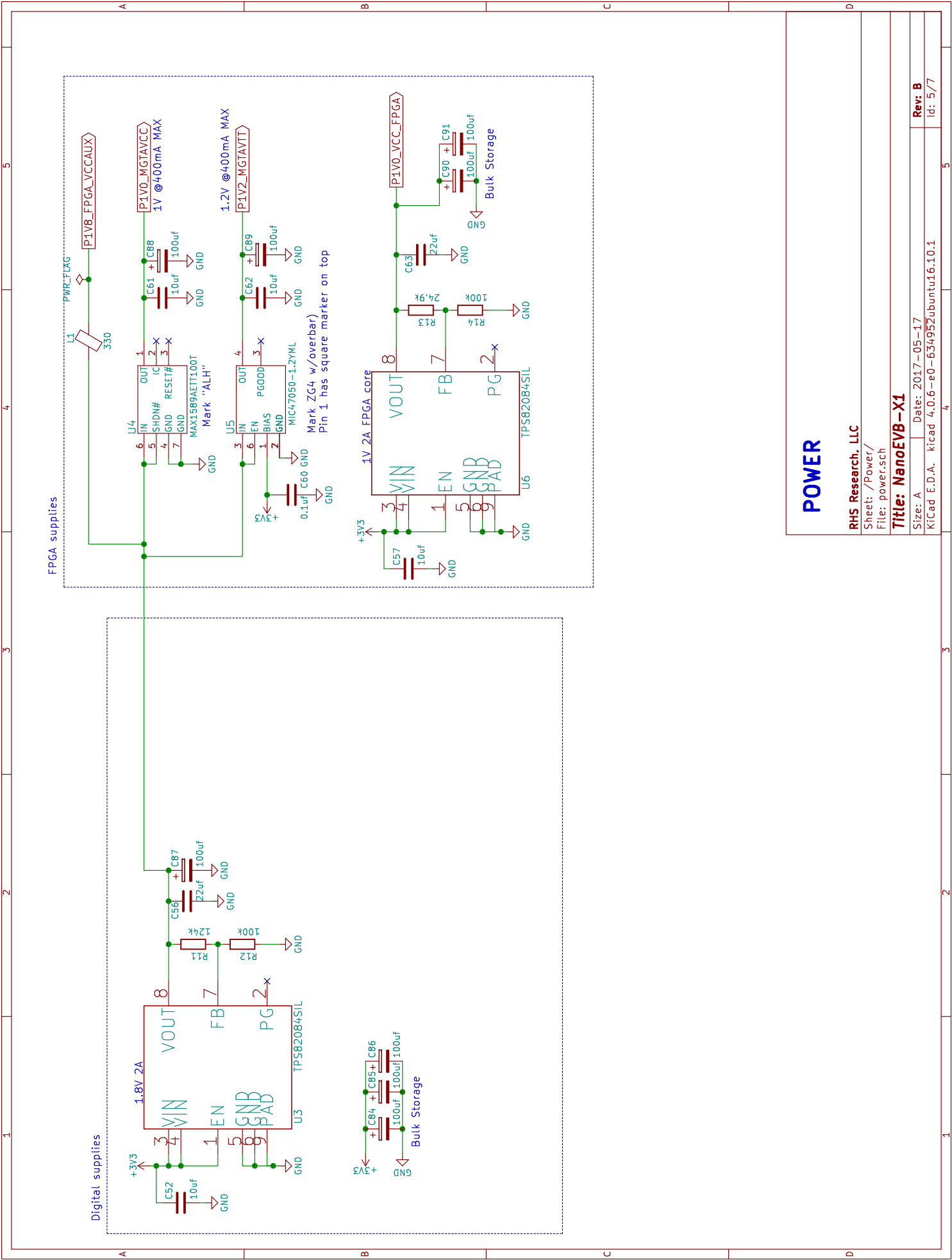


# POWER & CONFIG

RHS Research, LLC	
Sheet: /FPGA Power & Config/ File: fpga_pwr_cfg.sch	
Title: NanoEVb-X1	
Size: A	Date: 2017-05-17
Kicad E.D.A. kicad 4.0.6-e0-634952ubuntu16.10.1	
Rev: B	
Id: 2/7	







## POWER

RHS Research, LLC

Sheet: /Power/

File: power.sch

Title: NanoEVB-X1

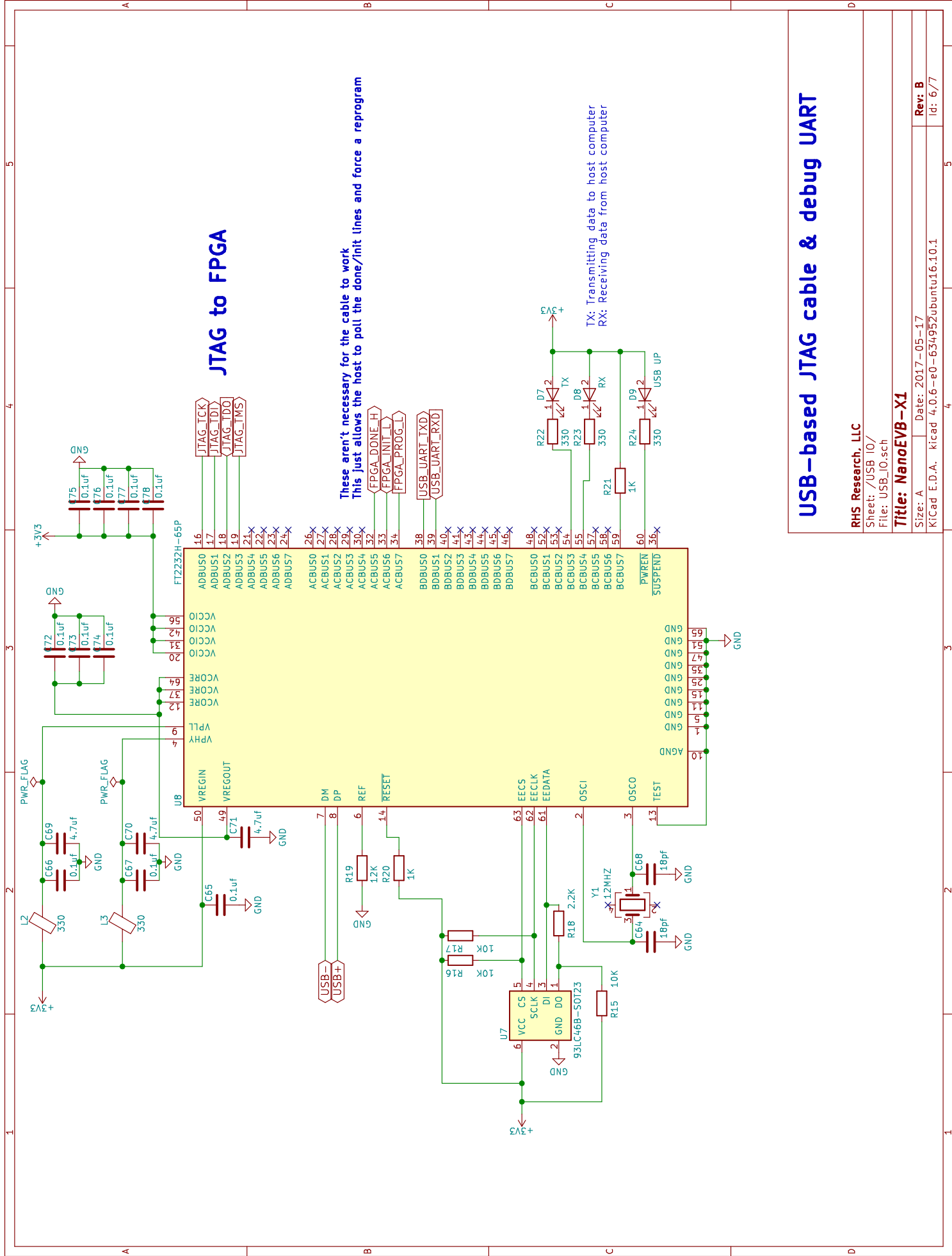
Size: A

Date: 2017-05-17

KiCad E.D.A. kicad 4.0.6-e0-634952ubuntu16.10.1

Rev: B

Id: 5/7



## JTAG to FPGA

These aren't necessary for the cable to work  
This just allows the host to poll the done/init lines and force a reprogram

## USB-based JTAG cable & debug UART

RHS Research, LLC

Sheet: 7/USB IO/  
File: USB\_IO.sch

Title: NanoEVB-X1

Size: A Date: 2017-05-17

Kicad E.D.A. kicad 4.0.6-e0-634952ubuntu16.10.1

Rev: B

Id: 6/7

