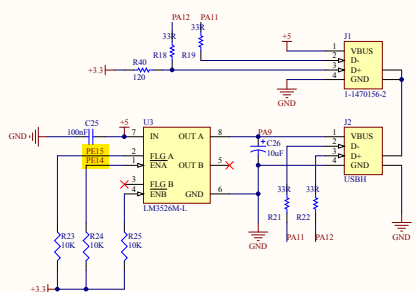


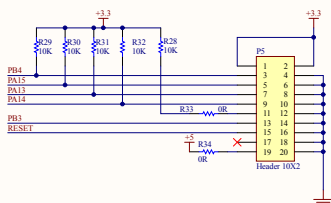
MAIN POWER



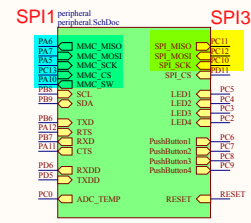
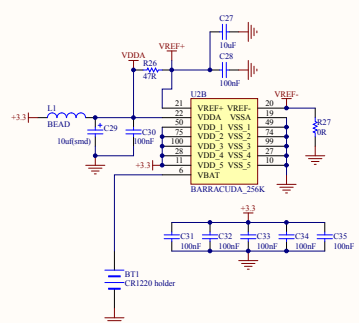
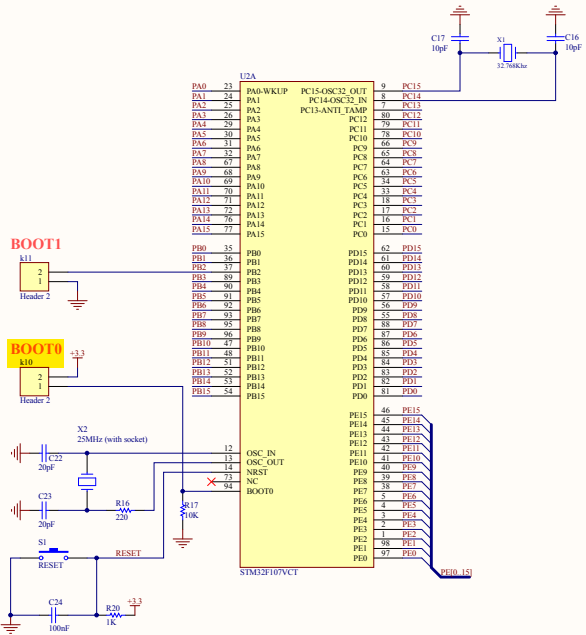
## USB HOST

## USB DEVISE

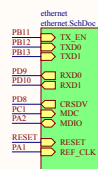
## JTAG



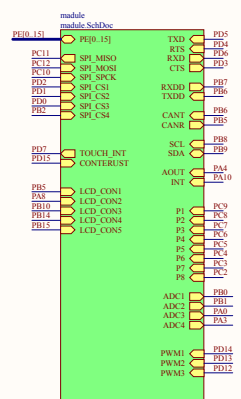
**This is programmer connections.**



## PERIPHERAL



## ETHERNET



## MADULE

Title			
Size A2	Number		Revision
Date:	07/14/2016	Sheet of	
File:	D:\Projects\mainBoard SchDoc	Drawn By:	

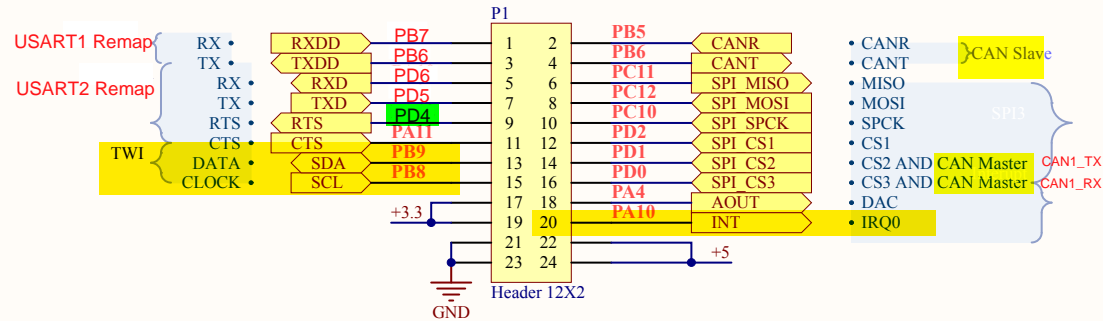
1

2

3

4

A

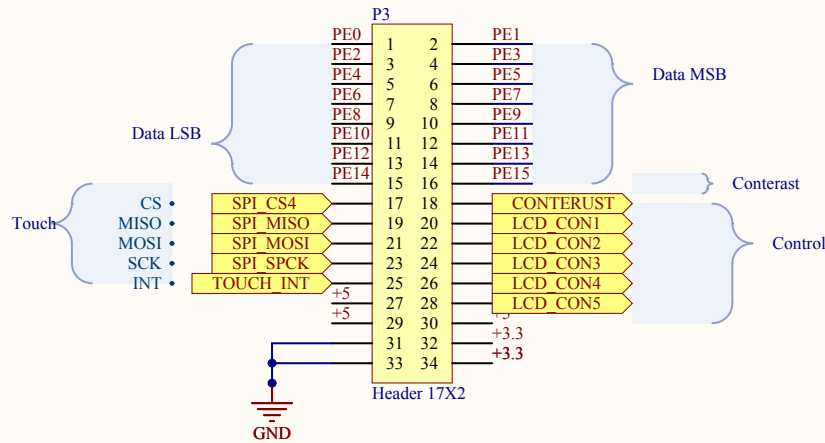


## Protocols

USART1(Remap)  
 USART2(Remap)  
 SPI3 (Remap)  
 I2C (Remap)  
 CAN Slave(Remap)  
 CAN Master(Remap)

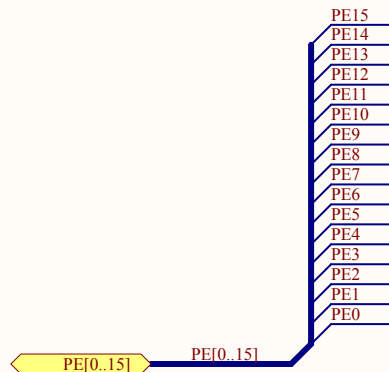
A

## LCD



C

D



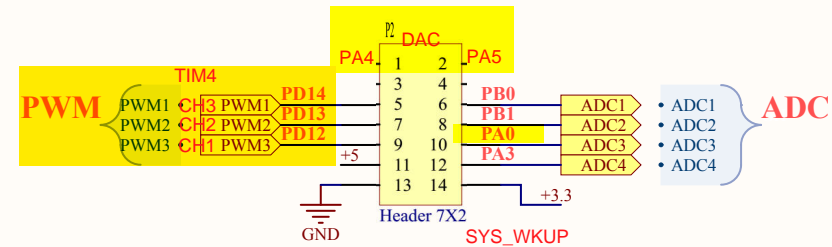
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2

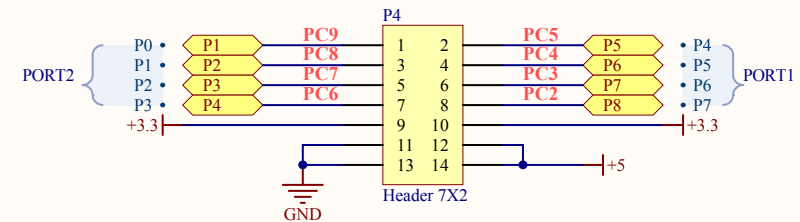
3

4

D



## ANALOG INPUT OUTPUT

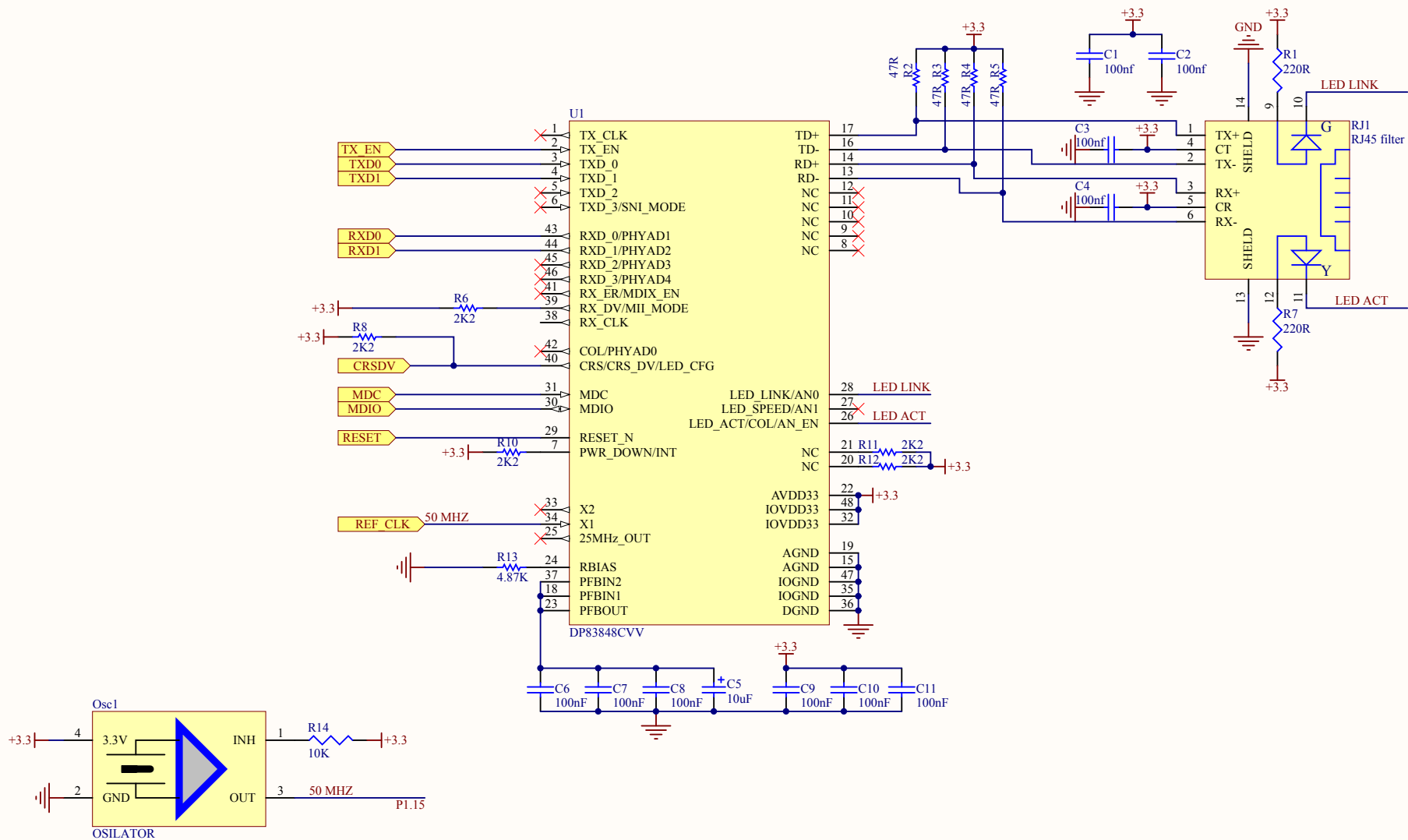


This Pins are conncted to LED and KEYS

## 8 PIN IO

Title		
Size	Number	Revision
A		
Date:	07/14/2016	Sheet of
File:	D:\Projects\...\module.SchDoc	Drawn By:





Title		
Size	Number	Revision
A4		
Date:	07/14/2016	Sheet of
File:	D:\Projects\...\ethernet.SchDoc	Drawn By: