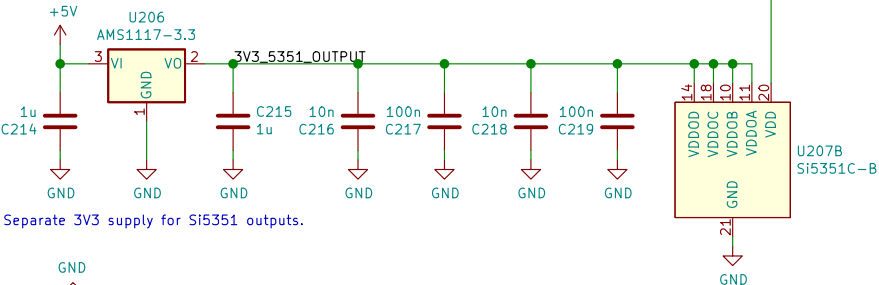


Separate 3V3 supply for Si5351 core, should reduce PLL phase noise.



Separate 3V3 supply for Si5351 outputs.

